

# JIADONG ZHU

M.Phil.  $\diamond$  Microelectronics Thrust

The Hong Kong University of Science and Technology (Guangzhou)

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## RESEARCH INTERESTS

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- AI for chip design
- Electronic design automation (EDA)
- FPGA architecture
- ML accelerator

## EDUCATION

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<b>HKUST(GZ), Guangzhou, China</b> M.Phil.    Microelectronic	Aug. 2022 – Jul. 2024
<b>Shanghai University, Shanghai, China</b> B.Eng.    Electrical Engineering and Automation	Sep. 2018 – Jul. 2022

## PUBLICATIONS

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$\dagger$  denotes equal contribution.

### Journal Papers

- [J1] Dongsheng Zuo  $\dagger$ , **Jiadong Zhu**  $\dagger$ , Yikang Ouyang, and Yuzhe Ma, “RL-MUL: Multiplier Design Optimization with Deep Reinforcement Learning”, under review in ACM Transactions on Design Automation of Electronic Systems (**TODAES**), arxiv preprint: 2404.00639.

### Conference Papers

- [C2] **Jiadong ZHU**  $\dagger$ , Dongsheng Zuo  $\dagger$ , and Yuzhe Ma, “A Holistic FPGA Architecture Exploration Framework for Deep Learning Acceleration”, IEEE/ACM Asian and South Pacific Design Automation Conference (**ASPDAC**), Tokyo, Jan. 20-23, 2025.
- [C1] Dongsheng Zuo, **Jiadong ZHU**, Chenglin Li, and Yuzhe Ma, “UFO-MAC: A Unified Framework for Optimization of High-Performance Multipliers and Multiply-Accumulators”, IEEE/ACM International Conference on Computer-Aided Design (**ICCAD**), New Jersey, Oct. 27–31, 2024.

## EXPERIENCE

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<b>Research Assistant</b> HKUST(GZ) Guangzhou, China	Sep. 2024 – Jan. 2025
<b>Engineering Intern</b> Festo (China) Ltd. Shanghai, China	Dec. 2021 – May. 2022
<b>Engineering Intern</b> SAIC Motor Passenger Vehicle Co., Ltd. Shanghai, China	Aug. 2021 – Oct. 2021
<b>Engineering Intern</b> Shanghai Automation Instrumentation Co., Ltd. Shanghai, China	Jul. 2021 – Aug. 2021
<b>Engineering Intern</b> Shanghai Automation Instrumentation Co., Ltd. Shanghai, China	Jul. 2020 – Sep. 2020

## AWARDS AND HONORS

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- [A5] **Full Postgraduate Studentship**, HKUST(GZ), 2022 – 2024.
- [A4] **First-Class Academic Scholarship**, SHU, 2021.
- [A3] **Flama Outstanding Scholarship**, SHU, 2020.
- [A2] **Self-Discipline Scholarship**, SHU, 2020.
- [A1] **China International College Students’ “Internet+” Innovation and Entrepreneurship Competition Second Prize**, SHU, 2020.

## **GRADUATE-LEVEL COURSES**

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IOTA5501: Convex and Nonconvex Optimization

MICS6000L: Computer Architecture

MICS6000Q: VLSI Design Optimization and Closure

MICS6000U: ML Accelerators