

# **Vehicle Intersection Control**

## MCMASTER UNIVERSITY

Development Process and Implementation SE 4G06

GROUP 6

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## 1 Revisions

Date	Revision Number	Authors	Comments
October 22, 2016	Revision 0	Alex Jackson Jean Lucas Ferreira Justin Kapinski Matthew Hobers Radhika Sharma Zachary Bazen	N/A

Table 1: VIC Table of Revisions

## **2 Overall Process Workflow**

## 2.1 Project Steps and Order

Step	Task	Deadline
1	Develop high level project requirements and potential parts list	Week of October 25
2	Acquire two, one tenth $(\frac{1}{10})$ scale RC cars  - micro controllers, sensor(s), camera(s) and mounting hardware	Week of November 1
3	Develop a track design	Week of November 1
4	Integrate car hardware, micro-controllers and computer	Week of November 15
5	Look for open source lane following and obstacle detection	Week of November 15
6	Integrate lane following to car	Week of November 28
7	Develop intersection and detection algorithms	End of December
8	Algorithm simulations	End of January
9	Integrate decision algorithms and intersection detection with vehicles	February
10	Algorithm Refinement	March
11	Final Implementation and Documentation	Beginning of April

Table 2: VIC Project Steps

Note: Appropriate documentation will be developed as appropriate with each step.

## 2.2 Step Inputs and Outputs

Step	Input	Output
1	Project Goals	High-level Design Document
2	Research Hardware Options	Purchase Hardware
3	Track expectations	Track design
4	Hardware and Micro-controllers	Integrated hardware/software system
5	Research open source software	Select software
6	Implementing sensors and software	Car that follows lanes
7	Implementing sensors and software	Car detects intersection

8	Algorithms simulation	System simulation
9	Software and hardware systems	Integrated system
10	Algorithm and testing plan	Refined algorithm
11	Final adjustments and documentation	Finished project

Table 3: VIC Project Steps Input and Output

### 2.3 Step Output Acceptance Criterion

Step	Output Acceptance Criterion		
1	Requirements document complete based on the IEEE or Volere templates		
2	Hardware received in manageable time window		
3	Developed track		
4	Cars are able to be controlled from central control and micro controllers		
5	Open source software is integrable to our system		
6	Cars stay in lane on the track		
7	Cars stop at intersection		
8	Simulation shows cars have correct behaviour		
9	Cars operate according to algorithm rules		
10	Improved algorithm		
11	Cars successfully navigate the track autonomously, including lane following, intersection detection, and intersection navigation		

Table 4: VIC Project Steps Output Acceptance Criterion

## **3 Step Completion Information**

## 3.1 Tools and Versions

**TBD** 

## 3.2 Tool Setting and Use

**TBD** 

#### 3.3 Standards

Volere and IEEE for software requirements specification Coding standards and conventions, for the programming language at hand.

#### 3.4 Work Assignments

Ideally we should create two subgroups (HW and SW), but we would still discuss both aspects as a whole group.

When it comes to implementation subgroups might be more efficient.

Step	Task	Assignment
1	Develop high level project requirements and potential parts list	VIC
2	Acquire two, one tenth $(\frac{1}{10})$ scale RC cars - micro controllers, sensor(s), camera(s) and mounting hardware	All Members
3	Develop a track plan	VIC
4	Integrate car hardware, micro-controllers and computer	HD
5	Look for open source lane following and obstacle detection	HD
6	Lane following and obstacle detection integrated and tested	SE
7	Intersection and Detection Algorithms Developed Concurrently	VIC
8	Algorithm simulations	SE
9	Integrate decision algorithms and intersection detection with vehicles	VIC
10	Algorithm Testing and Refinement	SE
11	Final Implementation and Documentation	VIC

Table 5: VIC Project Assignments

#### **Table Key**

SE: Software Team (Mathew Hobers, Alexander Jackson, Jean Ferreira) HD: Hardware Team (Justin Kapimski, Radhika Sharma, Zachery Bazen)

VIC: Whole VIC Team

Note: These assignments are tentative and subject to change as project advances.

#### **4 Version Control Information**

The version control of choice for this project is GitHub. Two repositories will likely be required: one for documentation and miscellaneous information and another for source code, libraries and dependencies.

## **5 Project Evolution**

#### 5.1 Bug and Change Tracking

Any issues with the project (i.e. bugs) will be posted on GitHub via the Issues panel. When a issue is posted, the appropriate members will take responsibility to fixing the bug. Once fixed, the issue will be closed. Appropriate members will include: developers, software team, hardware team or both.

### **5.2 Project Change Documentation**

VIC will log project changes through GitHub version control logs, personal log books and VIC documentation.

#### **5.3 Project Change Classification**

VIC will classify changes in the following ways: Global change, Software change and Hardware change.

A global change constitutes a change that affects both hardware and software aspects of the system. This type of change would fundamentally alter the functionality of the system.

A software change only affects the software aspects of the system.

A hardware change only affects the hardware aspects of the system.

#### **5.4 Making Project Change Decisions**

Change Type	<b>Change Severity</b>	<b>Decision Assignment</b>	<b>Change Documentation</b>
	Software	VIC Team	Logged in VIC documents
Global	Hardware	VIC Team	Logged in VIC documents
	Other	VIC Team	Logged in VIC documents
Software	Local	Developer	GitHub Commit Log
Software	API Change	Software Team	Logged in VIC documents
Hardware	Local	Developer	Log Books
Tiaidwaic	Interface Change	Hardware Team	Logged in VIC documents