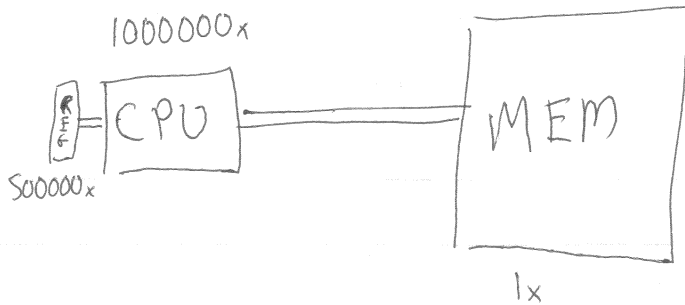


4-1/



$$M(P) = M(P')$$

$$V(P) < V(P')$$

$$OPT(P \Rightarrow P')$$

$M = \text{meaning} \neq \text{behavior}$

$V = \text{value}$

~~FLAT~~

Change mem-ref into reg-ref

x^*

$$v=1, w=46, x=v+7,$$

$$y=7+x, z=x+w, \text{ret } z+(-y);$$

-inf = 0	(program	(v w x y z t1 t2)
1	(movg	4 v) \emptyset
2	(movg	46 w) v
3	(movg	v x) w, v
4	(addg	7 x) w, x
5	(movg	x y) w, x
6	(addg	4 y) y, w, x
7	(movg	x z) y, w, x
8	(addg	w z) z, y, w
9	(movg	y t1) z, y
10	(negg	t1) t1, z
11	(movg	z t2) t1, z
12	(addg	t1 t2) t1, t2
13	(movg	t2 rax) t2
14		\emptyset

+inf =

1-2/

assign mapped all vars to mem
regalloc map some vars to regs
and some to mem

big
vars, mem

small
regs

- 1x naive: all mem
- 100x less naive: first |Registers| get reg, else mem
- 10000x better: some ~~variable~~ pairs of variables don't overlap in usefulness, so, they can share registers
- 30000x best: range-splitting (move a lightly used variable to mem when lightly used)

Liveness = when a variable is needed
: a range of program points

Interference = when two variables are needed at once
: an overlapping of ranges (i.e. being in the same liveness-set for one instruction)

Spilled = a variable not in a register

L_i = the liveness set of instruction i
 $L_{a,i}$ = liveness ~~before~~ after i
 $L_{b,i}$ = liveness ~~after~~ before i

writes-to
 $W: instr \rightarrow Vars$
reads-from
 $R: instr \rightarrow vars$

$$L_{b,0} = \emptyset \quad L_{a,0} = \emptyset \quad L_{a,n} = \emptyset$$
$$L_{a,k} = L_{b,k+1}$$
$$L_{b,k} = (L_{a,k} - W(k)) \cup R(k)$$

$$W(addg \ s \ d) = \{d\} \quad W(movg \ s \ d) = \{d\}$$
$$R(addg \ s \ d) = \{s, d\} \quad W(movg \ s \ d) = \{s\}$$

4-3/

Interferences = live at same time

u interferes with v iff

$$\exists i. \{u, v\} \subseteq L(i)$$

1. for $q = 0$ to N variables
2. for $v = u+1$ to N variables
3. for $i = 0$ to $\text{EOP}(IS)$
if $u \in L(i) \wedge v \in L(i)$
 $m[u][v] = 1$
break i

$$n \cdot \frac{n}{2} \cdot k \leq k \cdot \frac{k}{2} \cdot k = O(k^3)$$

Interference graph $I = (Vars, E)$ E_{uv} iff u interferes with v

for $i = 0$ to $\text{EOP}(IS)$

match instr i with

$(\text{movg } s \text{ } d) \Rightarrow \forall v \in L(i), \text{ add } (d, v) \text{ to } E$
 $\text{pushg } s$
 $\text{popg } d$ unless $v = d$ or $v = s$
 $(\text{addg } s \text{ } d) \Rightarrow \forall v \in L(i), \text{ add } (d, v) \text{ to } E$
 subg unless $v = d$

negg is like add but w/o s

