

CPE403 – Advanced Embedded Systems

Design Assignment 2

DO NOT REMOVE THIS PAGE DURING SUBMISSION:

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Github Repository link (root):

Youtube Playlist link (root):n/a

Follow the submission guideline to be awarded points for this Assignment.

Submit the following for all Assignments:

1. In the document, for each task submit the modified or included code (from the base code) with highlights and justifications of the modifications. Also include the comments. If no base code is provided, submit the base code for the first task only.
2. Create a private Github repository with a random name (no CPE/403, Lastname, Firstname). Place all labs under the root folder TIVAC, sub-folder named Assignment1, with one document and one video link file for each lab, place modified c files named as asng_taskxx.c.
3. If multiple c files or other libraries are used, create a folder asng1_t01 and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) with startup_ccs.c and other include files, c) text file with youtube video links (see template).
5. Submit the doc file in canvas before the due date. The root folder of the github assignment directory should have the documentation and the text file with youtube video links.
6. Organize your youtube videos as playlist under the name “cpe403”. The playlist should have the video sequence arranged as submission or due dates.
7. Only submit pdf documents. Do not forget to upload this document in the github repository and in the canvas submission portal.

1. Code for Tasks. for each task submit the modified or included code (from the base code) with highlights and justifications of the modifications. Also include the comments. If no base code is provided, submit the base code for the first task only. Use separate page for each task.

```
2. #include "inc/tm4c123gh6pm.h"
3. #include <stdbool.h>
4. #include <stdint.h>
5. #include "inc/hw_ints.h"
6. #include "inc/hw_memmap.h"
7. #include "inc/hw_adc.h"
8. #include "inc/hw_types.h"
9. #include "inc/hw_udma.h"
10. #include "driverlib/adc.h"
11. #include "driverlib/debug.h"
12. #include "driverlib/gpio.h"
13. #include "driverlib/interrupt.h"
14. #include "driverlib/pin_map.h"
15. #include "driverlib/rom.h"
16. #include "driverlib/rom_map.h"
17. #include "driverlib/sysctl.h"
18. #include "driverlib/systick.h"
19. #include "driverlib/timer.h"
20. #include "driverlib/uart.h"
21. #include "driverlib/udma.h"
22. #include "utils/uartstdio.h"
23. #include "driverlib/i2c.h"
24.
25.
26. void ConfigureUART(void);
27. void initI2C0(void);
28. void I2C0_Read16(uint8_t, uint8_t ,uint16_t*);
29. //void I2C0_send(uint8_t, uint8_t);
30.
31.
32.
33. int main(void)
34. {
35.     uint16_t *var;
36.     initI2C0();
37.     //I2C0_send(0x40,0xff);
38.     I2C0_Read16(0x40,0xff,var);
39.     ConfigureUART();
40.     UARTprintf("F: %3d\t\n",*var);
41.
42.
43.
44.
45. }
46. void initI2C0(void) // initialized my I2C 0
47. {
48.     SysCtlPeripheralEnable(SYSCTL_PERIPH_I2C0);
49.     SysCtlDelay(3);
50.
51.     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOB);
```

```

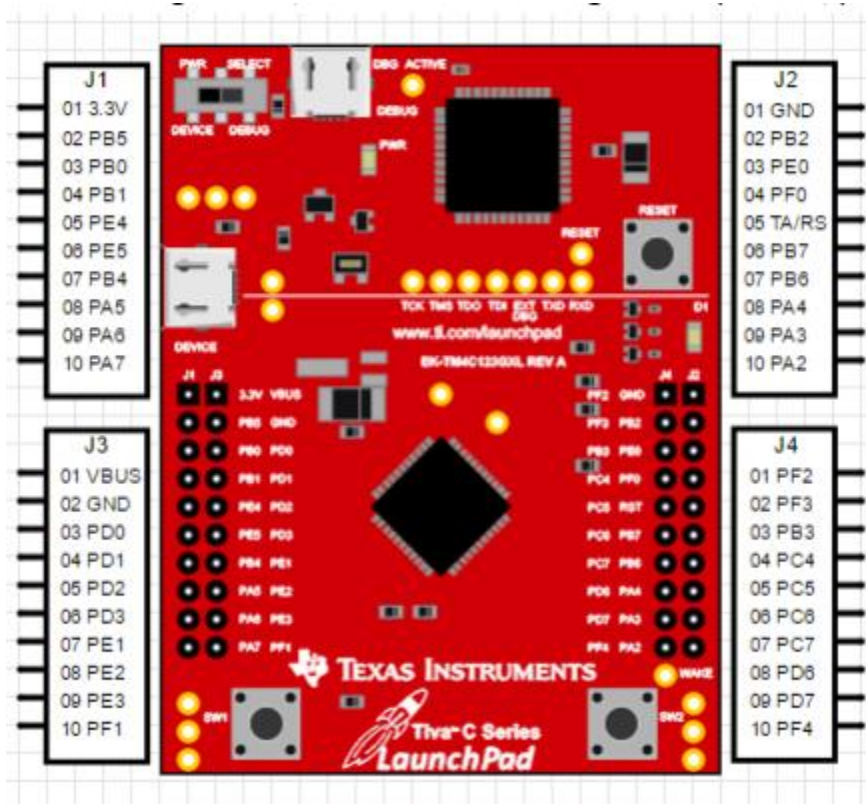
52.         SysCtlDelay(3);
53.
54.         GPIOPinConfigure(GPIO_PB2_I2C0SCL);
55.         GPIOPinConfigure(GPIO_PB3_I2C0SDA);
56.
57.         GPIOPinTypeI2CSCL(GPIO_PORTB_BASE, GPIO_PIN_2);
58.         GPIOPinTypeI2C(GPIO_PORTB_BASE, GPIO_PIN_3);
59.
60.         I2CMasterInitExpClk(I2C0_BASE, SysCtlClockGet(), true); // turn I2c
Automatically/ and configures it
61.     }
62.     /* void I2C0_send(uint8_t slave_addr,uint8_t num_of_args)
63.     {
64.         I2CMasterSlaveAddrSet(I2C0_BASE,slave_addr,false);
65.         va_list vargs;
66.         va_start(vargs,num_of_args);
67.         I2CMasterDataPut(I2C0_BASE,va_arg(vargs,uint32_t));
68.         I2CMasterControl(I2C0_BASE,I2C_MASTER_CMD_SINGLE_SEND);
69.         while(I2CMasterBusy(I2C0_BASE));
70.         va_end(vargs);
71.         I2CMasterDataPut(I2C0_BASE,va_arg(vargs,uint32_t));
72.         I2CMasterControl(I2C0_BASE, I2C_MASTER_CMD_BURST_SEND_FINISH);
73.         while(I2CMasterBusy(I2C0_BASE));
74.         va_end(vargs);
75.
76.     }*/
77.     void I2C0_Read16(uint8_t slave_addr, uint8_t pointer_reg, uint16_t* RxData
)
78.     {
79.         uint8_t data;
80.         I2CMasterSlaveAddrSet(I2C0_BASE, slave_addr, true);
81.         I2CMasterDataPut(I2C0_BASE, pointer_reg);
82.         I2CMasterControl(I2C0_BASE, I2C_MASTER_CMD_BURST_SEND_START);
83.         while(I2CMasterBusy(I2C0_BASE));
84.         I2CMasterSlaveAddrSet(I2C0_BASE, slave_addr, true);
85.         I2CMasterControl(I2C0_BASE, I2C_MASTER_CMD_BURST_RECEIVE_CONT);
86.         while(I2CMasterBusy(I2C0_BASE));
87.         //MSB first
88.         data = I2CMasterDataGet(I2C0_BASE);
89.         *RxData = (uint16_t)(data << 8);
90.         I2CMasterControl(I2C0_BASE, I2C_MASTER_CMD_BURST_RECEIVE_CONT);
91.         while(I2CMasterBusy(I2C0_BASE));
92.         //LSB later
93.         data = I2CMasterDataGet(I2C0_BASE);
94.         *RxData |= (uint16_t)(data);
95.         I2CMasterControl(I2C0_BASE, I2C_MASTER_CMD_BURST_SEND_FINISH);
96.         while(I2CMasterBusy(I2C0_BASE));
97.     }
98.
99.
100.    void ConfigureUART(void){
101.
102.        SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA);
103.
104.

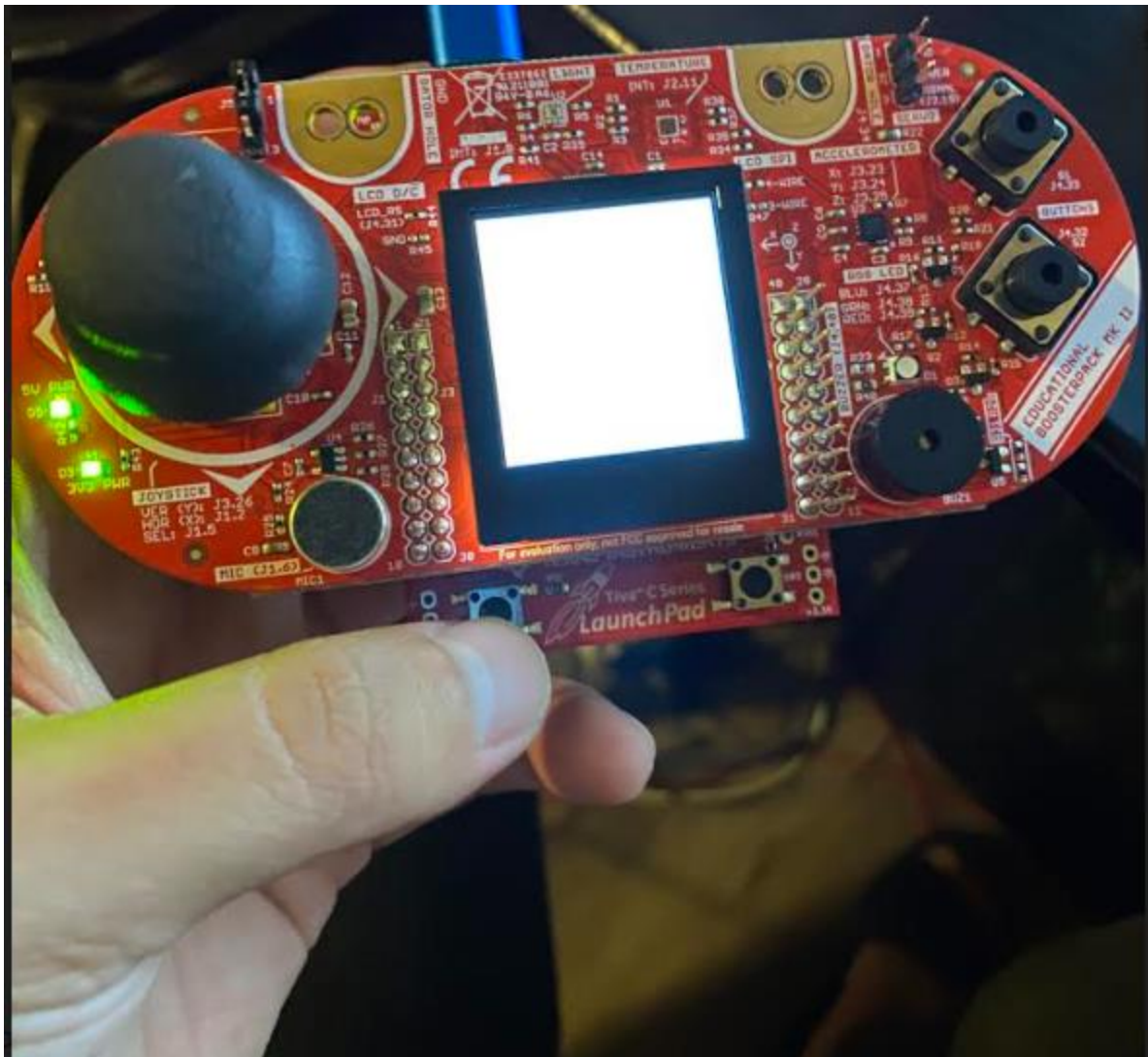
```

```
105.         SysCtlPeripheralEnable(SYSCTL_PERIPH_UART0);
106.
107.         GPIOPinConfigure(GPIO_PA0_U0RX);
108.         GPIOPinConfigure(GPIO_PA1_U0TX);
109.         GPIOPinTypeUART(GPIO_PORTA_BASE, GPIO_PIN_0 | GPIO_PIN_1);
110.
111.
112.         UARTStdioConfig(0, 115200, SysCtlClockGet());
113.     }
114.
115.
116.
117.
118.
119.
120.
```

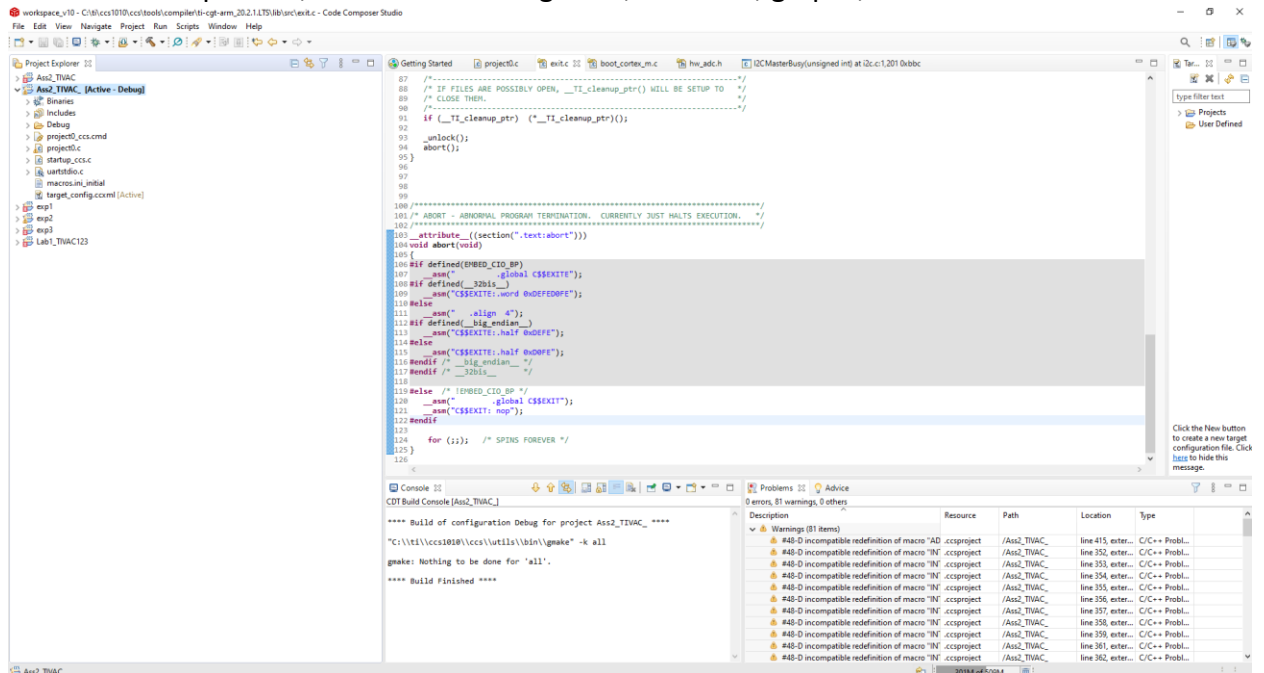
121.

Block diagram and/or Schematics showing the components, pins used, and interface.

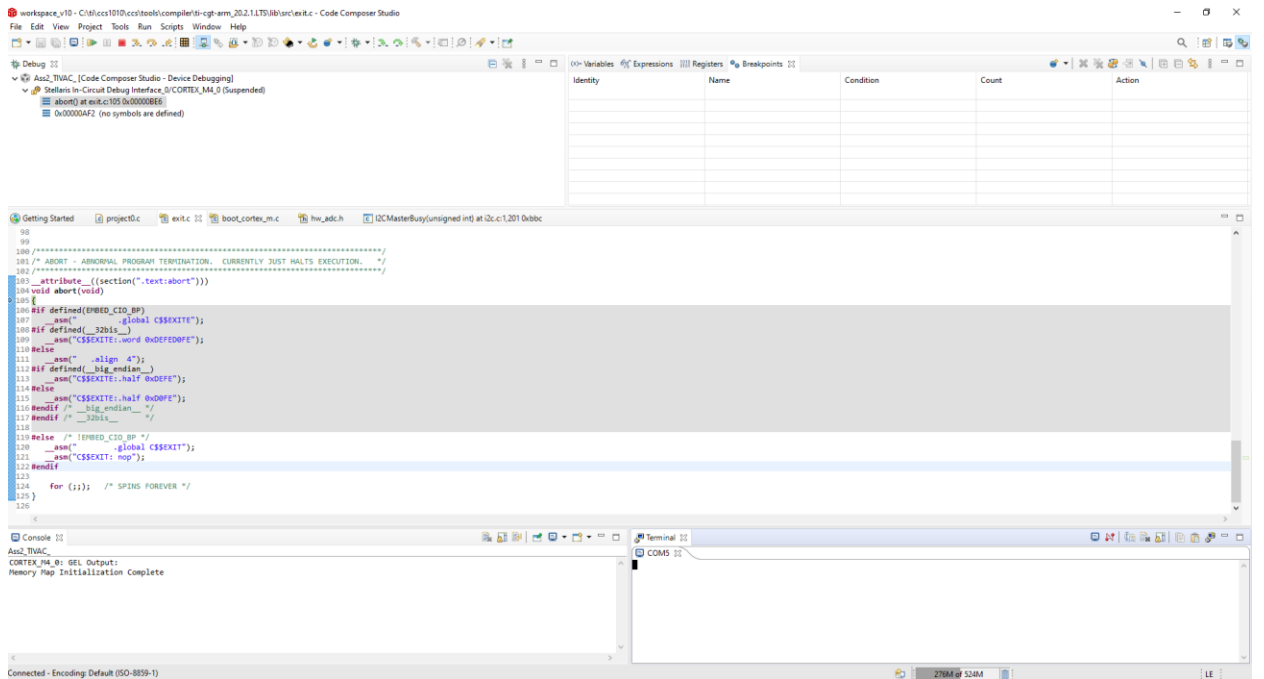




Screenshots of the IDE, physical setup, debugging process - Provide screenshot of successful compilation, screenshots of registers, variables, graphs, etc.



Error:



122. Declaration

I understand the Student Academic Misconduct Policy -
<http://studentconduct.unlv.edu/misconduct/policy.html>

"This assignment submission is my own, original work".

Jeb Marinas