CPE403 – Advanced Embedded Systems

# Design Assignment # 6

DO NOT REMOVE THIS PAGE DURING SUBMISSION:

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Github Repository link (root): https://github.com/jebmarinas/Micro\_projects

Youtube Playlist link (root): https://youtu.be/hQtL6oGKNCI

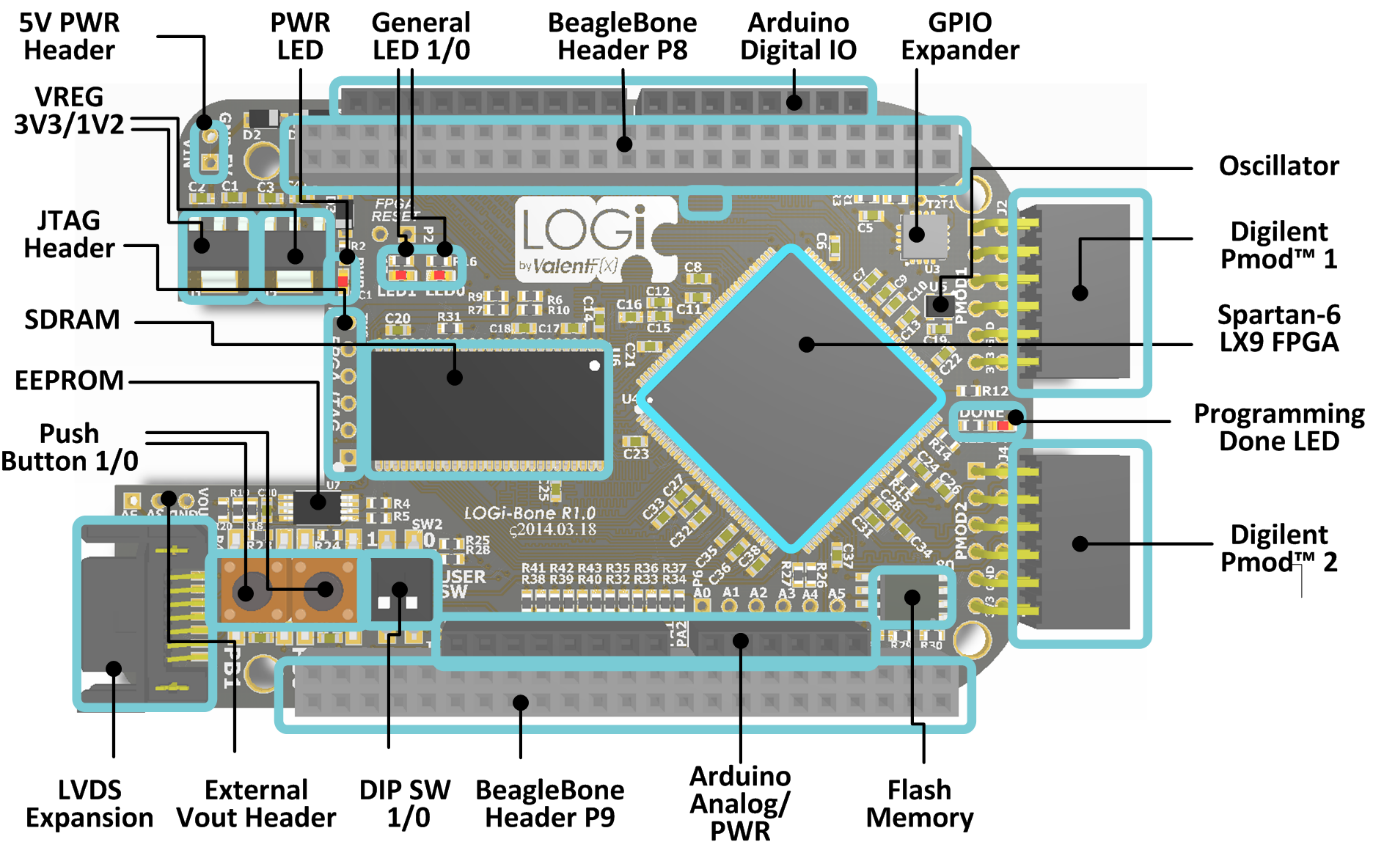
**Follow the submission guideline to be awarded points for this Assignment.**

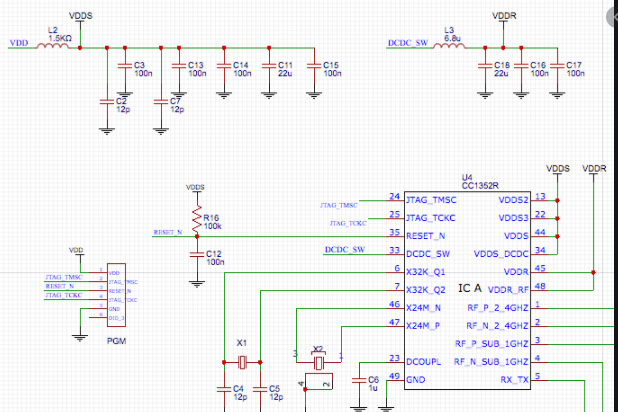
Submit the following for all Assignments:

1. In the document, for each task submit the modified or included code (from the base code) with highlights and justifications of the modifications. Also include the comments. If no base code is provided, submit the base code for the first task only.
2. Create a private Github repository with a random name (no CPE/403, Lastname, Firstname). Place all labs under the root folder TIVAC, sub-folder named Assignment1, with one document and one video link file for each lab, place modified c files named as asng\_taskxx.c.
3. If multiple c files or other libraries are used, create a folder asng1\_t01 and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) with startup\_ccs.c and other include files, c) text file with youtube video links (see template).
5. Submit the doc file in canvas before the due date. The root folder of the github assignment directory should have the documentation and the text file with youtube video links.
6. Organize your youtube videos as playlist under the name “cpe403”. The playlist should have the video sequence arranged as submission or due dates.
7. Only submit pdf documents. Do not forget to upload this document in the github repository and in the canvas submission portal.
8. Code for Tasks. for each task submit the modified or included code (from the base code) with highlights and justifications of the modifications. Also include the comments. If no base code is provided, submit the base code for the first task only. Use separate page for each task.

-No Code was changed , since we area using old hex , so old files from assignments before

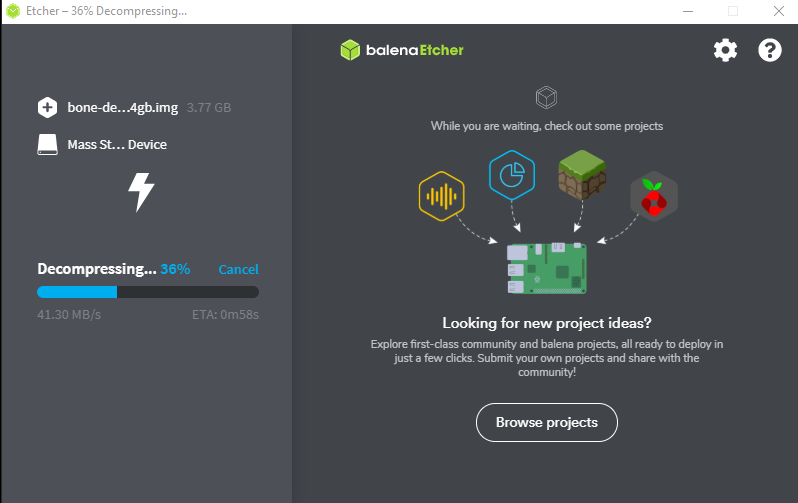
2.Block diagram and/or Schematics showing the components, pins used, and interface.



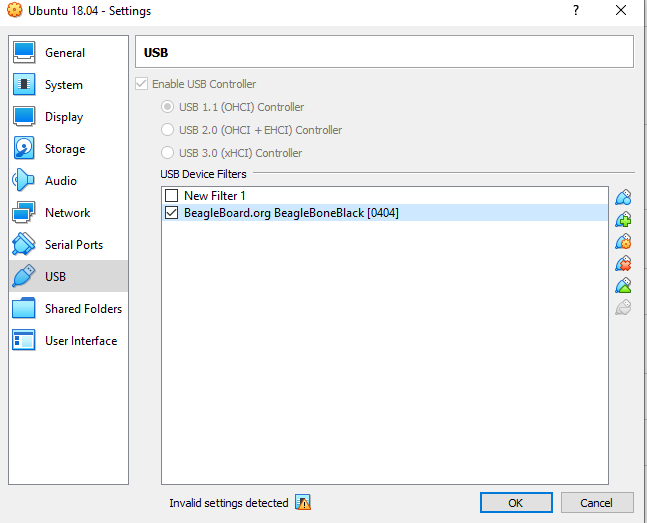


1. Screenshots of the IDE, physical setup, debugging process - Provide screenshot of successful compilation, screenshots of registers, variables, graphs, etc.

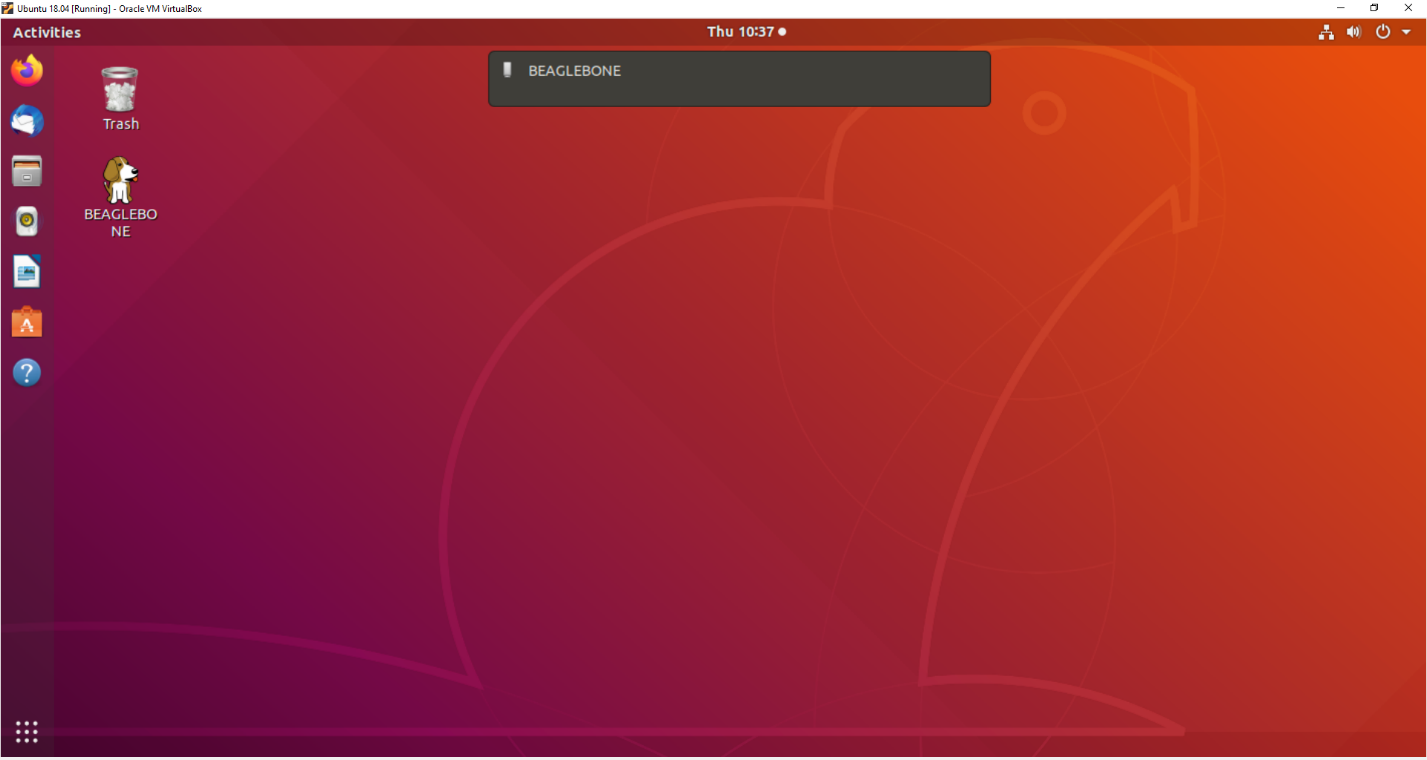
**When I successfully downloaded the Debian image.**

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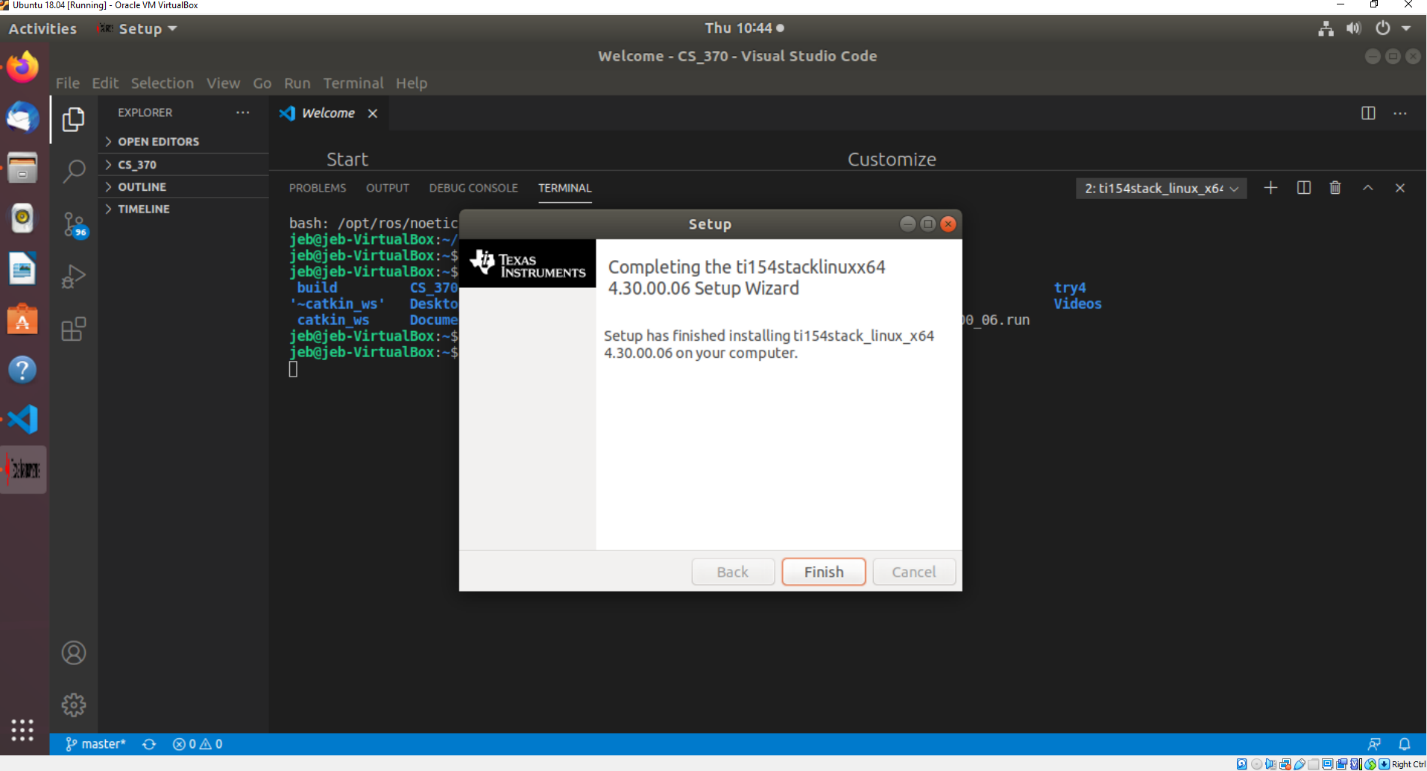
**Successfully connecting the board onto my VM device:**

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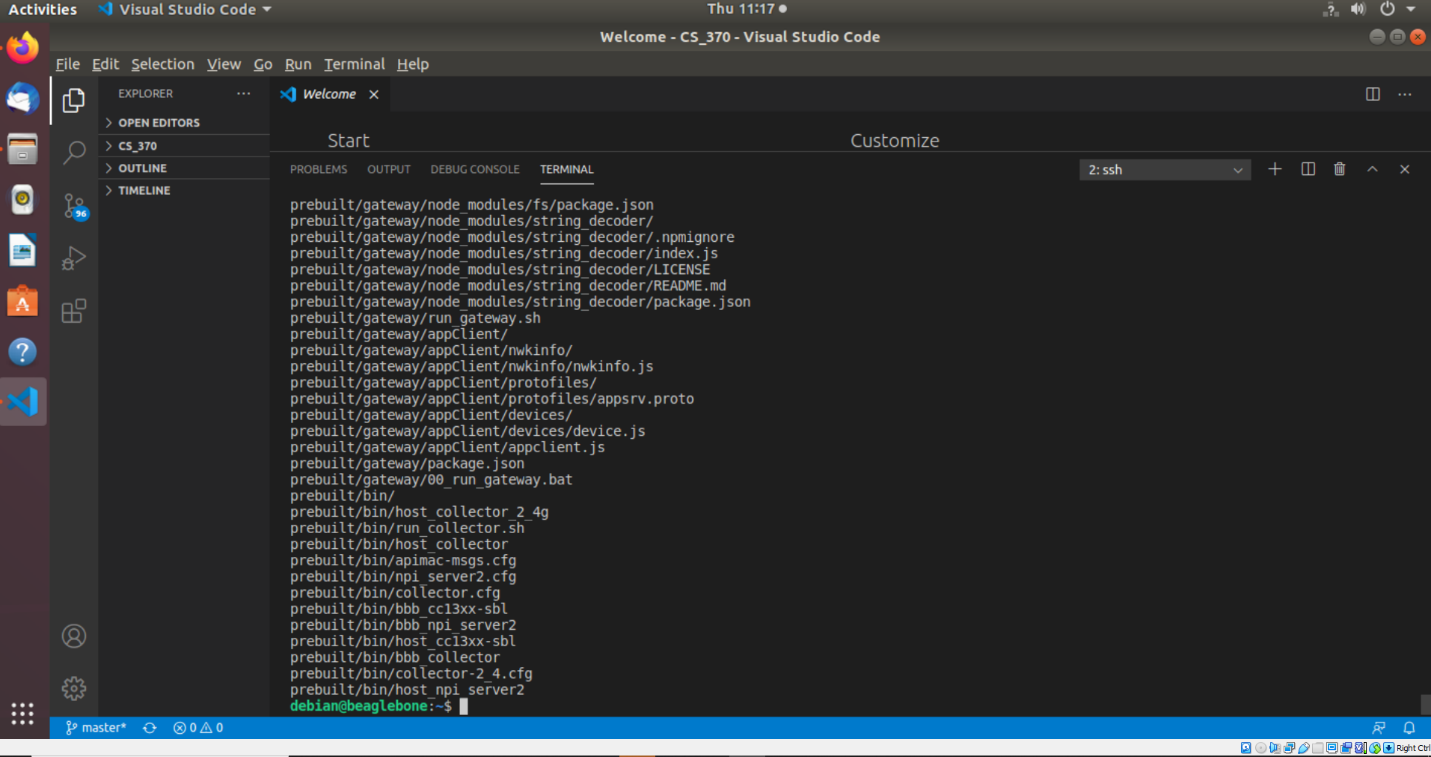
**When Beagle Board is being successfully recognized in my Virtual Box:**

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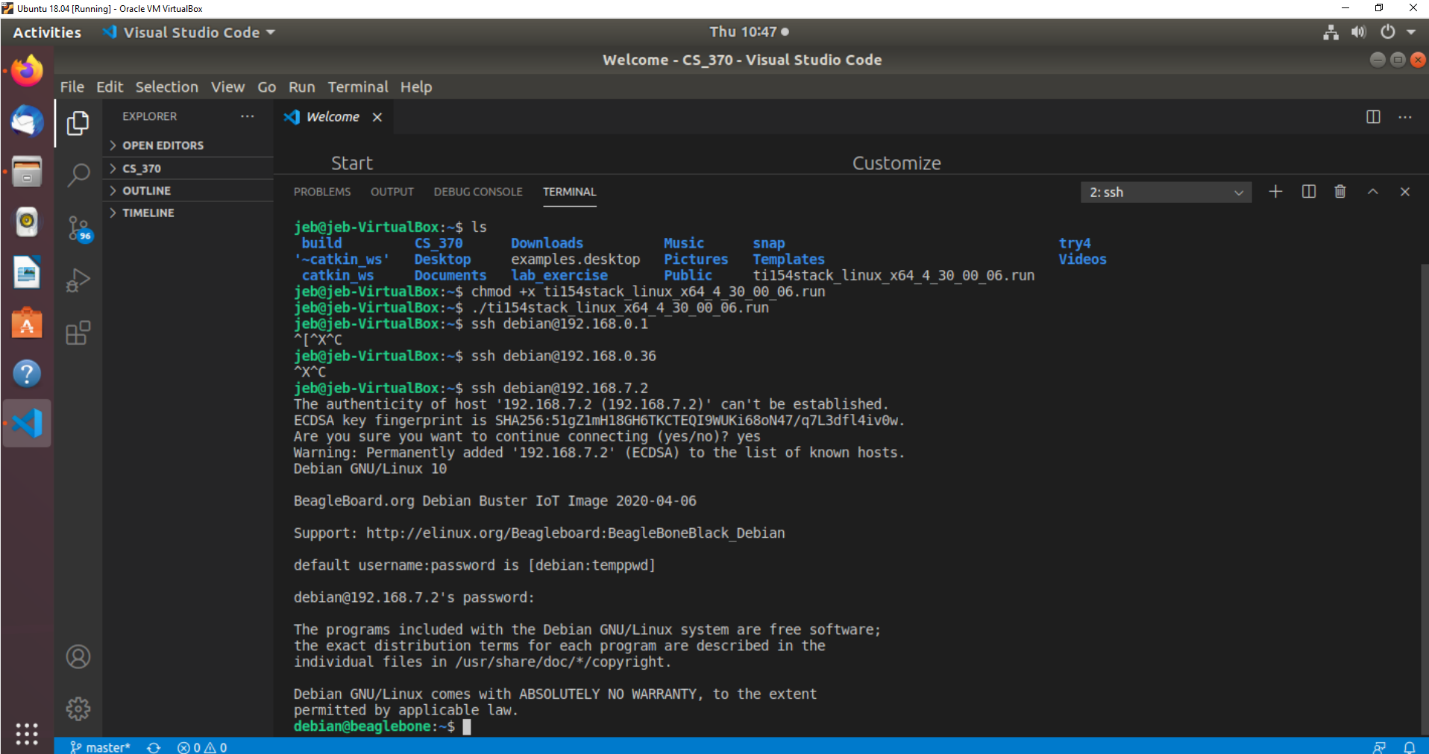
**Successful download of TI-54 Linux:**

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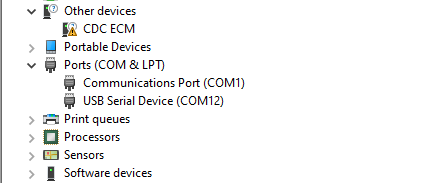
**Successful file Extraction:**

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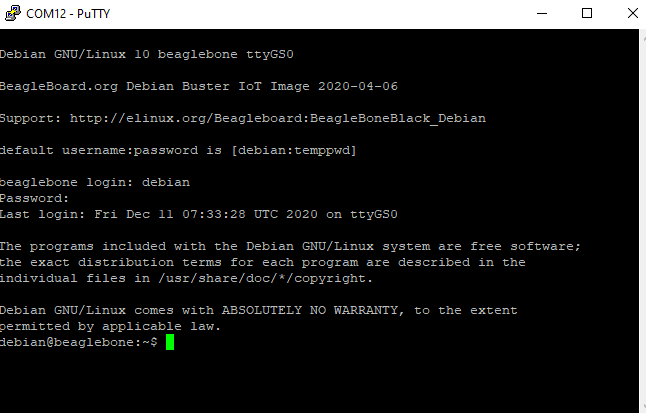
**Successful logged in for Beagle Bone through UBUNTU:**

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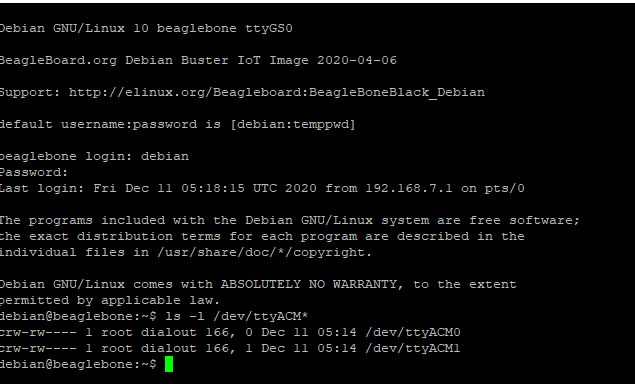
**Successful port host connection**

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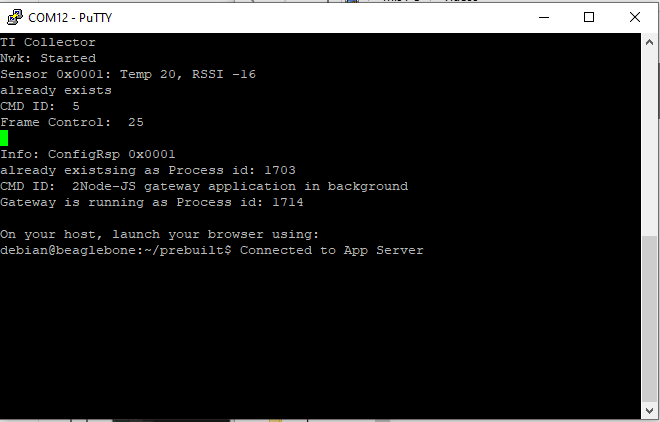
**Successful Host log onto Beagle Bone:**

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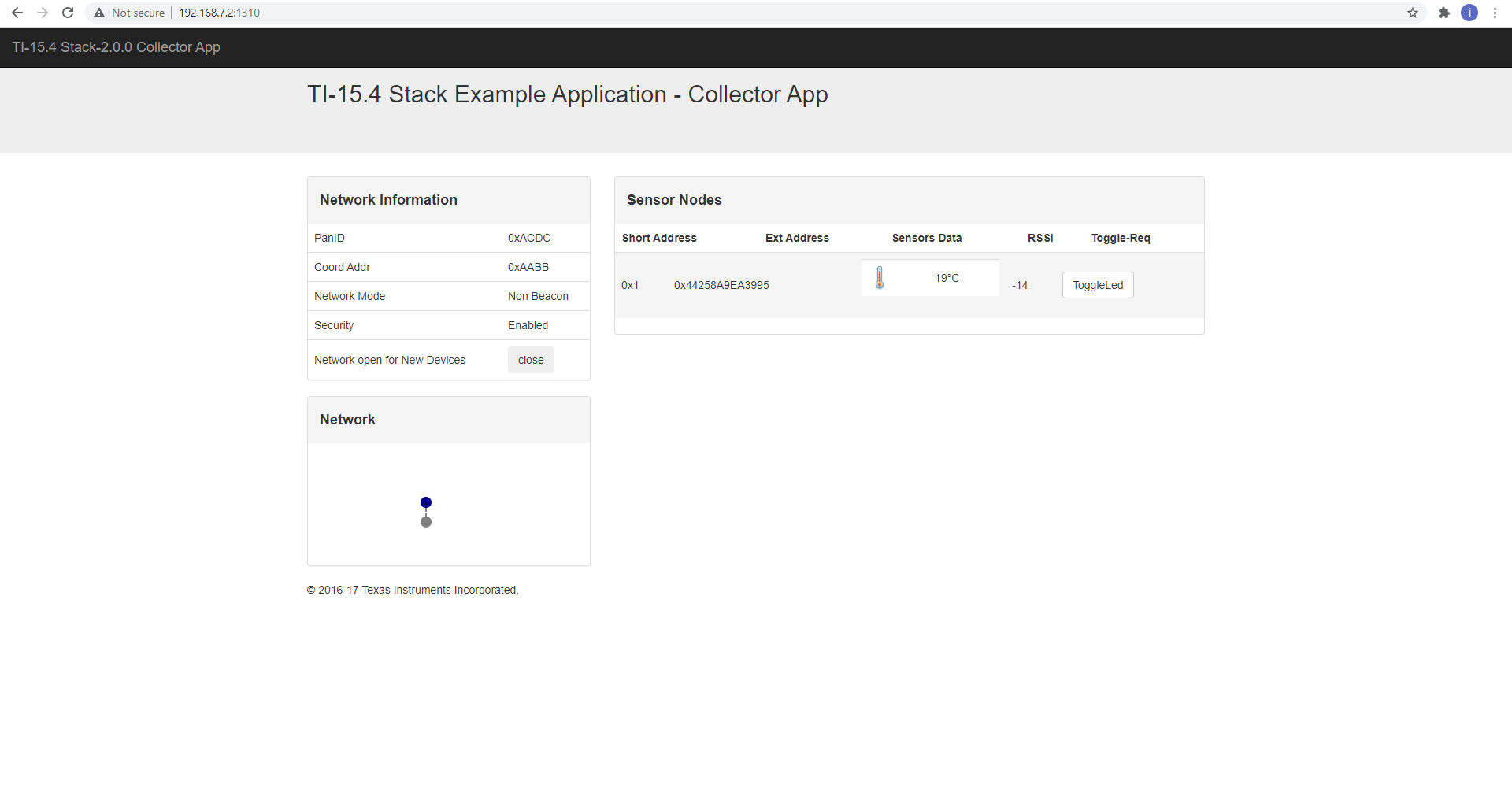
**Successful connection through host and the sensor:**

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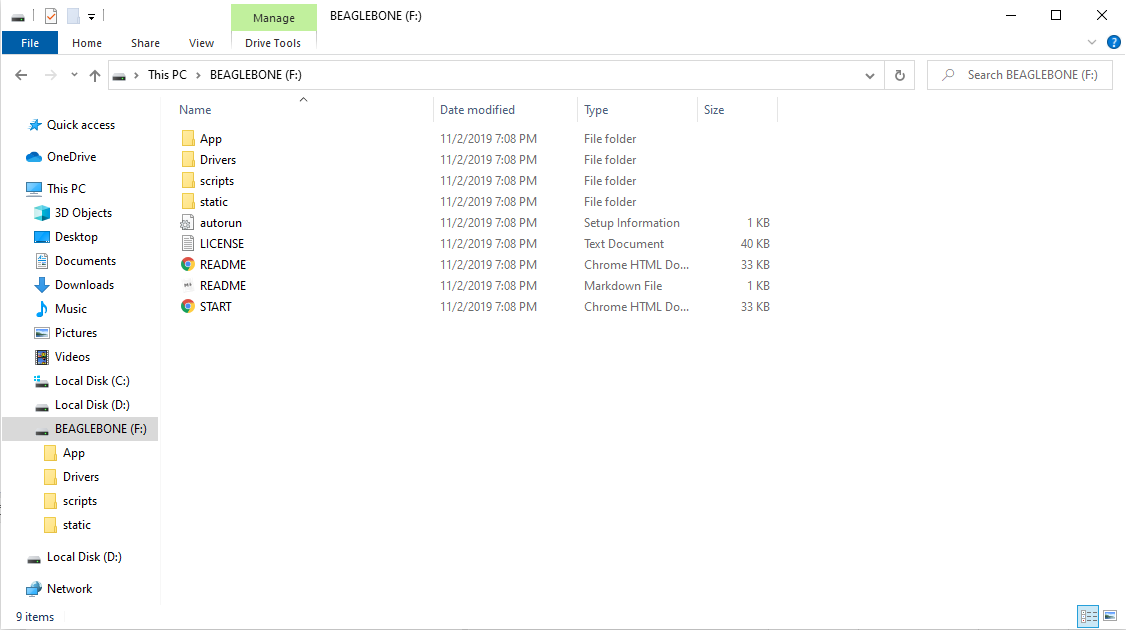
**Successful Connection to my TI host:**

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**Successful Sensor Output:**

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**Inside Beagle bone files**

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**Set up:**

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1. Declaration

I understand the Student Academic Misconduct Policy - http://studentconduct.unlv.edu/misconduct/policy.html

“This assignment submission is my own, original work”.

Name of the Student

Jeb Marinas