Sujeevan VIGNESWARAN

2nd Year PhD Student

CONTACT

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📍 Talence, Gironde

📅 DoB : 8th of May 2000

in linkedin.com/in/sujeevan-vigneswaran

SKILLS

Design Software : ADS Keysight, CST Studio, Cadence, Proteus

Programming Language : C, C++, Python, Matlab, VHDL, Java

LANGUAGES

❖ **French –** Native Speaker
❖ **English –** Fluent
❖ **Tamil –** Native Speaker
❖ **Spanish –** Beginner Level

HOBBIES

- Cycling (MTB)
- Reading

PROFILE

I'm a PhD student working on the design of power amplifier in GaN technology for 5G applications. Subject : Design and digital linearization of a Doherty power amplifier in GaN technology for 5G base stations.

EDUCATION

- **PhD Student in Electronic**

  IMS Laboratory, **Bordeaux, France** | 2023 – Today

- **Master in Electrical Engineering (M2)**

  📍 *University of Bordeaux*, **Bordeaux, France** | 2020 – 2023

  - Department of Electronics [mmW IC design, Reliability, Assembly, PA/LNA Design]

- **Electronic Engineering Diploma**

  📍 *ENSEIRB-MATMECA*, **Bordeaux, France** | 2020 – 2023

  - Department of Electronics [Mathematical Techniques, Embedded Systems, C/C++ Languages]
  - Specialization in Radio and Telecommunication Systems [Radio Frequency architectures, PA/LNA Design, Antenna Design]

WORKING EXPERIENCE

- **Internship – 2nd Year Master's Degree**

  📍 *UMS, Villebon-Sur-Yvette, France* | 2023 (6 months)

  - Design of a linear power amplifier un GaAs for WiFi applications

- **Internship – 1st Year Master's Degree**

  📍 *Universidad del Pais Vasco, Bilbao, Spain*| 2022 (4 months)

  - Automatic cardiac rhythm classification with concurrent Manuel chest compressions

- **Internship – 3rd Year Bachelor's Degree**

  📍 *SEF Power, Fontenay-Trésigny, France*| 2021 (2 months)

  - Assembly of electronic cards
  - Exploring the professional world