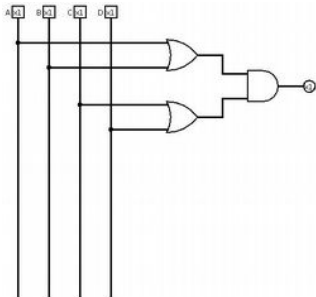
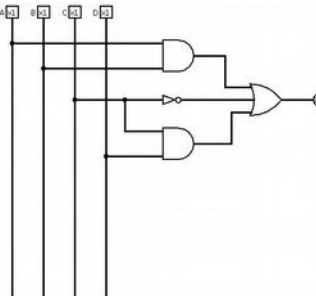
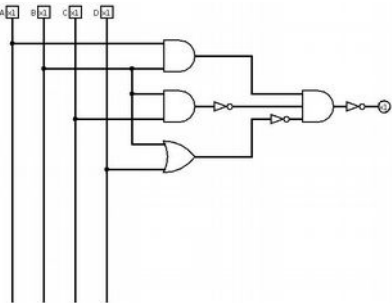
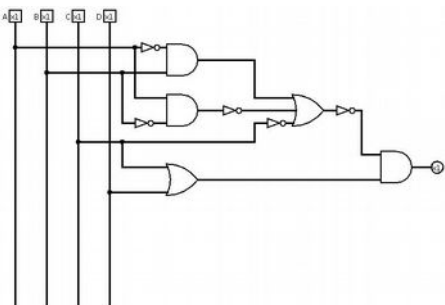
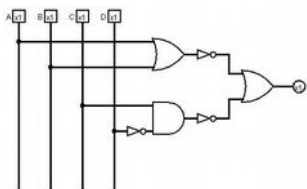
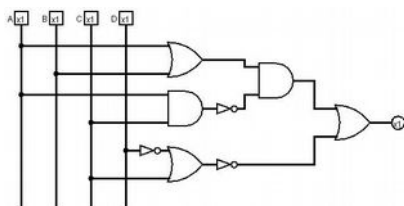


Expressões Obtidas de Circuitos Lógicos

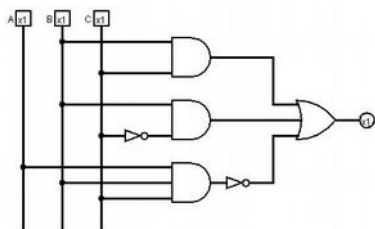
 <p>A logic circuit with four inputs labeled A, B, C, and D. The circuit consists of two OR gates and one AND gate. The first OR gate has inputs A and B. The second OR gate has inputs C and D. The outputs of these two OR gates are connected to the inputs of a single AND gate, which produces the final output.</p>	$(A + B) \cdot (C + D)$
 <p>A logic circuit with four inputs labeled A, B, C, and D. The circuit consists of two AND gates, one OR gate, and one NOT gate. The first AND gate has inputs A and B. The second AND gate has inputs C and D. The output of the first AND gate is connected to one input of an OR gate. The output of the second AND gate is connected to the input of a NOT gate. The output of the NOT gate is connected to the other input of the OR gate. The output of the OR gate produces the final result.</p>	$(A \cdot B) + \overline{C} + (C \cdot D)$
 <p>A logic circuit with four inputs labeled A, B, C, and D. The circuit consists of two AND gates, one OR gate, one NOT gate, and one AND gate. The first AND gate has inputs A and B. The second AND gate has inputs B and C. The output of the first AND gate is connected to one input of a third AND gate. The output of the second AND gate is connected to the input of a NOT gate. The output of the NOT gate is connected to the other input of the third AND gate. The output of the third AND gate is connected to one input of an OR gate. The other input of the OR gate is connected to inputs B and D. The output of the OR gate produces the final result.</p>	$(A \cdot B) \cdot (B \cdot C) \cdot (B + D)$
 <p>A logic circuit with four inputs labeled A, B, C, and D. The circuit consists of two AND gates, two OR gates, one NOT gate, and one AND gate. The first AND gate has inputs A and B. The output of the first AND gate is connected to one input of a second AND gate. The other input of the second AND gate is connected to the input of a NOT gate. The output of the NOT gate is connected to the other input of the second AND gate. The output of the second AND gate is connected to one input of an OR gate. The other input of the OR gate is connected to inputs A and B. The output of the OR gate is connected to one input of a third OR gate. The other input of the third OR gate is connected to inputs C and D. The output of the third OR gate produces the final result.</p>	$((A + B) + (A \cdot \overline{B}) + \overline{C})) \cdot (C + D)$



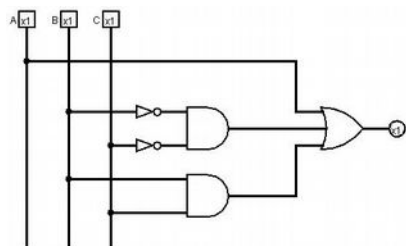
$$(A + B) + ((C \cdot D) + 1)$$



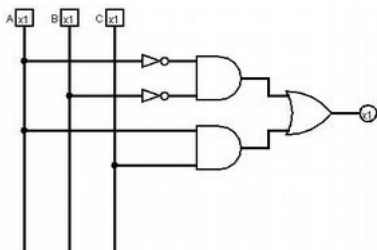
$$((A + B) \cdot (A \cdot C)) + ((C + D) + 1)$$



$$(B \cdot C) + (B \cdot C) + (A \cdot B \cdot C)$$



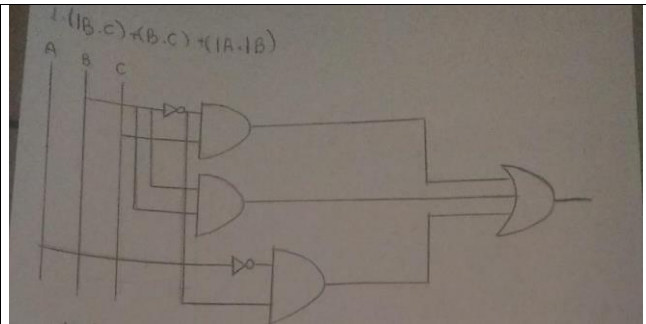
$$A + ((B \cdot C) + (B \cdot C))$$



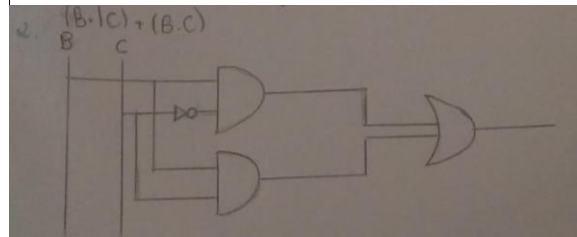
$$((A \cdot B) + (A \cdot C))$$

Circuitos Lógicos Obtidos de Expressões

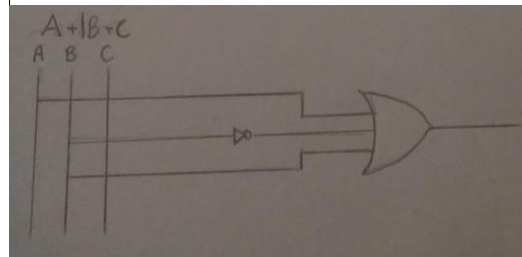
1- $(|B.C) + (B.C) + (|A.B)$



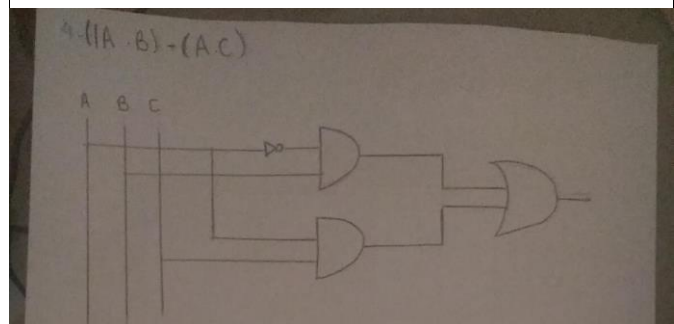
2- $(B.|C) + (B.C)$



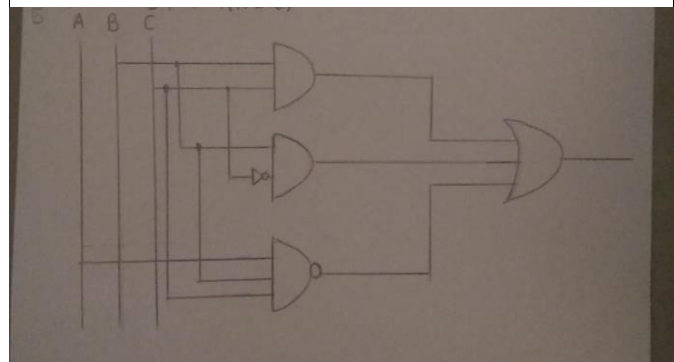
3- $A + |B + C$



4- $(|A . B) + (A . C)$



5- $(B.C) + (B.|C) + |(A.B.C)$



6- $(\neg B.C) + (\neg A.\neg B) + (A.B.C)$

