

# THESIYA JEEL RAMESHBHAI

B.Tech – Computer Science and Engineering

Tel +91 94295 59098 @ jeelthesiya01@gmail.com @ 202511065@dau.ac.in

in linkedin.com/in/jeel-thesiya-8a2331369 GitHub github.com/jeel00dev



## EDUCATION

<b>Dhirubhai Ambani University</b>	2021 – 2027 (Expected)
<b>B.Tech – Computer Science and Engineering</b> CPI: 7.67	Gandhinagar, Gujarat
<b>Krishna Royal School (GHSEB)</b>	2019 – 2021
<b>12th – Science Stream</b> Percentage: 70%	Gujarat
<b>Krishna Science School (GSEB)</b>	2017 – 2019
<b>10th</b> Percentage: 91%	Gujarat

## PROJECTS

<b>AlphaZero Chess Engine</b>   C++, Python, Cython, PyTorch, MCTS, Reinforcement Learning	GitHub
<ul style="list-style-type: none"><li>Built an AlphaZero-style self-play chess engine from scratch with a C++ chess environment and Cython bindings for high-performance Python-C++ interop, eliminating all handcrafted evaluation functions.</li><li>Implemented Monte Carlo Tree Search (MCTS) coupled with a PyTorch neural network trained entirely via self-play; automated a full pipeline — game generation, replay buffer storage, network training, and model checkpointing.</li><li>Developed an ELO rating system and arena evaluation framework to benchmark successive model checkpoints and track strength progression across training iterations.</li></ul>	
<b>C++ Machine Learning Library (AMD GPU)</b>   C++, Vulkan, OpenCL, GPGPU, Linear Algebra	GitHub
<ul style="list-style-type: none"><li>Engineered a from-scratch C++ ML library targeting AMD GPU acceleration via Vulkan compute shaders and OpenCL kernels, optimizing tensor operations and matrix multiplication for non-CUDA GPU architectures.</li><li>Implemented core ML primitives including forward/backward passes, activation functions, and gradient descent optimizers with hardware-aware memory management and GPU workgroup tuning.</li><li>Designed a clean API for building and training neural networks entirely in C++, achieving significant speedups over CPU-only execution by leveraging parallel GPU compute pipelines.</li></ul>	
<b>Custom 5×6.5 Handwired Keyboard (RP2040 Zero)</b>   C, QMK Firmware, RP2040, Python, CAD/3D Print	GitHub
<ul style="list-style-type: none"><li>Designed and hand-wired a full Dactyl Manuform-style ergonomic split keyboard (5×6.5 layout) around the RP2040 Zero MCU, including custom PCB-free wiring, diode matrix soldering, and TRRS interconnect.</li><li>Authored a custom QMK keymap and firmware configuration for the RP2040 target; set up the full QMK toolchain with Python-based build automation and UF2 flashing workflow for the RP2040 bootloader.</li><li>Produced STL models for 3D-printed case components and documented the full build process — wiring diagrams, soldering reference photos, and flashing instructions — for reproducibility.</li></ul>	

## TECHNICAL SKILLS

**Languages:** C++, C, Python, Java, Go, JavaScript, Bash

**Developer Tools:** Git, Docker, Linux, QMK Firmware, VS Code

**ML / AI:** PyTorch, NumPy, Cython, Reinforcement Learning, MCTS, Neural Networks

**GPU / Systems:** Vulkan Compute, OpenCL, GPGPU, RP2040 / Embedded C, 3D Printing (STL/CAD)

**Coursework:** Data Structures & Algorithms, Operating Systems, Computer Networks, DBMS, Machine Learning

**Areas of Interest:** Systems Programming, GPU Computing, AI/ML, Embedded Systems, Compiler Design