

P-Channel 20 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
- 20	0.112 at V _{GS} = - 4.5 V	- 3.1	3.3 nC
	0.142 at V _{GS} = - 2.5 V	- 2.7	

FEATURES

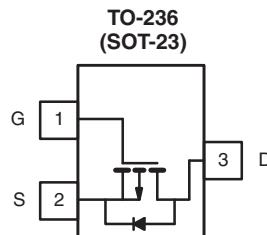
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available

APPLICATIONS

- Load Switch



Top View

Si2301CDS (N1)*

* Marking Code

Ordering Information: Si2301CDS-T1-E3 (Lead (Pb)-free)
Si2301CDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 20		V
Gate-Source Voltage	V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	- 3.1	A
	T _C = 70 °C		- 2.5	
	T _A = 25 °C		- 2.3 ^{b, c}	
	T _A = 70 °C		- 1.8 ^{b, c}	
Pulsed Drain Current	I _{DM}	- 10		W
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	- 1.3	
	T _A = 25 °C		- 0.72 ^{b, c}	
Maximum Power Dissipation	T _C = 25 °C	P _D	1.6	
	T _C = 70 °C		1.0	
	T _A = 25 °C		0.86 ^{b, c}	
	T _A = 70 °C		0.55 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	120	145	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	62	78	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 175 °C/W.

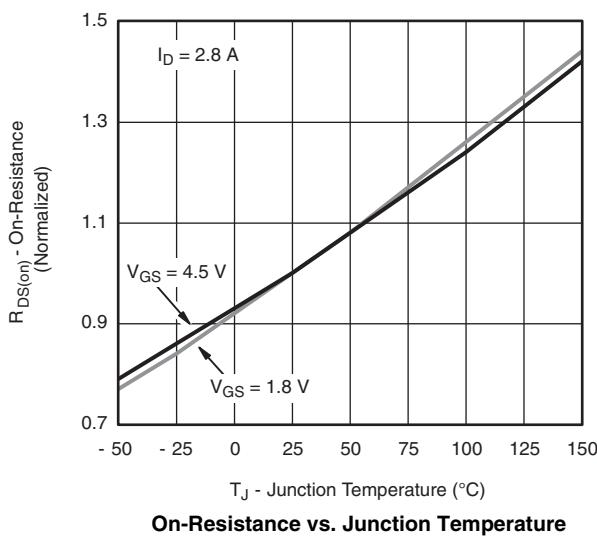
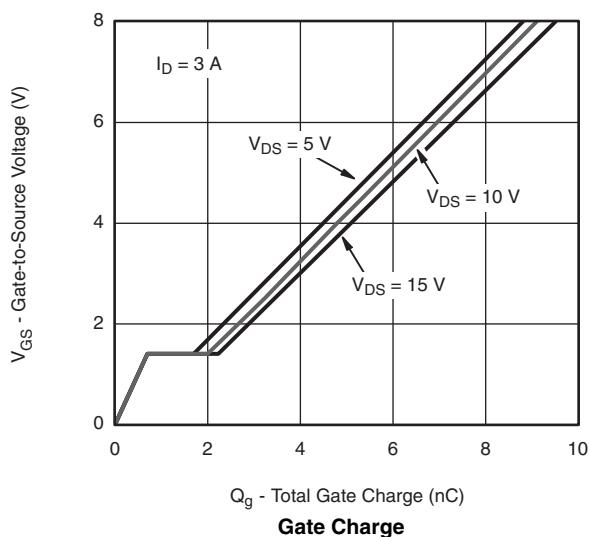
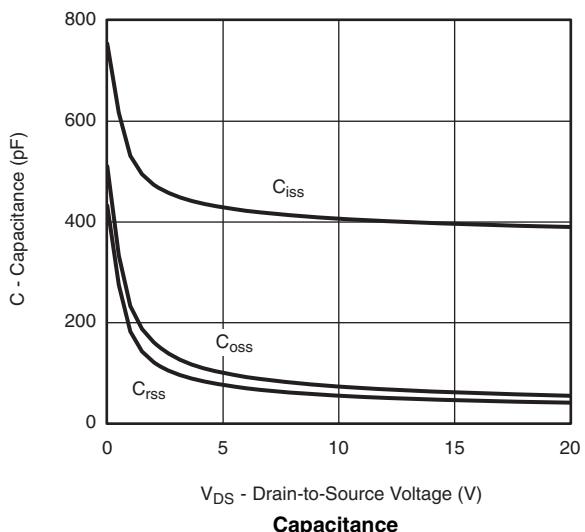
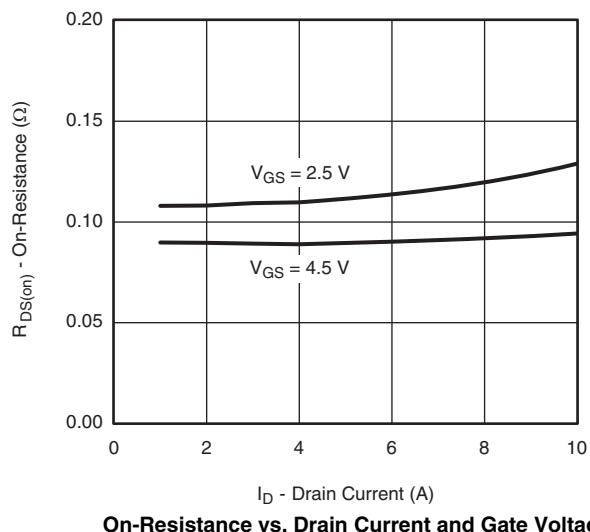
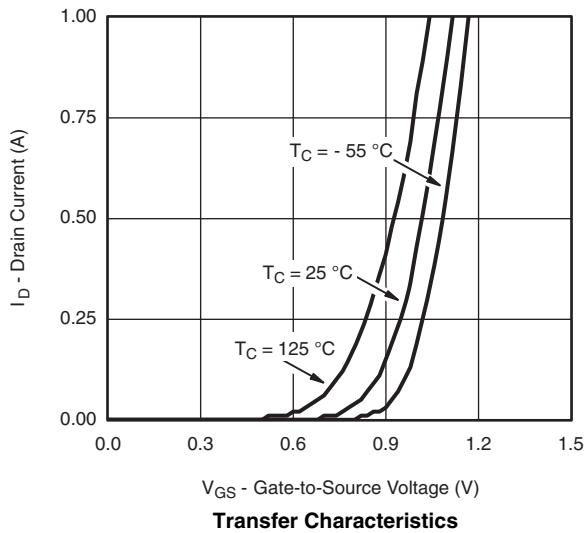
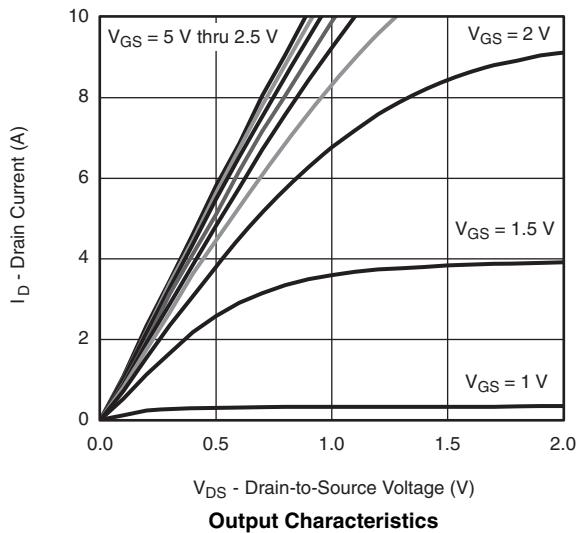
MOSFET SPECIFICATIONS ($T_J = 25^\circ\text{C}$, unless otherwise noted)

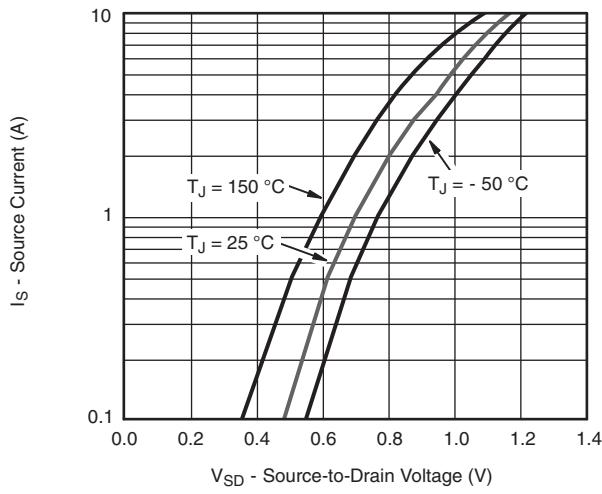
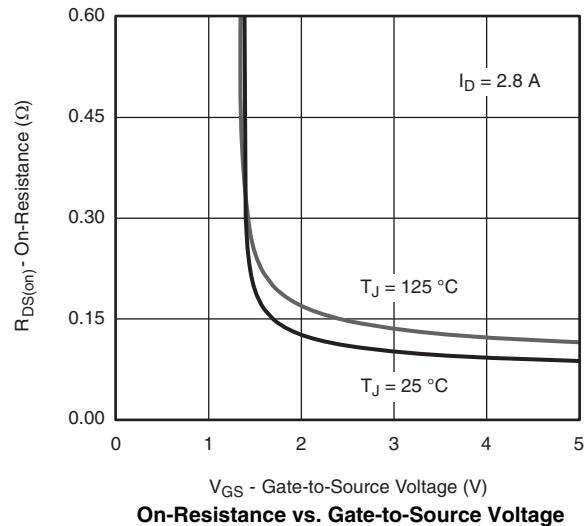
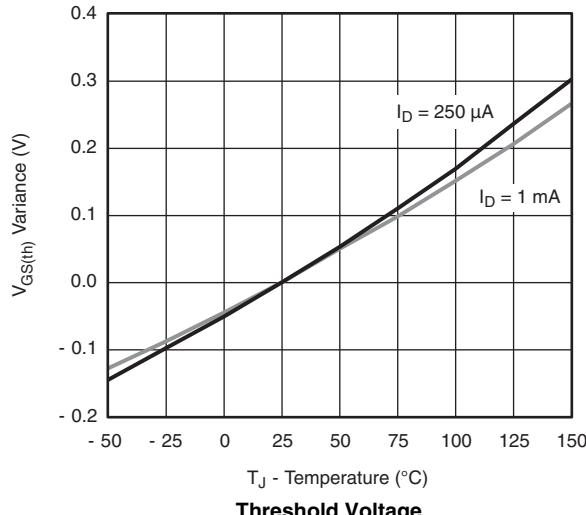
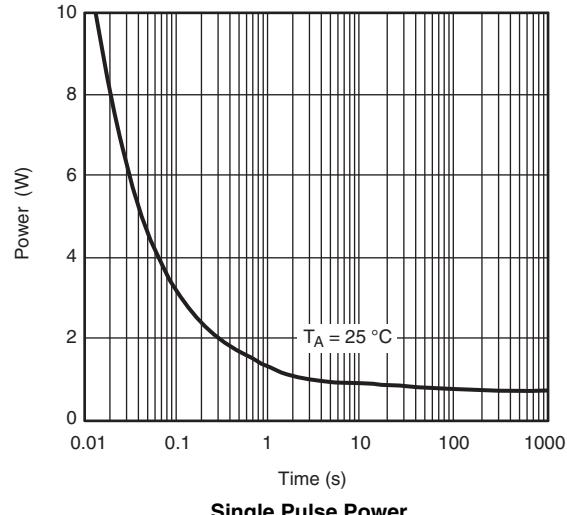
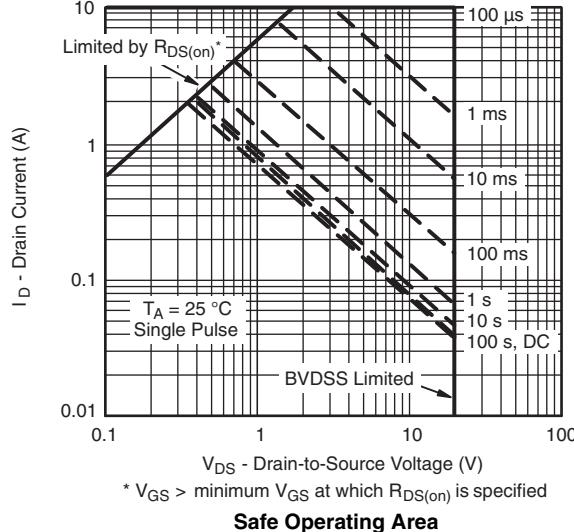
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20			V	
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250 \mu\text{A}$		-18		mV/°C	
$V_{GS(\text{th})}$ Temperature Coefficient	$\Delta V_{GS(\text{th})}/T_J$			2.2			
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	-0.4		-1	V	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μA	
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$			-10		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} \leq -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-6			A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$		0.090	0.112	Ω	
		$V_{GS} = -2.5 \text{ V}, I_D = -2.0 \text{ A}$		0.110	0.142		
Forward Transconductance ^a	g_{fs}	$V_{DS} = -5 \text{ V}, I_D = -2.8 \text{ A}$		9.5		S	
Dynamic^b							
Input Capacitance	C_{iss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		405		pF	
Output Capacitance	C_{oss}			75			
Reverse Transfer Capacitance	C_{rss}			55			
Total Gate Charge	Q_g	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -3 \text{ A}$		5.5	10	nC	
Gate-Source Charge	Q_{gs}			3.3	6		
Gate-Drain Charge	Q_{gd}			0.7			
Gate Resistance	R_g		$f = 1 \text{ MHz}$	1.3			
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D = -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		6.0		Ω	
Rise Time	t_r			11	20	ns	
Turn-Off Delay Time	$t_{d(\text{off})}$			35	60		
Fall Time	t_f			30	50		
				10	20		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$ $I_S = -0.7 \text{ A}$ $I_F = -3.0 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$			-1.3	A	
Pulse Diode Forward Current ^a	I_{SM}				-10		
Body Diode Voltage	V_{SD}				-0.8	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}			30	50	ns	
Body Diode Reverse Recovery Charge	Q_{rr}			25	50		
Reverse Recovery Fall Time	t_a			15		ns	
Reverse Recovery Rise Time	t_b			15			

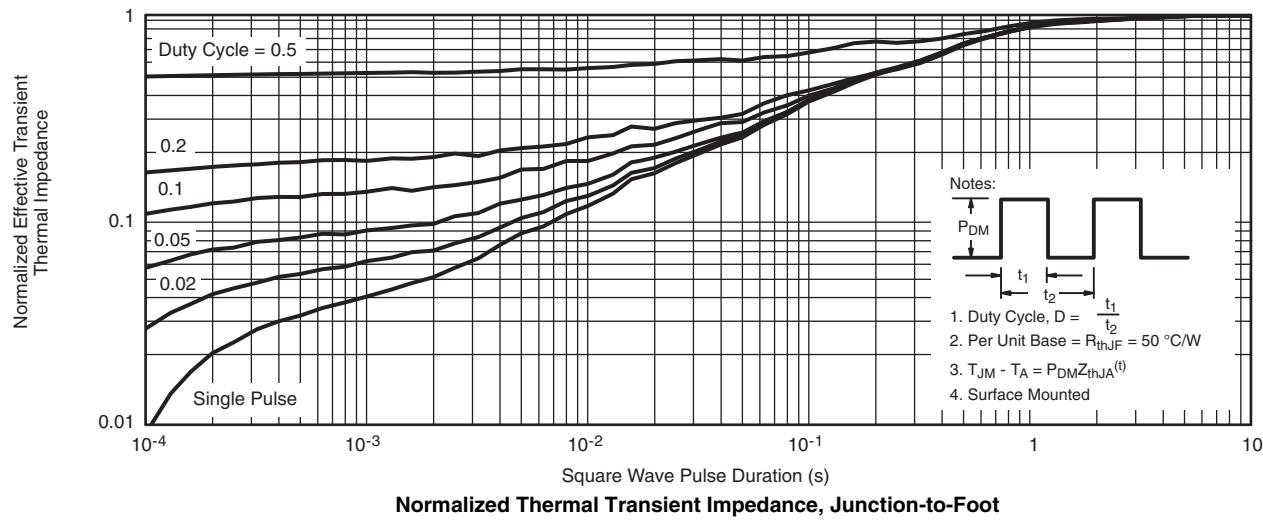
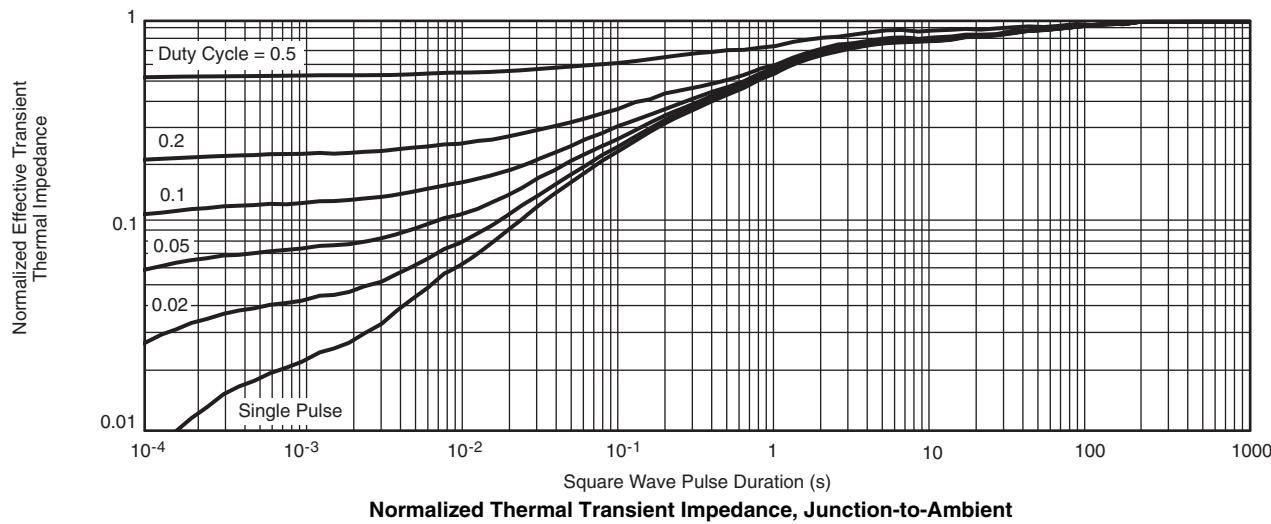
Notes:

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

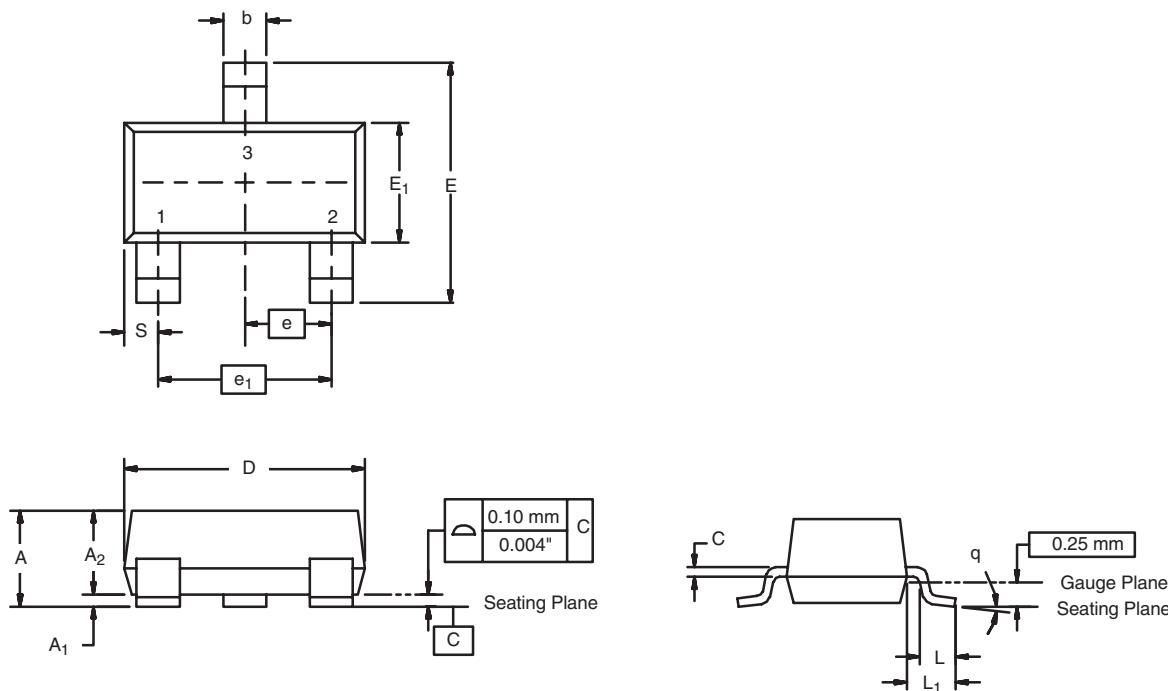
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power****Safe Operating Area**

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


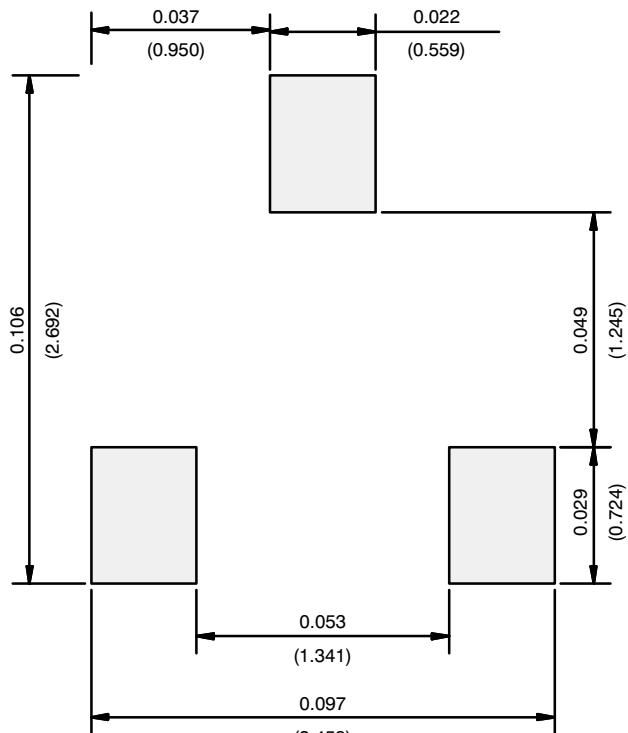
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SOT-23 (TO-236): 3-LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°

ECN: S-03946-Rev. K, 09-Jul-01
DWG: 5479

RECOMMENDED MINIMUM PADS FOR SOT-23[Return to Index](#)



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