

1.

Area( $\mu\text{m}^2$ )	449628
Clock cycle(ns)	7.2ns
Total simulation time(ns)	8540.395 ns
Cost( $\mu\text{m}^2 \cdot \text{ns}^2$ )	3.2792E+13

2.

area

```
Combinational area:      300597.657832
Buf/Inv area:           50295.659483
Noncombinational area:   98736.060307
Macro/Black Box area:    69557.296875
Net Interconnect area:   4422995.715668

Total cell area:         468891.015013
Total area:              4891886.730681
```

RTL

```
Simulation Summary
-----
Congratulations! All data have been generated successfully!
-----PASS-----
err= 0
Simulation complete via $finish(1) at time 8539200 PS + 0
./testfixture_c.v:201      #(`CYCLE/2); $finish;
```

Gate level

```
Simulation Summary
-----
Congratulations! All data have been generated successfully!
-----PASS-----
err= 0
Simulation complete via $finish(1) at time 8540395 PS + 0
./testfixture_c.v:201      #(`CYCLE/2); $finish;
ncsim> exit
```

3.

- (1)把 ROM 的 value 掃一遍讀出來，存到自己的暫存器，
- (2)mark label
- (3)存到 SRAM