Cinterion® TX62/TX82

Hardware Interface Description

1VV0301898 - Rev. 0
V01.200e
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Preliminary
Confidential

Contents

Tables

Figures

1 Applicability Table

Table 1: **Applicability table**

Products
Cinterion® TX62-W (as of v01.200)
Cinterion® TX62-W-B (as of v01.000)
Cinterion® TX62-W-C (as of v01.000)
Cinterion® TX82-W (as of v01.200)
Cinterion® TX62-W-B (as of v01.200 Engineering Samples)

The following table shows the differences between the product variants. Wherever necessary a note is made to differentiate between the product variants.

Table 2: **Differences between product variants**

Feature	TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B
Dimensions (mm)	15.3x15.3x 2.9	15.3x20.9x2.28	15.3x20.9x2.92	15.3x20.9x2.28	15.3x20.9x2.92
GSM (2G)	--	--	--	Supported	Supported
450MHz bands (Bd31, Bd72)	--	--	Supported	--	--
RF Output Power Class	Class 5	Class 3	Class 2 for 450MHz bands Class 3 for all other bands	Class 5	Class 3

Note:

TX82-W-B is still at an early development stage and not yet officially available.

2 Introduction

2.1 Scope

This document[1] describes the hardware of the Cinterion® Cinterion® TX62/TX82 module variants optimized for global coverage as they support a comprehensive set of bands required for global deployment. It helps you quickly retrieve interface specifications, electrical and mechanical details and information on the requirements to be considered for integrating further components.

2.2 Audience

This document is intended for system integrators that are using the Telit XX123Z4 module in their products.

2.3 Contact Information, Support

For technical support and general questions, e-mail:

- TS-EMEA@telit.com
- TS-AMERICAS@telit.com
- TS-APAC@telit.com
- TS-SRD@telit.com
- TS-ONEEDGE@telit.com

Alternatively, use: https://www.telit.com/contact-us/

Product information and technical documents are accessible 24/7 on our website: https://www.telit.com

2.4 Conventions

Note: Provide advice and suggestions that may be useful when integrating the module.

Danger: This information MUST be followed, or catastrophic equipment failure or personal injury may occur.

ESD Risk: Notifies the user to take proper grounding precautions before handling the product.

Warning: Alerts the user on important steps about the module integration.

All dates are in ISO 8601 format, that is YYYY-MM-DD.

1. The document is effective only if listed in the appropriate Release Notes as part of the technical documentation delivered with your Telit Cinterion product.

2.5 Terms and Conditions

Refer to https://www.telit.com/hardware-terms-conditions/.

2.6 Disclaimer

2.7 Key Features at a Glance

Feature	Implementation
General	
Frequency bands (see chapter 3.2.1)	GSM (TX82-W and TX82-W-B): 850/900/1800/1900 LTE Cat M1 (TX82-W, TX82-W-B, TX62-W, TX62-W-B): 700 (Bd12, Bd13, Bd28, Bd85), 800 (Bd18, Bd19, Bd20, Bd26, Bd27), 850 (Bd5), 900 (Bd8), AWS-3 (Bd66), AWS-1 (Bd4), 1800 (Bd3), 1900 (Bd2, Bd25), 2100 (Bd1) LTE Cat NB1/2: (TX82-W, TX82-W-B, TX62-W, TX62-W-B): 600 (Bd71; not supported with TX82-W-B), 700 (Bd12, Bd13, Bd28, Bd85), 800 (Bd18, Bd19, Bd20, Bd26), 850 (Bd5), 900 (Bd8), AWS-3 (Bd66), AWS-1 (Bd4), 1800 (Bd3), 1900 (Bd2, Bd25), 2100 (Bd1) LTE Cat M1 (TX62-W-C): 450 (Bd31, Bd72), 700 (Bd28), 800 (Bd20), 900 (Bd8), 1800 (Bd3), 2100 (Bd1) LTE Cat NB1/2 (TX62-W-C): 450 (Bd31, Bd72), 700 (Bd28), 800 (Bd20),900 (Bd8), 1800 (Bd3), 2100 (Bd1) **Note:** With TX62-W-C support for LTE Cat NB1/2 is by default deactivated, but may be activated on demand.
GSM class	Small MS
Output power (according to Release 7)	GSM/GPRS (TX82-W and TX82-W-B): Class 4 (+33dBm ±2dB) for GSM850 and GSM900 Class 1 (+30dBm ±2dB) for GSM1800 and GSM1900 Class E2 (+27dBm ± 3dB) for GSM850 8-PSK and GSM 900 8-PSK Class E2 (+26dBm +3 /-4dB) for GSM 1800 8-PSK and GSM1900 8-PSK

Feature	Implementation
Output power (according to 3GPP Release 13)	TX62-W and TX82-W: LTE Cat M1: Class 5 (+20dBm ±2dB) for all supported LTE Cat M1 bands LTE Cat NB1/2: Class 5 (+20dBm ±2dB) for all supported LTE Cat NB1/2 bands TX62-W-B and TX82-W-B: LTE Cat M1: Class 3 (+23dBm ±2dB) for all supported LTE Cat M1 bands LTE Cat NB1/2: Class 3 (+23dBm ±2dB) for all supported LTE Cat NB1/2 bands TX62-W-C: LTE Cat M1: Class 2 (+26dBm ±2dB) for LTE CAT M1 bands Bd31, Bd72 Class 3 (+23dBm ±2dB) for all other supported LTE Cat M1 bands LTE Cat NB1/2: Class 3 (+23dBm ±2dB) for all supported LTE Cat NB1/2 bands Note: The duty cycle of Cat M1 UL signal is less than 50%.
Power supply (see Section 3.1.2 and Section 4.4)	TX82-W: - LTE and GSM: 3.1V to 4.6V - LTE with GSM deactivated: 2.8V to 4.6V TX82-W-B: - LTE and GSM: 3.1V to 4.5V - LTE with GSM deactivated: 2.9V to 4.5V TX62-W: - LTE: 2.55V to 4.8V TX62-W-B: - LTE: 2.5V to 4.5V TX62-W-C: - LTE: 3.2V to 4.2V
Operating temperature (board temperature) (see Section 4.5)	Normal operation: -30°C to +85°C Extended operation: -40°C to +90°C

Feature	Implementation
Physical (see Section 5.1)	Dimensions: TX62-W: 15.3 mm x 15.3 mm x 2.9 mm TX82-W, TX82-W-B, TX62-W-B and TX62-W-C: 15.3 mm x 20.9 mm x 2.28 mm TX82-W-B, and TX62-W-C: 15.3 mm x 20.9 mm x 2.92 mm Weight: TX62-W: approx. 1g TX82-W, TX82-W-B, TX62-W-B and TX62-W-C: approx. 1.7g
RoHS (see Section 6.1)	All hardware components fully compliant with EU RoHS Directive
LTE features	
3GPP Release 14	LTE Cat M1 (HD-FDD) DL: max. 300kbps, UL: max. 1.1Mbps LTE Cat NB1 (HD-FDD) DL: max. 27kbps, UL: max. 63kbps LTE Cat NB2 (HD-FDD) DL: max. 124kbps, UL: max. 158kbps

Feature	Implementation
GSM/GPRS/EGPRS features	
Data transfer	GPRS (TX82-W and TX82-W-B): Multislot Class 10 Full PBCCH support Mobile Station Class B Coding Scheme 1 – 4 EGPRS (TX82-W and TX82-W-B): Multislot Class 10 EDGE E2 power class for 8 PSK Downlink coding schemes – CS 1-4, MCS 1-9 Uplink coding schemes – CS 1-4, MCS 1-9 SRB loopback and test mode B 8-bit, 11-bit RACH PBCCH support 1 phase/2 phase access procedures Link adaptation and IR NACC, extended UL TBF Mobile Station Class B
SMS	Point-to-point MT and MO Text and PDU mode Storage: SIM card plus SMS locations in mobile equipment
GNSS Features	
Modes (see Section 3.3)	Standalone GNSS (GPS, GLONASS, BeiDou, Galileo)
Protocol	NMEA (for GNSS related sentences)
General	Automatic power saving modes
Software	
AT commands	Hayes 3GPP TS 27.007, TS 27.005, Telit Cinterion AT commands for RIL compatibility
Embedded process-ing platform (optional)	Embedded processing option with API. Memory space available for embedded applications is 512KB for application code, 512KB for File System and 672KB for RAM. Please take into account that the application code is copied into RAM. For more details, please consult software documentation.
SIM Application Toolkit	SAT Release 99
Firmware update	Firmware update from external application over ASC0 and ASC1 interface.

Feature	Implementation
Interfaces	
Module interface	Surface mount device with solderable connection pads (SMT application interface). Land grid array (LGA) technology ensures high solder joint reliability and allows the use of an optional module mounting socket. For more information on how to integrate SMT modules see also [5]. This application note comprises chapters on mounting and application layout issues as well as on additional SMT application development equipment.
USB (see Section 3.1.3)	USB 2.0 High Speed (480Mbit/s) device interface, Full Speed (12Mbit/s) compliant
2 serial interfaces (see Section 3.1.4 and Section 3.1.5)	ASC0: 8-wire modem interface with status and control lines, unbalanced, asynchronous Adjustable baud rates: 300bps to 921,600bps Supports RTS0/CTS0 hardware flow control (as configuration option). ASC1: 4-wire, unbalanced asynchronous modem interface Adjustable baud rates: 300bps to 921,600bps Supports RTS1/CTS1 hardware flow control (as configuration option).
UICC interface (see Section 3.1.6)	Supported SIM/USIM cards: 1.8V
eUICC interface (see Section 3.1.7	Supports embedded MFF-XS UICC interface (as an option).
GPIO interface (see Section 3.1.8)	TX62-W: 6 I/O pins of the application interface programmable as GPIO. Programming can be done via AT commands. TX82-W, TX82-W-B, TX62-W-B and TX62-W-C: 7 I/O pins of the application interface programmable as GPIO. Programming can be done via AT commands. With the embedded processing option 12 (TX62-W) or 13 (TX82-W, TX82-W-B, TX62-W-B and TX62-W-C) I/O pads are programmable as GPIOs and may be shared with other functions (ASC0, ASC1/SPI, fast shutdown, and status).
Status (see Section 3.1.12.1)	Supports status indication LED.
Fast shutdown (see Section 3.1.12.3)	Supports fast shutdown interrupt signal.

Feature	Implementation
ADC Input	Analog-to-Digital Converter with one unbalanced analog inputs
SIM switch (see Section 3.1.12.4)	Supports signal to switch between two externally connected SIMs.
Antenna interface pads (see Section 3.2)	50Ω. GSM/LTE Main antenna, GNSS antenna
I²C interface (see Section 3.1.10)	I²C interface only available with embedded processing option.
SPI interface (see Section 3.1.11)	SPI interface only available with embedded processing option.
Power on/off, Reset	
Power on/off	Switch-on by hardware signal ON Switch-off by AT command and hardware signal FST_SHDN Automatic switch-off in case of critical voltage conditions
Reset	Orderly shutdown and reset by AT command Emergency reset by hardware signal EMERG_RST

Feature	Implementation
Special features	
Approval (see Section 6)	RED, CE, FCC, ISED, UL, RoHS, and REACH compliant GCF, PTCRB
Phonebook	SIM and phone
Cinterion® IoT Suite services	(Optionally) supports an IoT Suite client based on the LWM2M protocol. The client can be configured to collect diagnostic information about the module and cellular network and to send it periodically to the Cinterion® IoT Suite server platform, where it can be visualized for further analysis. Communication to Telit Cinterion Device Management Hub is realized using a resource-efficient protocol specifically designed by Telit Cinterion in order to keep the energy and data usage to a minimum. The protocol behavior may be influenced by means of configuration. Additionally, the service provides device control functionality. This includes remote flash file system management, module firmware over-the-air updates (FOTA) and remote configuration. The Cinterion® IoT Suite also generates alarms when a specific module or network parameter changes or exceeds a threshold. Alarms can be sent to the platform as soon as possible disregarding the connection interval. For more information, please refer to [7] and [8].
Evaluation kit (For ordering information see Section 8.1)	
LGA DevKit	LGA DevKit designed to test Telit Cinterion LGA modules. For more information see also LGA DevKit.
Evaluation module	Cinterion® TX62/TX82 module soldered onto a dedicated PCB that can be connected to the an approval adapter in order to be mounted onto the DSB75 or DSB-Mini.
DSB75	DSB75 Development Support Board designed to test and type approve Telit Cinterion modules and provide a sample configuration for application engineering. A special adapter is required to connect the Cinterion® TX62/TX82 evaluation module to the DSB75.

2.8 TX62/TX82 System Overview

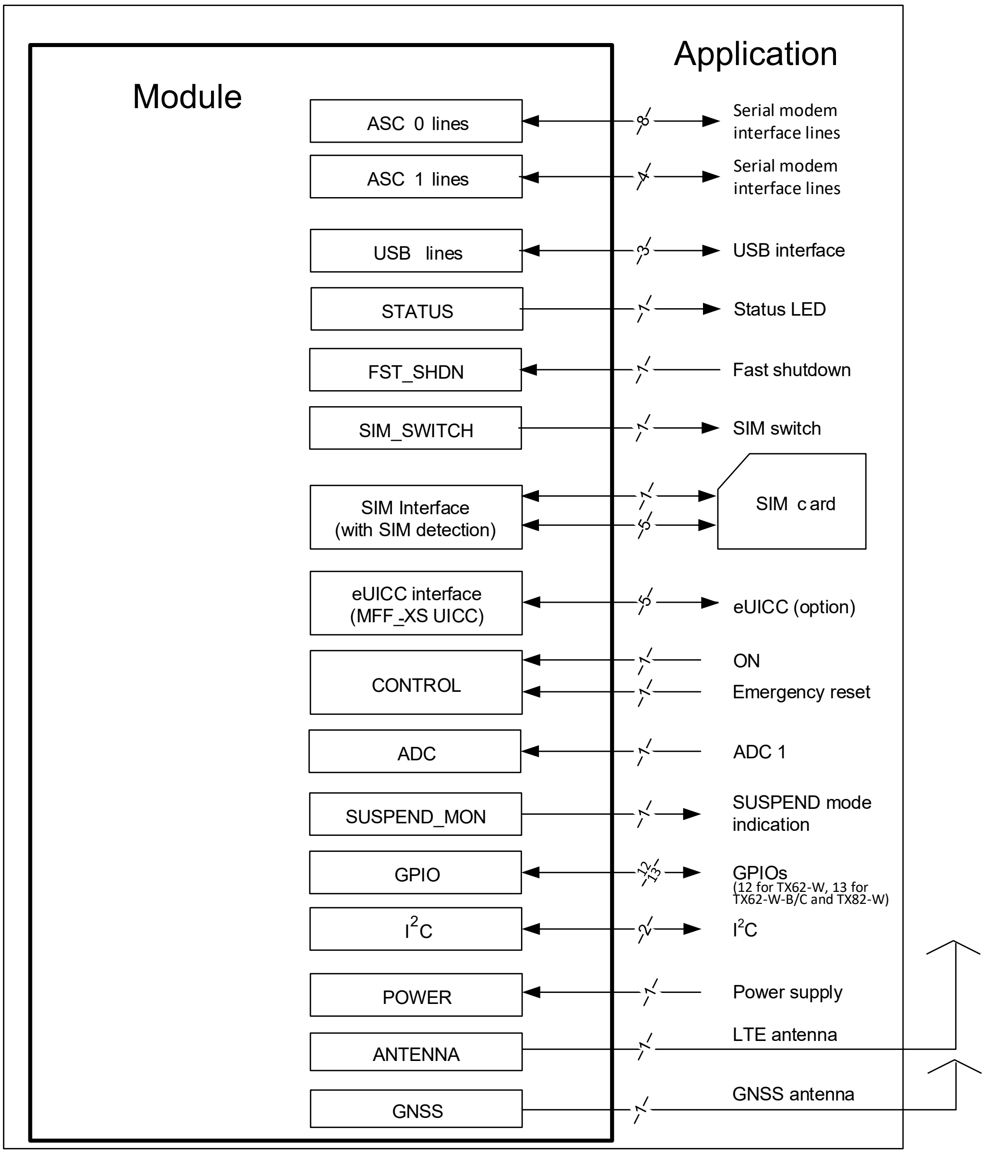


Figure 1: TX62/TX82 system overview

Please note that the I^{2}C function and some GPIO lines are available with the embedded processing option only. Also, some GPIO lines may be shared with further functions that are also only available with the embedded processing option. For details see Section 3.1, and Section 3.1.9.1.

2.9 Circuit Concept

Figure 2, Figure 3, Figure 4, Figure 5 and Figure 6 show block diagrams for the Cinterion® TX62/TX82 module variants, and illustrate the major functional components:

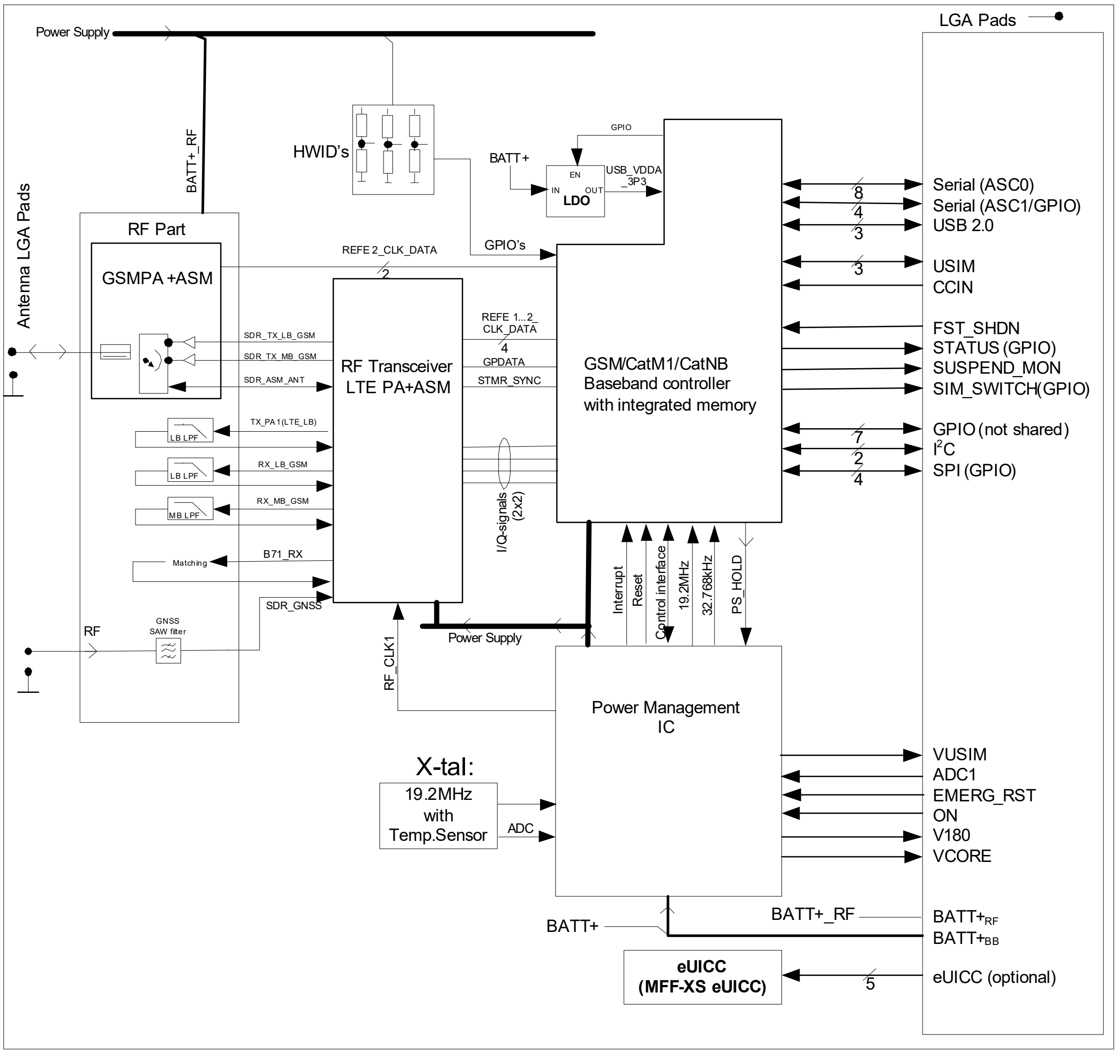


Figure 2: TX82-W block diagram

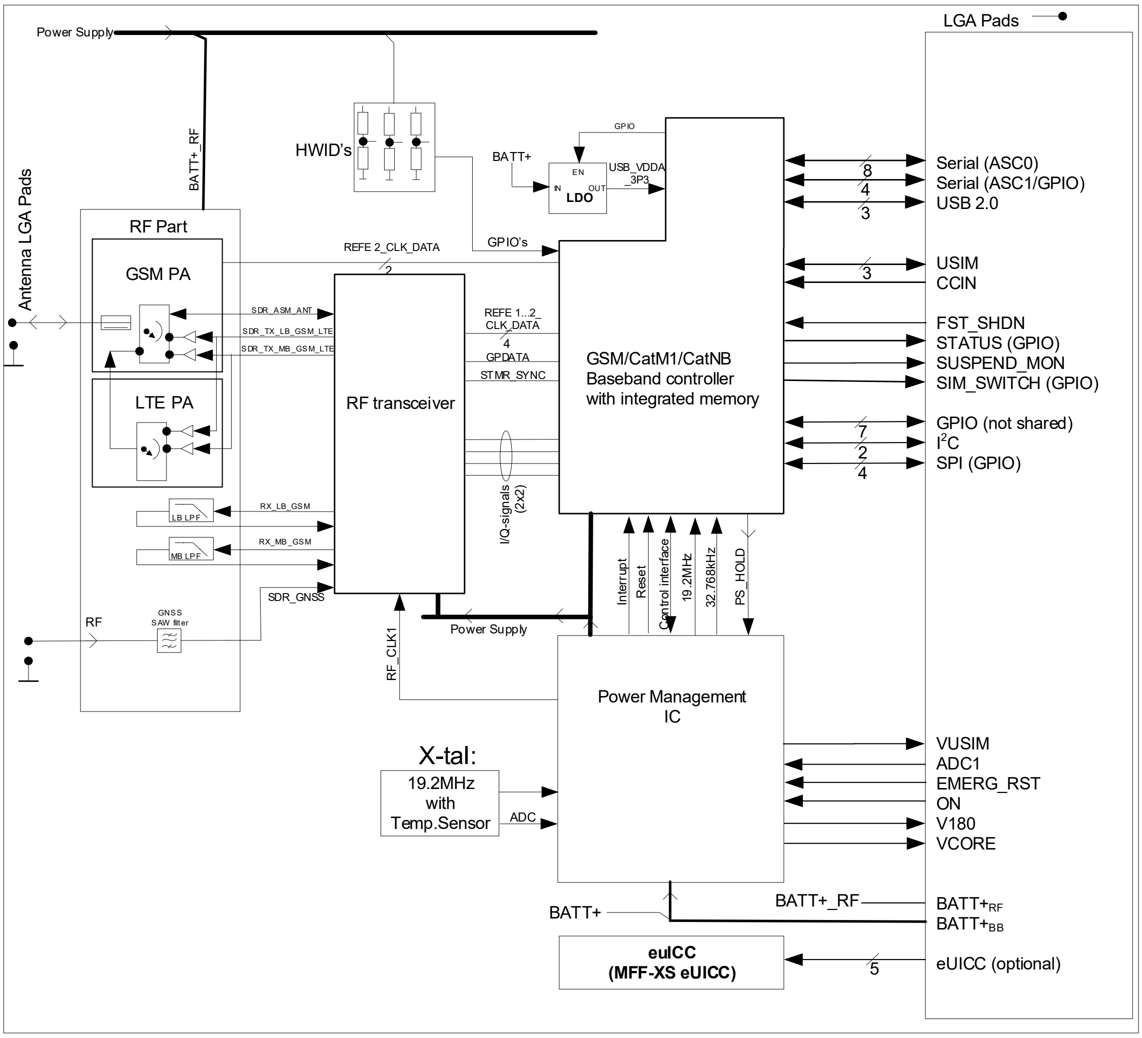


Figure 3: TX82-W-B block diagram

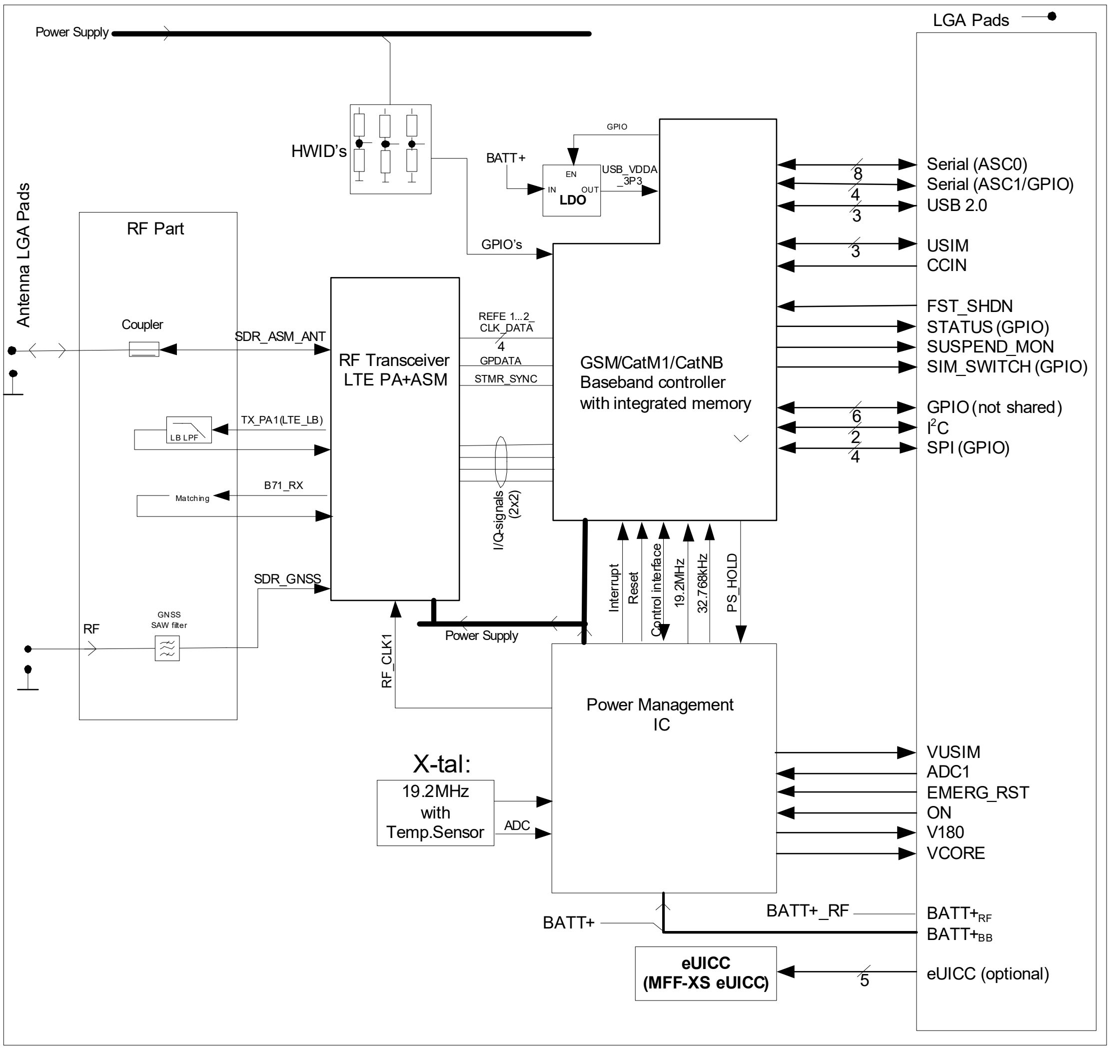


Figure 4: TX62-W block diagram

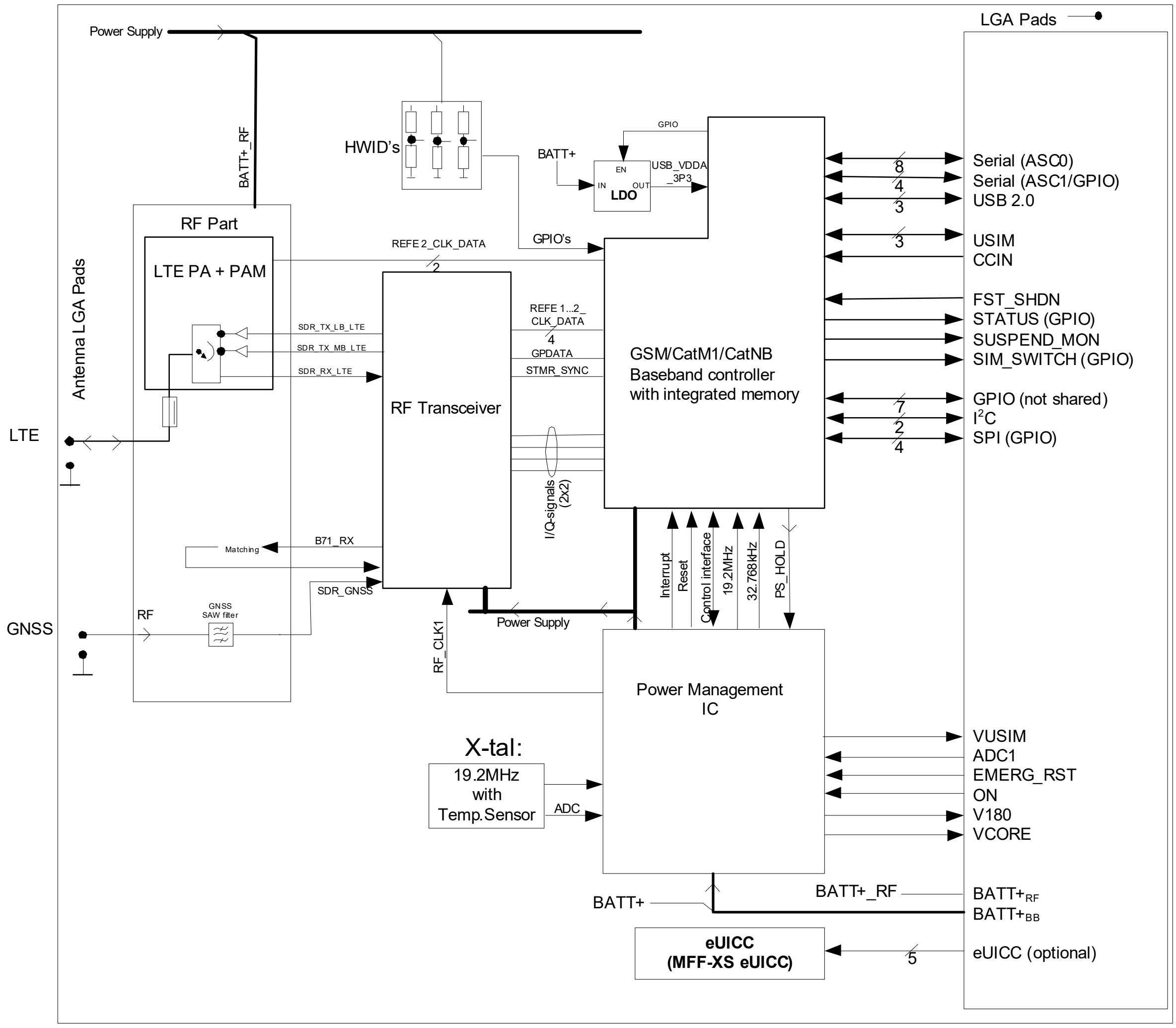


Figure 5: TX62-W-B block diagram

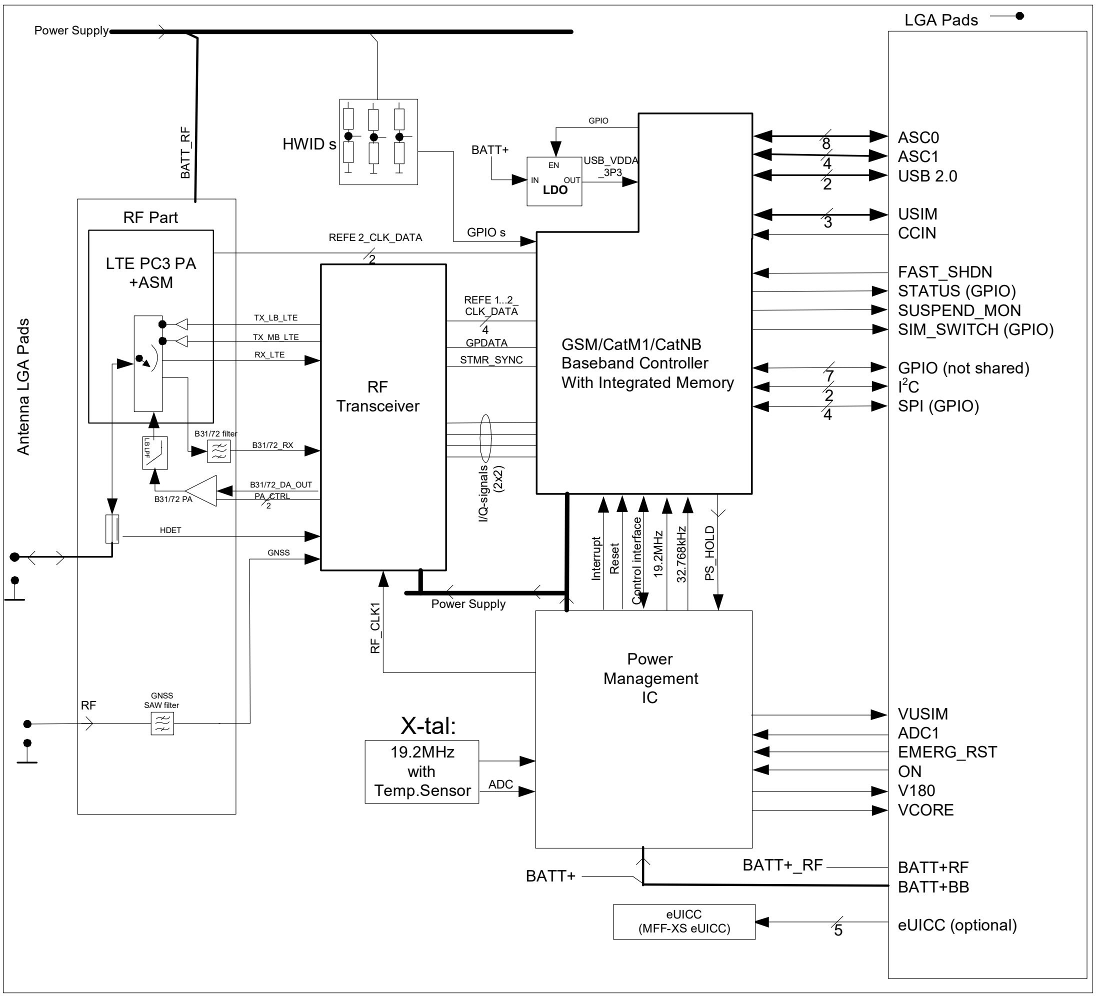


Figure 6: **TX62-W-C block diagram**

Note:

The I²C function and some GPIO lines are available with the embedded processing option only. Also, some GPIO lines may be shared with further functions that are also only available with the embedded processing option. For details see Section 3.1, and Section 3.1.9.1.

3 Interface Characteristics

Cinterion® TX62/TX82 is equipped with an SMT application interface that connects to the external application. The SMT application interface incorporates the various application interfaces as well as the RF antenna interface.

3.1 Application Interface

3.1.1 Pad Assignment

The SMT application interface on the Cinterion® TX62/TX82 provides connecting pads to integrate the module into external applications. The pads listed in Table 3 apply only to TX82-W, TX82-W-B, TX62-W-B and TX62-W-C. Table 4 lists the common pads of Cinterion® TX62/TX82. Figure 8 (bottom view) and Figure 7 (top view) show the connecting pads' numbering plan of TX62-W (pads inside dark violet rectangle) as well as TX82-W, TX82-W-B, TX62-W-B and TX62-W-C (pads inside light violet rectangle).

As a rule all pads should be soldered for mechanical stability and heat dissipation.

Signal pads that are not used, i.e., marked as “rfu” (reserved for future use) or “nc” (not connected), need to be soldered, but should not have an electrical connection to the external application or GND. Also, pads marked as “rfu” are further qualified as “dnu” (do not use), indicating that they are currently not supported, but internally connected for possible future usage. In addition, pads mentioned in squared brackets (I2CDAT and I2CCLK pads, SPI pads, as well as shared GPIO pads) are available with the embedded processing option only.

Note:

The reference voltages listed in Table 5 are the values measured directly on the Cinterion® TX62/TX82 module. They do not apply to the accessories connected.

Note:

Telit Cinterion strongly recommends to provide test points for certain signal lines to and from the module while developing SMT applications – for debug, test and/or trace purposes during the manufacturing process. In this way it is possible to detect soldering (and other) problems. Please refer to [5] and [6] for more information on test points and how to implement them. The signal lines for which test points should be provided for are marked as “Test point recommended“ in Table 5.

Table 3: **Overview: Pad assignments TX82-W, TX82-W-B, TX62-W-B and TX62-W-C additional pads**[1]

Pad no.	Signal name	Pad no.	Signal name	Pad no.	Signal name
B5	GND	E18	nc	L5	nc
B6	nc	E19	nc	L6	nc
B18	nc	G5	nc	L18	nc
B19	GND	G6	rfu (dnu)	L19	nc
C5	nc	G18	nc	M5	GND
C6	nc	G19	nc	M6	nc
C18	nc	J5	nc	M18	nc
C19	nc	J6	nc	M19	GND
E5	nc	J18	nc		
E6	GPIO6	J19	nc		

1. rfu = reserved for future use, i.e., currently not supported; dnu = do not use; nc = internally not connected

Table 4: **Overview: Pad assignments common to Cinterion® TX62/TX82[1] [2]**

Pad no.	Signal name	Pad no.	Signal name	Pad no.	Signal name
B7	GND	E12	rfu (dnu)	J15	V180
B8	GND	E13	rfu (dnu)	J16	GND
B9	GNSS_ANT	E14	GND	J17	CC2_VPP
B10	GND	E15	EMERG_RST	K7	nc
B11	GND	E16	VCORE	K8	RTS1 [GPIO18/SPI_CS]
B12	RF_OUT	E17	nc	K9	STATUS [GPIO5]
B13	GND	F7	GND	K10	FST_SHDN
B14	GND	F8	GPIO25	K11	nc
B15	rfu (dnu)	F9	SUSPEND_MON	K12	RING0
B16	GND	F10	GND	K13	DTR0
B17	GND	F11	rfu (dnu)	K14	DCD0
C7	rfu (dnu)	F12	rfu (dnu)	K15	CCCLK
C8	SIM_SWITCH [GPIO8]	F13	rfu (dnu)	K16	rfu (dnu)
C9	GND	F14	GND	K17	CC2_CLK
C10	GND	F15	ADC1	L7	nc
C11	GND	F16	nc	L8	TXD1 [GPIO17/MISO]
C12	GND	F17	GND	L9	VUSB_IN
C13	GND	G7	nc	L10	rfu (dnu)
C14	GND	G8	GPIO20	L11	DSR0
C15	GND	G9	nc	L12	RTS0
C16	GND	G10	GND	L13	CTS0
C17	nc	G14	GND	L14	CCVCC
D7	rfu (dnu)	G15	$BATT+_{RF}$[3]	L15	CCRST
D8	GPIO22	G16	$BATT+_{RF}$[3]	L16	CC2_VCC
D9	nc	G17	nc	L17	CC2_RST
D10	GND	H7	nc	M7	GND
D11	GND	H8	GPIO23	M8	GND
D12	GND	H9	[I2CDAT]	M9	USB_DP
D13	GND	H10	[I2CCLK]	M10	USB_DN
D14	GND	H14	GND	M11	GND
D15	ON	H15	$BATT+_{BB}$	M12	RXD0
D16	nc	H16	$BATT+_{BB}$	M13	TXD0
D17	nc	H17	nc	M14	CCIO
E7	GPIO7	J7	nc	M15	CCIN
E8	GPIO21	J8	CTS1 [GPIO19/SPI_CLK]	M16	CC2_IO
E9	rfu (dnu)	J9	RXD1 [GPIO16/MOSI]	M17	GND
E10	GND	J10	GND		
E11	rfu (dnu)	J14	rfu (dnu)		

1. rfu = reserved for future use, i.e., currently not supported; dnu = do not use; nc = internally not connected
2. Pads mentioned in squared brackets (I2CDAT and I2CCLK pads, SPI pads as well as shared GPIO pads) are available with the embedded processing option only.
3. Available with TX82-W, TX82-W-B, TX62-W-B, TX62-W-C. Internally not connected with TX62-W.

	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5
M	GND	nc	GND	CC2_IO	CCIN	CCIO	TXD0	RXD0	GND	USB_DN	USB_DP	GND	GND	nc	GND
L	nc	nc	CC2_RST	CC2_VCC	CCRST	CCVCC	CTS0	RST0	DSR0	rfu (dnu)	VUSB_IN	TXD1 [GPIO17/ MISO]	nc	nc	nc
K			CC2_CLK	rfu (dnu)	CCCLK	DCD0	DTR0	RING0	nc	FST_SHDN	STATUS [GPIO5]	RTS1 [GPIO18/ SPI_CS]	nc		
J	nc	nc	CC2_VPP	GND	V180	nc	Reserved for future use			GND	RXD1 [GPIO16/ MOSI]	CTS1 [GPIO19/ SPI_CLK]	nc	nc	nc
H			nc	$BATT+_{BB}$	$BATT+_{BB}$	GND				[I2CCLK]	[I2CDAT]	GPIO23	nc		
G	nc	nc	nc	$BATT+_{RF}$	$BATT+_{RF}$	GND	Marking			GND	nc	GPIO20	nc		
F			GND	nc	ADC1	GND	rfu (dnu)	rfu (dnu)	rfu (dnu)	GND	SUSPEND_MON	GPIO25	GND		
E	nc	nc	nc	VCORE	EMERG_RST	GND	rfu (dnu)	rfu (dnu)	rfu (dnu)	GND	rfu (dnu)	GPIO21	GPIO7	GPIO6	nc
D			nc	nc	ON	GND	GND	GND	GND	GND	nc	GPIO22	rfu (dnu)		
C	nc	nc	nc	GND	GND	GND	GND	GND	GND	GND	GND	SIM_SWITCH [GPIO8]	rfu (dnu)	nc	nc
B	GND	rfu (dnu)	GND	GND	nc	GND	GND	RF_OUT	GND	GND	GNSS_ANT	GND	GND	nc	GND

Figure 7: TX62/TX82 top view: Pad assignments

	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
M	GND	nc	GND	GND	USB_DP	USB_DN	GND	RXD0	TXD0	CCIO	CCIN	CC2_IO	GND	nc	GND
L	nc	nc	nc	TXD1 [GPIO17/ MISO]	VUSB _IN	rfu (dnu)	DSR0	RST0	CTS0	CCVCC	CCRST	CC2_VCC	CC2_RST	nc	nc
K			nc	RTS1 [GPIO18/ SPI_CS]	STATUS [GPIO5]	FST_SHDN	nc	RING0	DTR0	DCD0	CCCLK	rfu (dnu)	CC2_CLK		
J	nc	nc	nc	CTS1 [GPIO19/ SPI_CLK]	RXD1 [GPIO16/ MOSI]	GND	Reserved for future use			nc	V180	GND	CC2_VPP	nc	nc
H			nc	GPIO23	[I2CDAT]	[I2CCLK]				GND	$BATT+_{BB}$	$BATT+_{BB}$	nc		
G	nc	rfu (dnu)	nc	GPIO20	nc	GND	Marking			GND	$BATT+_{RF}$	$BATT+_{RF}$	nc		
F			GND	GPIO25	SUSPEND _MON	GND	rfu (dnu)	rfu (dnu)	rfu (dnu)	GND	ADC1	nc	GND		
E	nc	GPIO6	GPIO7	GPIO21	rfu (dnu)	GND	rfu (dnu)	rfu (dnu)	rfu (dnu)	GND	EMERG_ RST	VCORE	nc	nc	nc
D			rfu (dnu)	GPIO22	nc	GND	GND	GND	GND	GND	ON	nc	nc		
C	nc	nc	rfu (dnu)	SIM_SWITCH [GPIO8]	GND	GND	GND	GND	GND	GND	GND	GND	nc	nc	nc
B	GND	nc	GND	GND	GNSS _ANT	GND	GND	RF_OUT	GND	GND	nc	nc GND	GND	ADC2	GND

Figure 8: TX62/TX82 bottom view: Pad assignments

3.1.2 Signal Properties

Table 5: Signal properties

Function	Signal name	IO	Signal form and level	Comment
Power supply	$BATT+_{BB}$ $BATT+_{RF}$	I	Normal voltage range: TX82-W (LTE and GSM): $V_I$min = 3.1 V...$V_I$max = 4.6V during Tx burst on board Imax = 2A, during Tx burst (GSM) __|¯¯|___|¯¯|__ N Tx = n ∗ 577µs peak current every 4.616ms TX82-W (LTE with GSM deactivated): $V_I$min = 2.8 V...$V_I$max = 4.6V TX82-W-B (LTE and GSM): $V_I$min = 3.1 V...$V_I$max = 4.5V during Tx burst on board Imax = 2A, during Tx burst (GSM) __|¯¯|___|¯¯|__ N Tx = n ∗ 577µs peak current every 4.616ms TX82-W-B (LTE with GSM deactivated): $V_I$min = 2.9 V...$V_I$max = 4.5V TX62-W (LTE): $V_I$min = 2.55 V...$V_I$max = 4.8V TX62-W-B (LTE): $V_I$min = 2.5 V...$V_I$max = 4.5V TX62-W-C (LTE): $V_I$min = 3.2 V...$V_I$max = 4.2V All products: $V_I$norm = 3.8V $I_{Power Down}$ = 14µA Extended voltage range: All products: $V_I$min = 2.5 V...$V_I$max = 4.8V	Lines of BATT+ and GND must be connected in parallel for supply purposes because higher peak currents may occur. $BATT+_{BB}$ at solder pads needs an additional low ESR 47µF capacitor (e.g, X7R MLCC, taking DCbias into account). $BATT+_{RF}$ at solder pads needs an additional low ESR 150µF capacitor (e.g, X7R MLCC, taking DCbias into account) - except for TX62-W, as in this case BATT+RF is internally not connected. A minimum ESR value <70mΩ is recommended. Minimum voltage must not fall below the specified normal minimum voltage including drops, ripple, spikes. Else the module may perform an uncontrolled shutdown. However, if using the extended voltage range, i.e., down to 2.5V or up to 4.8V, the module remains fully functional and safe while possibly no longer being fully compliant with 3GPP or other wireless standards. Please note that the module is in this case switched on at a voltage of >2.65V. Please note that if both voltage domains and power supply lines are referred to - i.e., $BATT+_{BB}$ *and* $BATT+_{RF}$ - BATT+ is used throughout the document.
Power supply	GND		Ground	Application Ground

Table 5: **Signal properties**

Function	Signal name	IO	Signal form and level	Comment
External supply voltage	V180	O	Normal operation: $V_O$norm = 1.80V ±2% $I_O$max = 10mA SLEEP mode Operation: V_OSleep = 1.80V ±3.7% $I_O$max = 10mA SUSPEND mode Operation: V_OSuspend = 0V $C_I$max = 1µF	V180 has to be used for the power indication circuit. V180 can also be used to supply level shifters at the interfaces. Test point recommended[1].
	VCORE	O	Normal Operation: $V_O$nom = 1.128V ±2% $I_O$max = 10mA SLEEP Mode Operation: $V_O$sleep = 0.5V...1.304V ±3% $I_O$max = 10mA SUSPEND Mode Operation: $V_O$suspend = 0V $C_I$max = 100nF	Test point recommended.
Ignition	ON	I	V_{IH}max = BATT+$_{BB}$ V_{IH}min = 1.3V V_{IL}max = 0.5V High level pulse width > 30ms ON ___|‾‾‾	This signal switches the module on. The ON signal is low to high edge sensitive triggered, and requires a 10k pull down resistor. Test point recommended.
Status	STATUS	O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V	If unused keep lines open. With the embedded processing option this line is also available as GPIO: STATUS --> GPIO5
Fast shutdown	FST_SHDN	I	V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V C_{Imax} = 50pF	If unused keep lines open. Fast shutdown period <15ms.

Table 5: **Signal properties**

Function	Signal name	IO	Signal form and level	Comment
Emergency reset	EMERG_RST	I	$R_I \approx 1k\Omega$, $C_I \approx 1nF$ (internal low pass filter) $V_{IH}min = 1.3V$ $V_{IL}max = 0.5V$ at ~1µA ¯¯|__|¯¯ low impulse width > 800ms	This line must be driven low by an open drain or open collector driver connected to GND. If unused keep lines open. Test point recommended.
USB	VUSB_IN	I	$V_I min = 4V$ $V_I max = 5.25V$ Active and suspend current: $I_{max} < 100µA$	All electrical characteristics according to USB Implementers' Forum, USB 2.0 Specification. If unused keep lines open. Used for tracing purposes only. Test points recommended.
	USB_DN	I/O	Full and high speed signal characteristics according USB 2.0 Specification.	
	USB_DP			
Serial Modem Interface ASC0	RXD0	O	$V_{OL}max = 0.45V$ at I = 4.5 mA $V_{OH}min = 1.20V$ at I = 2.5 mA $V_{OH}max = 1.95V$	If unused keep lines open. RTS0 can be used to wakeup the module from SLEEP mode, but not from SUSPEND/PSM mode. Test points recommended for RXD0, TXD0, RTS0, and CTS0.
	CTS0	O		
	DSR0	O		
	DCD0	O		
	RING0	O		
	TXD0	I	$V_{IL}max = 0.5V$ $V_{IH}min = 1.3V$ $V_{IH}max = 1.95V$	
	RTS0	I		
	DTR0	I		
Serial Modem Interface ASC1	RXD1	O	$V_{OL}max = 0.45V$ at I = 4.5 mA $V_{OH}min = 1.20V$ at I = 2.5 mA $V_{OH}max = 1.95V$	If unused keep lines open. Test points recommended for RXD1, TXD1, RTS1, and CTS1. With embedded processing option ASC1 lines are shared with SPI interface lines, see Section 3.1.8.
	CTS1	O		
	TXD1	I	$V_{IL}max = 0.5V$ $V_{IH}min = 1.3V$ $V_{IH}max = 1.95V$	
	RTS1	I		

Table 5: Signal properties

Function	Signal name	IO	Signal form and level	Comment
SIM card detection	CCIN	I	Internal pull down resistor: 100k R_I » 110kΩ V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V	CCIN = High, SIM card inserted. If unused keep line open.
1.8V SIM Card Interface	CCVCC	O	$V_O$min = 1.5V $V_O$typ = 1.8V $V_O$max = 2V $I_O$max = -60mA	Maximum cable length or copper track to SIM card holder should not exceed 100mm. For more information on how to connect the SIM interface pads including possible external capacitors and ESD protection please refer to Section 3.1.6.
	CCRST CCCLK	O O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V	
	CCIO	I/O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V	

Table 5: **Signal properties**

Function	Signal name	IO	Signal form and level	Comment
1.8V eUICC interface	CC2_VPP	--	Used for single wire protocol (SWP NFC) in MFF-XS eUICC.	SWP NFC is currently not supported and deactivated for the eUICC. Thus, there are two options: If an external SWP master is connected nevertheless (or for future use) the CC2_VPP line should be pulled up by an external 10k resistor to VCC. If there is no plan to use SWP the CC2_VPP line can be grounded.
	CC2_VCC	I	$V_I$min = 1.62V $V_I$typ = 1.8V $V_I$max = 1.98V	Maximum cable length or copper track should be no longer as 100mm to eUICC interface. The signals CC2_RST, CC2_IO, CC2_CLK and CC2_VCC are protected against ESD with a special diode array. If unused keep lines open.
	CC2_CLK	I	V_{IL}max = 0.2*CC2_VCC (at I_{OL}max = -200µA) V_{IL}min = -0.3V (at I_{OL} max = -200µA) V_{IH}max=CC2_VCC+0.3V (at I_{OH}max = +20µA) V_{IH}min = 0.8*CC2_VCC (at I_{OH}max = +20µA)	
	CC2_RST	I	V_{IL}max = 0.2*CC2_VCC (at I_{OL}max = -20µA) V_{IL}min = -0.3V (at I_{OL} max = -20µA) V_{IH}max=CC2_VCC+0.3V (at I_{OH}max = +20µA) V_{IH}min = 0.7*CC2_VCC (at I_{OH}max = +20µA)	
	CC2_IO	I/O	V_{IL}max = 0.2*CC2_VCC (at I_{IH} = +1mA/ +20µA) V_{IL}min = -0.3V (at I_{IH} = +1mA/+20 µA) V_{IH}min = 0.7*CC2_VCC (at I_{IH} = -20/ +20µA) V_{IH}max = CC2_VCC+0.3V (at I_{IH} = -20/ +20µA) V_{OL}max = 0.15*CC2_VCC (at I_{OL} = -1mA) V_{OH}min = 0.7*CC2_VCC (at I_{IH} = -20/ +20µA) V_{OH}max = CC2_VCC+0.3V (at I_{IH} = -20/+20µA)	
SIM switch	SIM_SWITCH	O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V	If unused keep lines open. With embedded processing option SIM_SWITCH is shared with GPIO8, see Section 3.1.8.

Table 5: Signal properties

Function	Signal name	IO	Signal form and level	Comment
I^2C	I2CDAT	I/O	No internal pull up resistors V_{OL}max = 0.45V at Imax = -4.5mA V_{OH}max = 1.95V V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V Note: Imax = I max external + I pull up	If unused keep lines open. Compatible with I^2C Bus Specification Version 5.0. Multimaster is not supported. The value of the pull-up depends on the capacitive load of the whole system (I^2C Slave + lines). Only available with embedded processing option.
	I2CCLK	O		
GPIO	GPIO6-GPIO7, GPIO20-GPIO23, GPIO25	I/O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V	If unused keep lines open. GPIO6 only available on TX82-W, TX82-W-B, TX62-W-B and TX62-W-C. Further GPIOs shared with other functions are available with embedded processing option (see Section 3.1.8).
SPI	SPI_CLK	O	V_{OL}max = 0.45V at I = 4.5mA V_{OH}min = 1.20V at I = 2.5mA V_{OH}max = 1.95V V_{IL}max = 0.5V V_{IH}min = 1.3V V_{IH}max = 1.95V	Shared with ASC1 function (see Section 3.1.8). Only available with embedded processing option.
	SPI_MOSI	O		
	SPI_MISO	I		
	SPI_CS	O		
ADC (Analog-to-Digital Converter)	ADC1	I	R_I = 10MΩ V_I = 0.1V ... 1.875V (valid range) V_{IH} max = 1.910V Resolution 64.979uV	If unused keep line open.
SUSPEND mode indicator	SUSPEND_ MON	O	V_{OL}max = 0.45V at I = 4.5 mA V_{OH}min = 1.20V at I = 2.5 mA V_{OH}max = 1.95V	High=Normal mode, Low=SUSPEND mode. If unused keep lines open.

1. Telit Cinterion strongly recommends to provide test points for certain signal lines to and from the module while developing SMT applications – for debug, test and/or trace purposes during the manufacturing process. In this way it is possible to detect soldering (and other) problems. Please refer to [5] and [6] for more information on test points and how to implement them. The signal lines for which test points should be provided for are marked as "Test point recommended" in the above table.

3.1.2.1 Absolute Maximum Ratings

The absolute maximum ratings stated in Table 5 are stress ratings under any conditions. Stresses beyond any of these limits will cause permanent damage to TX62/TX82.

Table 6: Absolute maximum ratings

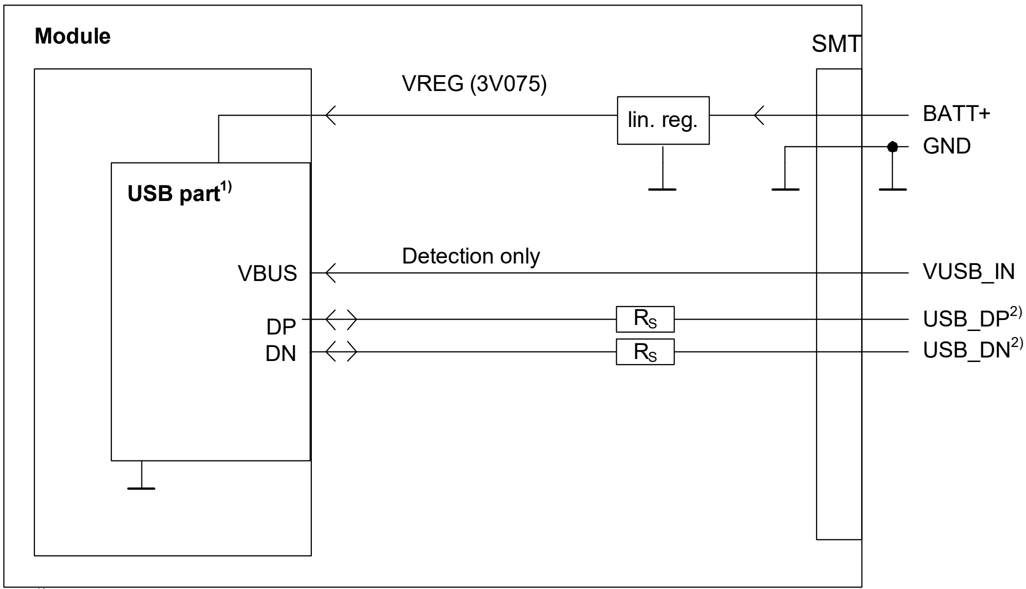
Parameter		Min	Max	Unit
Supply voltage $BATT+_{BB}$ (no service)		-0.5	+6.0	V
Supply voltage $BATT+_{RF}$ (not available with TX62-W)	(TX82-W; no service)	-0.5	+6.0	V
	(TX82-W-B; no service)	-0.5	+5.0	V
	(TX62-W-B; no service)	-0.5	+6.0	V
	(TX62-W-C; no service)	-0.5	+5.0	V
Voltage at all digital lines in Power Down mode		-0.5	+0.5	V
Voltage at digital lines 1.8V domain in normal operation[1]		-0.3	+2.09	V
Current at digital lines in normal operation		-5	+5	mA
Voltage at SIM interface, CCVCC 1.8V in normal operation		-0.3	+2.0	V
Current at SIM interface in normal 1.8V operation		-	-600	mA
Voltage at ADC line in normal operation		-0.5	+1.910	V
V180 in normal operation		-0.3	+2.09	V
Current at V180 in normal operation		-	-600	mA
VCORE in normal operation		+0.5	+1.304	V
Current at VCORE in normal operation		-	-1200	mA
Voltage at USB lines		-0.5	5.75	V

1. A maximum rating of 1.95V (for V_{IH}max) is recommended for all digital lines. Exceeding this value however will not necessarily harm the module as long as the rating remains below the absolute maximum rating of 1.95+0.14V, but it will decrease the safety margin in case of short spikes or ripple.

3.1.3 USB Interface

Cinterion® TX62/TX82 supports a USB 2.0 High Speed (480Mbit/s) device interface that is Full Speed (12Mbit/s) compliant.

The external application is responsible for supplying the VUSB_IN line. This line is used for cable detection only. The USB part (driver and transceiver) is supplied by means of BATT+. This is because Cinterion® TX62/TX82 is designed as a self-powered device compliant with the "Universal Serial Bus Specification Revision 2.0"[2].



1) All serial (including R_S) and pull-up resistors for data lines are implemented.
2) If the USB interface is operated in High Speed mode (480MHz), it is recommended to take special care routing the data lines USB_DP and USB_DN. Application layout should in this case implement a differential impedance of 90 ohms for proper signal integrity.

Figure 9: USB circuit

To properly connect the module's USB interface to the external application, a USB 2.0 compatible connector and cable or hardware design is required. For more information on the USB related signals see Table 5. Furthermore, the USB modem driver distributed with Cinterion® TX62/TX82 needs to be installed.

2. The specification is ready for download on https://www.usb.org/document-library/usb-20-specification

3.1.4 Serial Interface ASC0

Cinterion® TX62/TX82 offers an 8-wire unbalanced, asynchronous modem interface ASC0 conforming to ITU-T V.24 protocol DCE signaling. The electrical characteristics do not comply with ITU-T V.28. The significant levels are 0V (for low data bit or active state) and 1.8V (for high data bit or inactive state). For electrical characteristics please refer to Table 5. For an illustration of the interface line's startup behavior see Figure 11.

Cinterion® TX62/TX82 is designed for use as a DCE. Based on the conventions for DCE-DTE connections it communicates with the customer application (DTE) using the following signals:

- Port TXD @ application sends data to the module's TXD0 signal line
- Port RXD @ application receives data from the module's RXD0 signal line

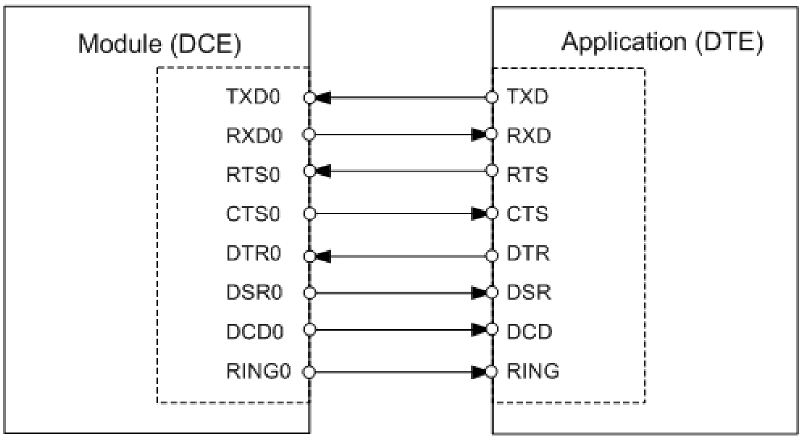
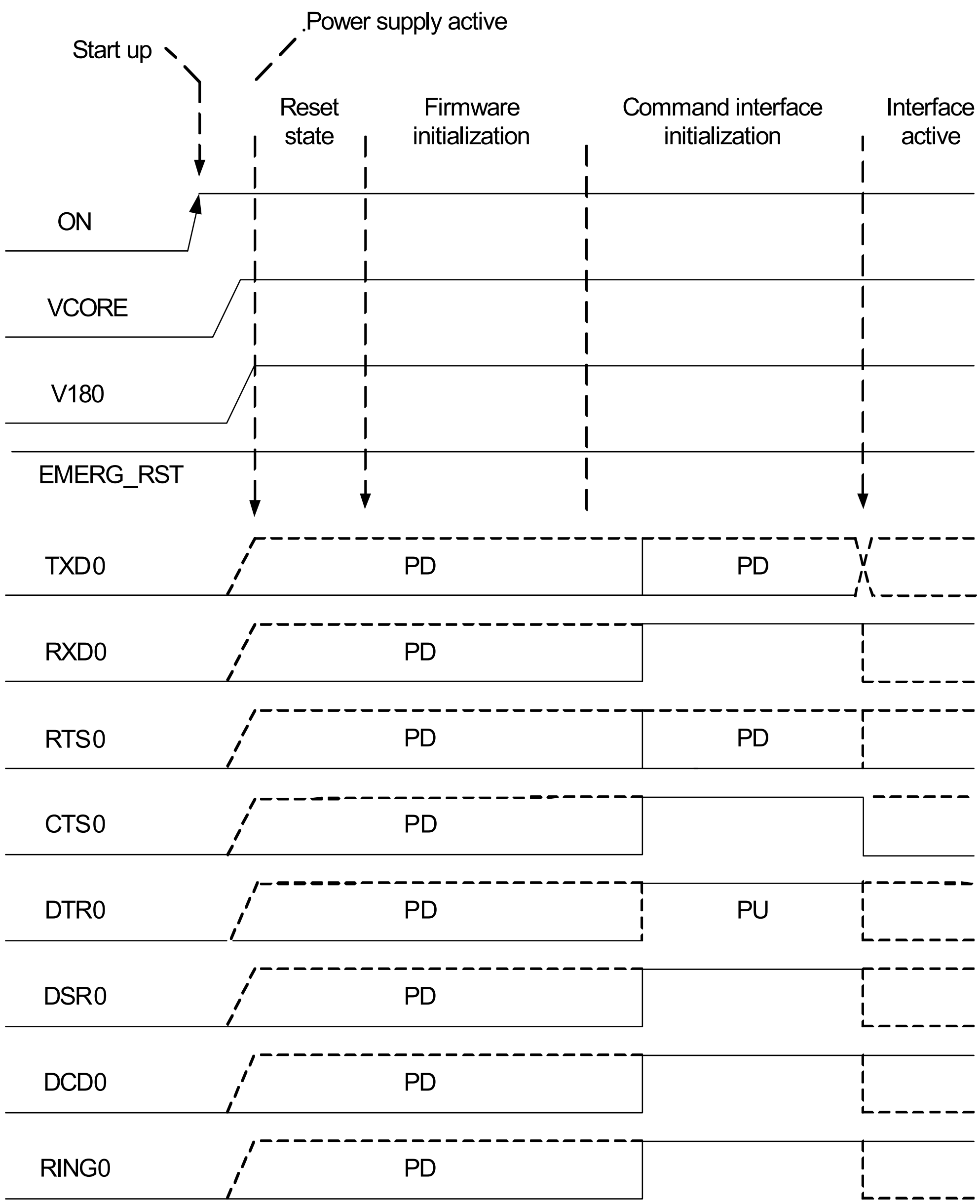


Figure 10: **Serial interface ASC0**

Features:

- Includes the data lines TXD0 and RXD0, the status lines RTS0 and CTS0 and, in addition, the modem control lines DTR0, DSR0, DCD0 and RING0.
- The RING0 signal serves to indicate incoming calls and other types of URCs (Unsolicited Result Code). It can also be used to send pulses to the host application, for example to wake up the application from power saving state.
- By default configured to 8 data bits, no parity and 1 stop bit.
- ASC0 can be operated at fixed bit rates from 300bps up to 921,600bps.
- Supports RTS0/CTS0 hardware flow control as a configuration option (see [1]). The hardware hand shake line RTS0 has an internal pull down resistor causing a low level signal, if the line is not used and open. Although hardware flow control is recommended, this allows communication by using only RXD and TXD lines.
- Wake up from SLEEP mode by RTS0 activation (high to low transition; see Section 4.3.1.1).

The following figure shows the startup behavior of the asynchronous serial interface ASC0:



Dotted lines indicate possible alternative signal states - depending on externally provided signal states.
For pull-up and pull-down values see Table 19.

Figure 11: ASC0 startup behavior

3.1.5 Serial Interface ASC1

Cinterion® TX62/TX82 provides a 4-wire unbalanced, asynchronous modem interface ASC1 conforming to ITU-T V.24 protocol DCE signaling. The electrical characteristics do not comply with ITU-T V.28. The significant levels are 0V (for low data bit or active state) and 1.8V (for high data bit or inactive state). For electrical characteristics please refer to Table 5. For an illustration of the interface line's startup behavior see Figure 13.

Cinterion® TX62/TX82 is designed for use as a DCE. Based on the conventions for DCE-DTE connections it communicates with the customer application (DTE) using the following signals:

- Port TXD @ application sends data to module's TXD1 signal line
- Port RXD @ application receives data from the module's RXD1 signal line

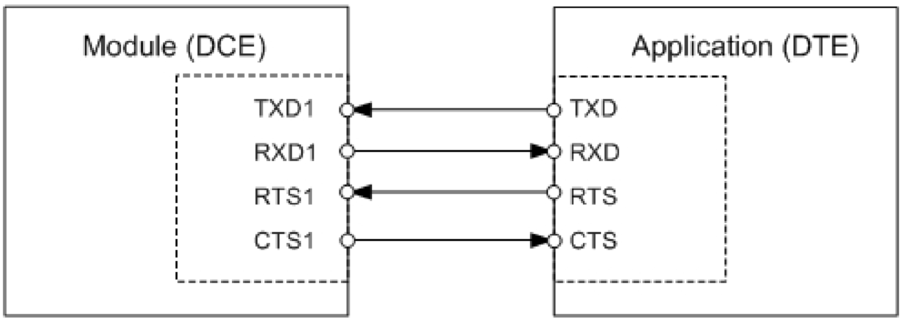


Figure 12: **Serial interface ASC1**

Features

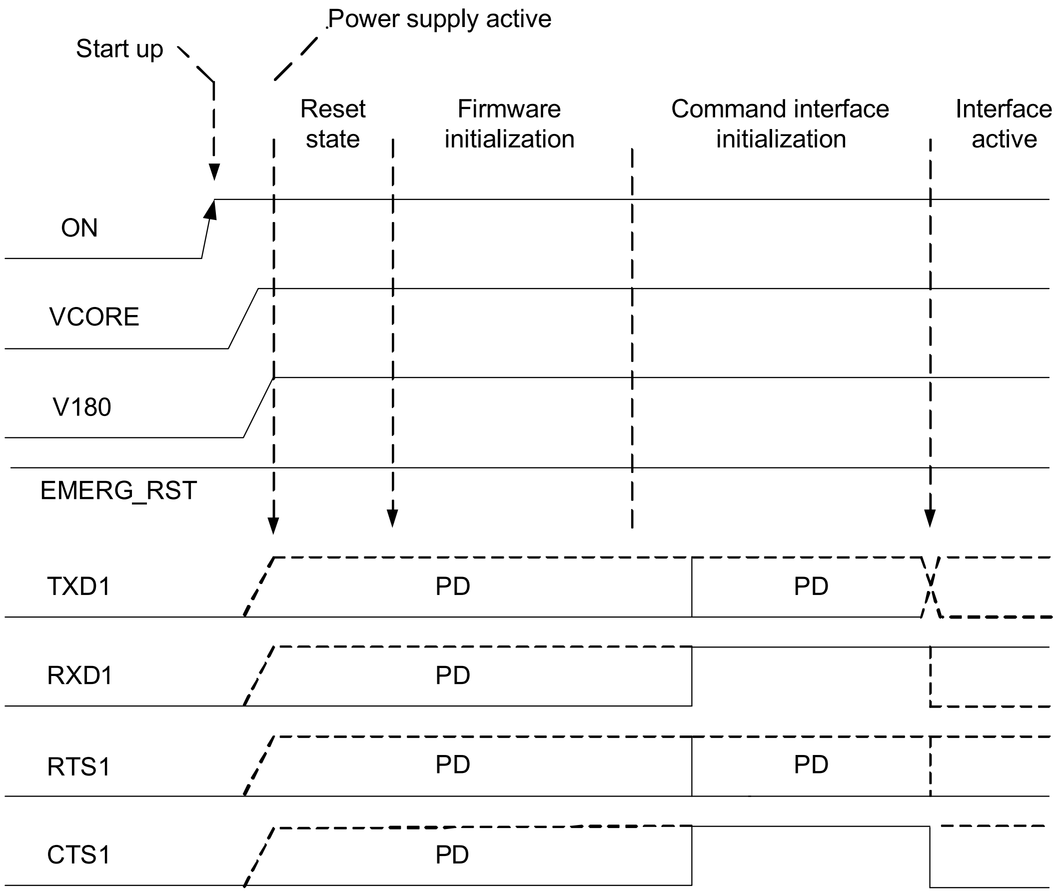
Includes only the data lines TXD1 and RXD1 plus RTS1 and CTS1 for hardware handshake.

Configured for 8 data bits, no parity and 1 or 2 stop bits.

ASC1 can be operated at fixed bit rates from 300bps to 921,600bps.

Supports RTS1/CTS1 hardware flow as a configuration option (see [1]). The hardware hand shake line RTS0 has an internal pull down resistor causing a low level signal, if the line is not used and open. Although hardware flow control is recommended, this allows communication by using only RXD and TXD lines.

The following figure shows the startup behavior of the asynchronous serial interface ASC1.



Dotted lines indicate possible alternative signal states - depending on externally provided signal states.
*) For pull-down values see Table 19.

Figure 13: ASC1 startup behavior

3.1.6 UICC/SIM/USIM Interface

Cinterion® TX62/TX82 has an integrated UICC/SIM/USIM interface compatible with the 3GPP 31.102 and ETSI 102 221. This is wired to the host interface in order to be connected to an external SIM card holder. Five pads on the SMT application interface are reserved for the SIM interface.

The UICC/SIM/USIM interface supports 1.8V SIM cards. Please refer to Table 5 for electrical specifications of the UICC/SIM/USIM interface lines.

The CCIN signal serves to detect whether a tray (with SIM card) is present in the card holder. Using the CCIN signal is mandatory for compliance with the GSM 11.11 recommendation if the mechanical design of the host application allows the user to remove the SIM card during operation. To take advantage of this feature, an appropriate SIM card detect switch is required on the card holder. For example, this is true for the model supplied by Molex, which has been tested to operate with Cinterion® TX62/TX82 and is part of the Telit Cinterion reference equipment submitted for type approval. See Section 8.1 for Molex ordering numbers.

Table 7: Signals of the SIM interface (SMT application interface)

Signal	Description
GND	Separate ground connection for SIM card to improve EMC. Telit Cinterion recommends to use pad J16 or pad M17 as ground connection.
CCCLK	UICC clock
CCVCC	SIM supply voltage.
CCIO	Serial data line, input and output.
CCRST	UICC reset
CCIN	Input on the baseband processor for detecting a SIM card tray in the holder. If the SIM is removed during operation the SIM interface is shut down immediately to prevent destruction of the SIM. The CCIN signal is by default low and must change to high level if a SIM card is inserted. The CCIN signal is mandatory for applications that allow the user to remove the SIM card during operation. The CCIN signal is solely intended for use with a SIM card. It must not be used for any other purposes. Failure to comply with this requirement may invalidate the type approval of Cinterion® TX62/TX82.

Note:

No guarantee can be given, nor any liability accepted, if loss of data is encountered after removing the SIM card during operation. Also, no guarantee can be given for properly initializing any SIM card that the user inserts after having removed the SIM card during operation. In this case, the application must restart Cinterion® TX62/TX82.

The figure below shows a circuit to connect an external SIM card holder.

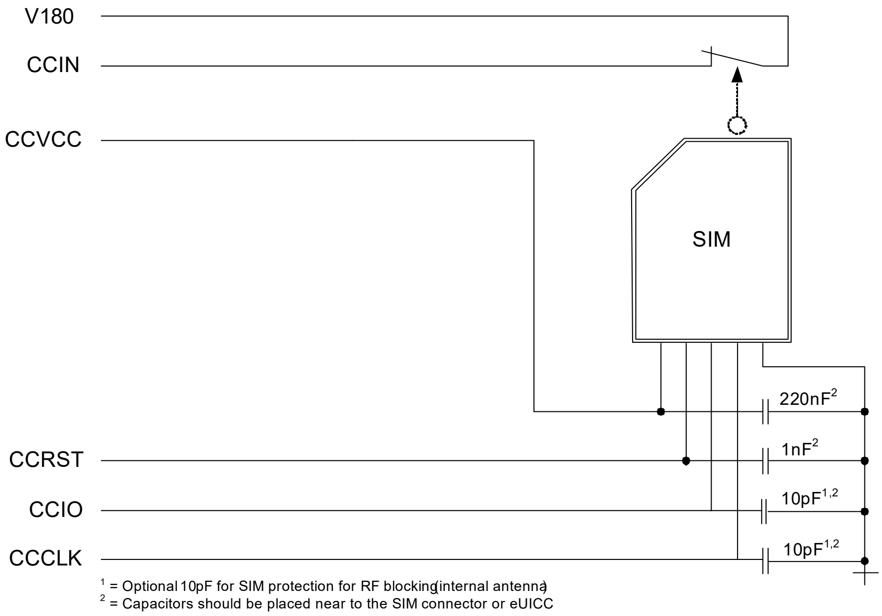


Figure 14: External UICC/SIM/USIM card holder circuit

The total cable length between the SMT application interface pads on Cinterion® TX62/TX82 and the pads of the external SIM card holder must not exceed 100mm in order to meet the specifications of 3GPP TS 51.010-1 and to satisfy the requirements of EMC compliance.

To avoid possible cross-talk from the CCCLK signal to the CCIO signal be careful that both lines are not placed closely next to each other. A useful approach is using a GND line to shield the CCIO line from the CCCLK line.

An example for an optimized ESD protection for the SIM interface is shown in Section 3.1.7.

It is possible to connect the UICC/USIM/SIM interface lines to an external SIM card multiplexer controlled by the module's SIM_SWITCH signal. Thus, it becomes possible to switch between two networks/subscriptions each with its own UICC, and maybe different connection speeds. See also Section 3.1.12.4.

3.1.7 Enhanced ESD Protection for SIM Interface

To optimize ESD protection for the SIM interface it is possible to add ESD diodes (e.g., NUP4114) to the SIM interface lines as shown in the example given in Figure 15.

The example was designed to meet ESD protection according ETSI EN 301 489-1/7: Contact discharge: ± 4kV, air discharge: ± 8kV.

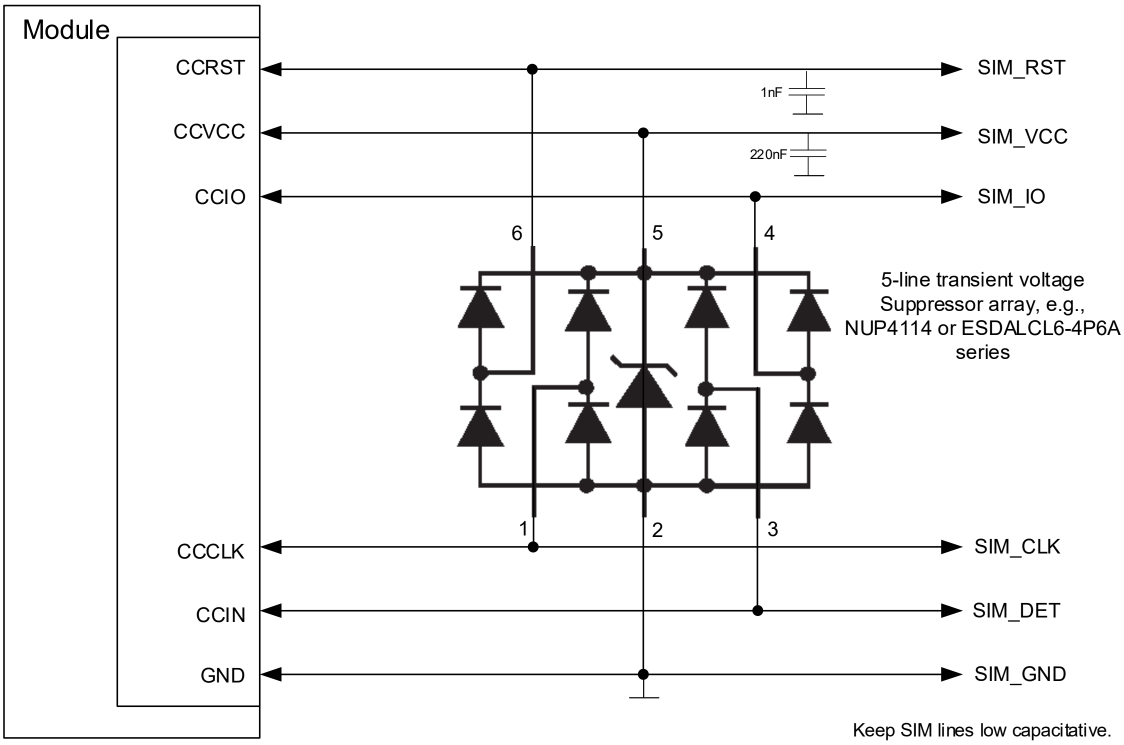


Figure 15: **SIM interface - enhanced ESD protection**

The capacitors shown in Figure 14 must be placed close to the SIM Connector.

3.1.8 eUICC Interface

As an option Cinterion® TX62/TX82 supports an eUICC in MFF-XS format. This MFF-XS eUICC is located under the shielding, is only connected to specific module pads, and has no physical connections with other circuits inside the module. Figure 16 shows an example of how to connect the eUICC to the module's SIM interface lines as well as a switch to select whether to use the internal MFF-XS eUICC or an external plug-in SIM card. Figure 17 shows an example for a direct connection to the module's SIM interface lines.

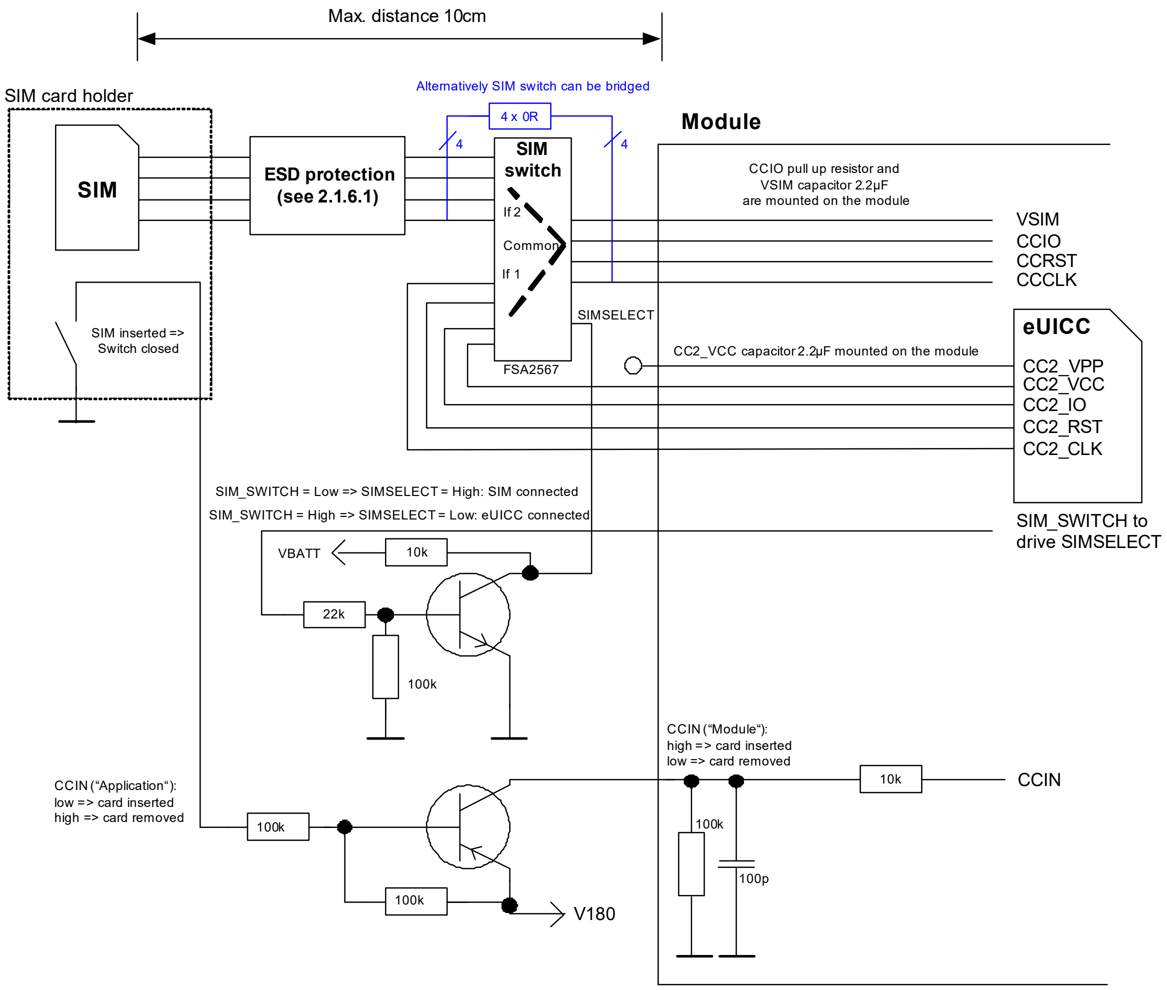


Figure 16: eUICC interface with switch for external SIM

The eUICC interface comprises five lines (plus ground) as listed below in Table 8.

Table 8: Signals of the eUICC interface option (SMT application interface)

Signal	Description
CC2_RST	Chip Card Reset
CC2_CLK	Chip Card Clock
CC2_IO	Chip Card I/O (data line)
CC2_VPP	-

Table 8: Signals of the eUICC interface option (SMT application interface)

Signal	Description
CC2_VCC	Operation voltage for SIM Card (=1.8V)
GND	eUICC Ground

If using a SIM switch to change between usage of an external SIM and an eUICC as shown in Figure 16, the module needs to be prepared for this dual mode by AT command.

By default, dual mode is disabled, and usage of the first SIM slot, i.e., the external SIM interface, is configured - as shown by the following AT command result:

```
AT^SCFG?
…
^SCFG: "SIM/CS","0"
^SCFG: "SIM/DualMode","0"
…
OK
```

To configure and use the eUICC, dual mode has to be enabled, and usage of the second SIM slot, i.e., the eUICC, needs to be specified - as shown by the following AT command sequence:

```
AT^SCFG="SIM/DualMode","1"
^SCFG: "SIM/DualMode","1"

OK
at^scfg="SIM/CS","3"
^SCFG: "SIM/CS","3"

OK
```

For details on these AT commands please refer to [1].

Figure 17 shows a direct connection of the internal eUICC to the module’s SIM interface lines. In this case no dual mode is possible.

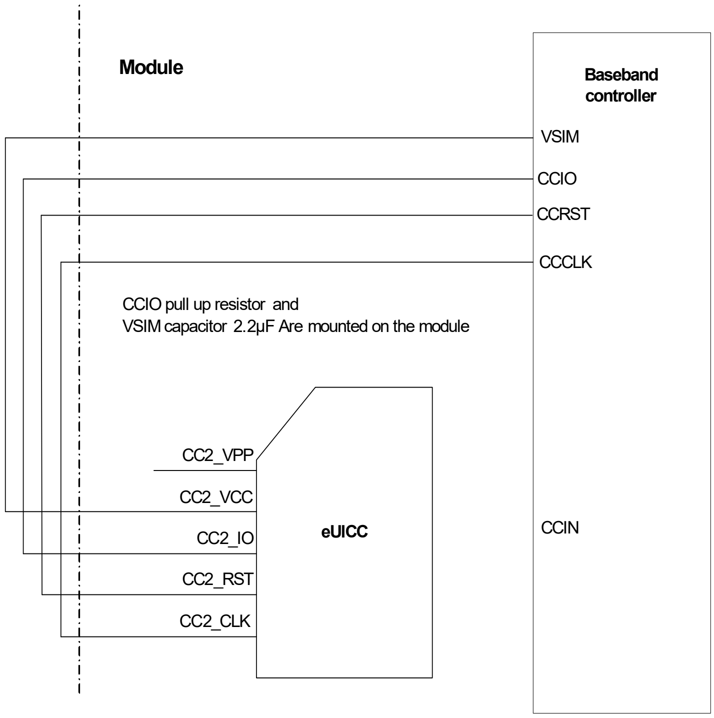


Figure 17: eUICC interface without SIM switch

In case the module is mounted onto the LGA DevKit, a direct connection to the eUICC can be achieved by bridging the following pads on the backside of the LGA DevKit: PAD 249 to 17, PAD 248 to 19, PAD 247 to 21, PAD 246 to 20, as also shown in Figure 18.

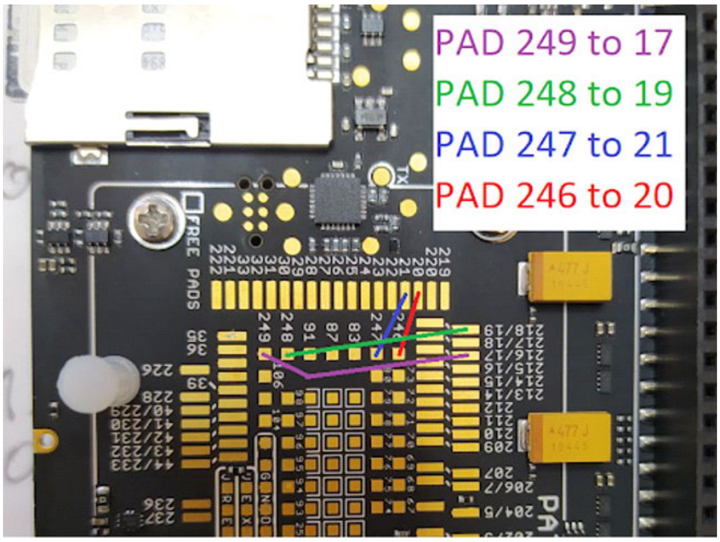


Figure 18: Interface bridging

3.1.9 GPIO

TX82-W, TX82-W-B, TX62-W-B and TX62-W-C have 7 GPIOs (GPIO6-7,20-23,25) and TX62-W has 6 GPIOs (GPIO7,20-23,25) for external hardware devices. Each GPIO can be configured for use as input or output. All settings are AT command controlled. The configuration is non-volatile and available after module restart.

The IO port driver has to be opened before using and configuring GPIOs. Before changing the configuration of a GPIO pin (e.g. input to output) the pin has to be closed. If the GPIO pins are not configured or the pins/driver were closed, the GPIO pins are high-Z with pull down resistor.

If a GPIO is configured to input, the pin has high-Z without pull resistor.

The following figure shows the start up behavior of the GPIOs interface.

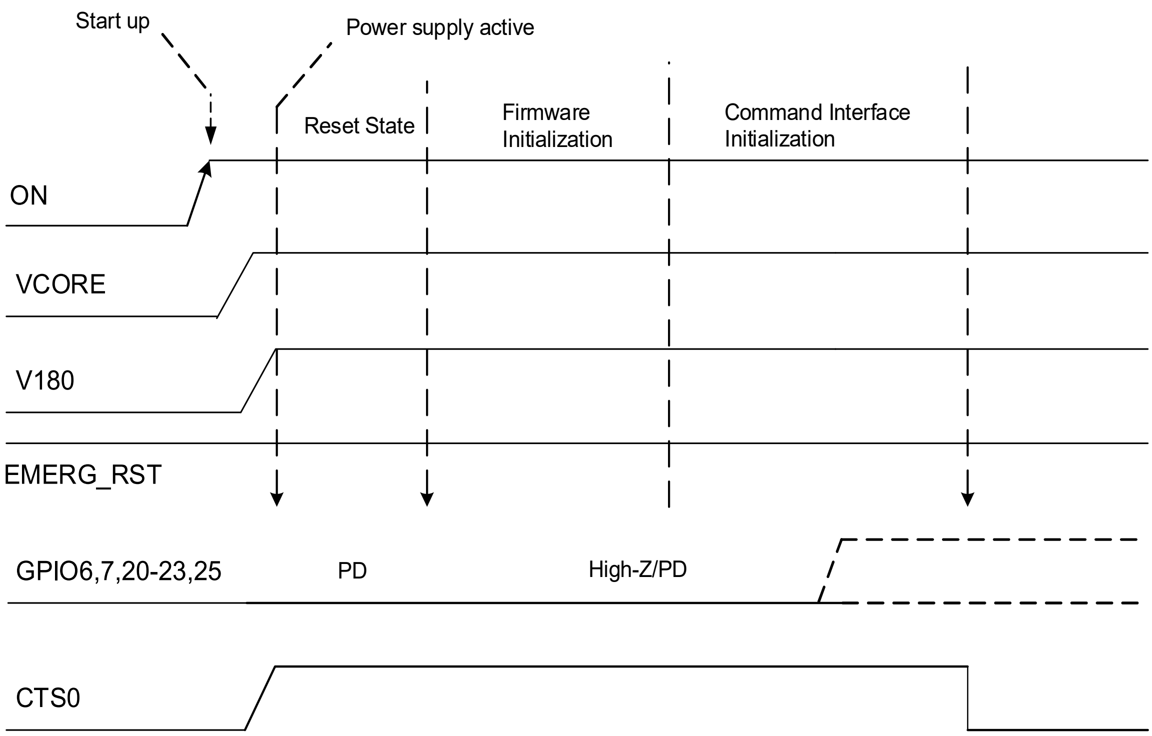


Figure 19: **GPIO start up behavior**

With the embedded processing option of Cinterion® TX62/TX82 additional GPIOs are provided and can be used - see below Section 3.1.9.1.

3.1.9.1 GPIOs Available with Embedded Processing Option

The embedded processing option of TX62/TX82 provides a GPIO interface with 13 configurable GPIO lines. Some GPIO lines are shared with other interfaces or functions, and are shown in the following table with their default assignments being marked green.

Table 9: **GPIO lines and alternative assignments**

GPIO	Status LED	ASC1	SPI	SIM Switch
GPIO5	STATUS			
GPIO6[1]				
GPIO7				
GPIO8				SIM_SWITCH
(GPIO16)[2]		RXD1	MOSI	
(GPIO17)[2]		TXD1	MISO	
(GPIO18)[2]		RTS1	SPI_CS	
(GPIO19)[2]		CTS1	SPI_CLK	
GPIO20				
GPIO21				
GPIO22				
GPIO23				
GPIO25				

1. Only available with TX82-W, TX82-W-B, TX62-W-B and TX62-W-C.
2. Not configurable as GPIO line with the embedded processing option.

After startup, the above mentioned alternative GPIO line assignments can be configured through embedded applications (see [9]), or in the case of GPIO5 and GPIO8 also through AT command. The configuration is non-volatile and available after module restart.

3.1.10 I2C Interface

The embedded processing option of Cinterion® TX62/TX82 provides an inter-integrated circuit interface. I²C is a serial, 8-bit oriented data transfer bus for bit rates up to 400kbps in Fast mode. It consists of two lines, the serial data line I2CDAT and the serial clock line I2CCLK. The module acts as a single master device, e.g. the clock I2CCLK is driven by the module. I2CDAT is a bi-directional line. Each device connected to the bus is software addressable by a unique 7-bit address, and simple master/slave relationships exist at all times. The module operates as master-transmitter or as master-receiver. The customer application transmits or receives data only on request of the module.

The I²C bus can be configured and activated via embedded application. For more information see [9].

The I²C interface can be powered via the V180 line of Cinterion® TX62/TX82. If connected to the V180 line, the I²C interface will properly shut down when the module enters the Power Down mode.

In the application I2CDAT and I2CCLK lines need to be connected to a positive supply voltage via a pull-up resistor. For electrical characteristics please refer to Table 5.

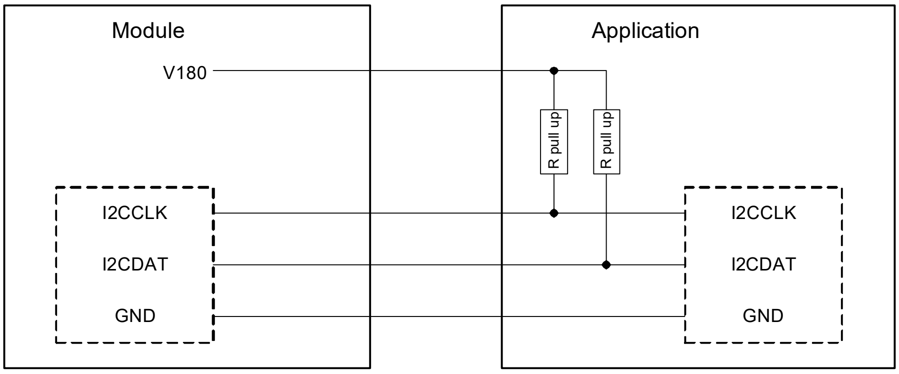


Figure 20: I²C interface connected to V180

Note:

Good care should be taken when creating the PCB layout of the host application: The traces of I2CCLK and I2CDAT should be equal in length and as short as possible.

3.1.11 SPI Interface

The embedded processing option of Cinterion® TX62/TX82 provides an SPI interface where four GPIO interface lines can be configured as Serial Peripheral Interface (SPI). The SPI is a synchronous serial interface allowing the module to control external sensors or components. The SPI interface supports only master mode. The transmission rates are up to 6.5Mbit/s. The SPI interface comprises the two data lines MOSI and MISO, the clock line SPI_CLK a well as the chip select line SPI_CS.

The GPIO lines are also shared with the ASC1 signal lines as shown in Section 3.1.9.1.

The SPI interface can be configured and activated via embedded application. For more information see [9].

In general, SPI supports four operation modes. The modes are different in clock phase and clock polarity. The module's SPI mode can be configured via embedded processing option. Make sure the module and the connected slave device works with the same SPI mode.

Figure 21 shows the characteristics of the four SPI modes. The SPI modes 0 and 3 are the most common used modes. For electrical characteristics please refer to Table 5.

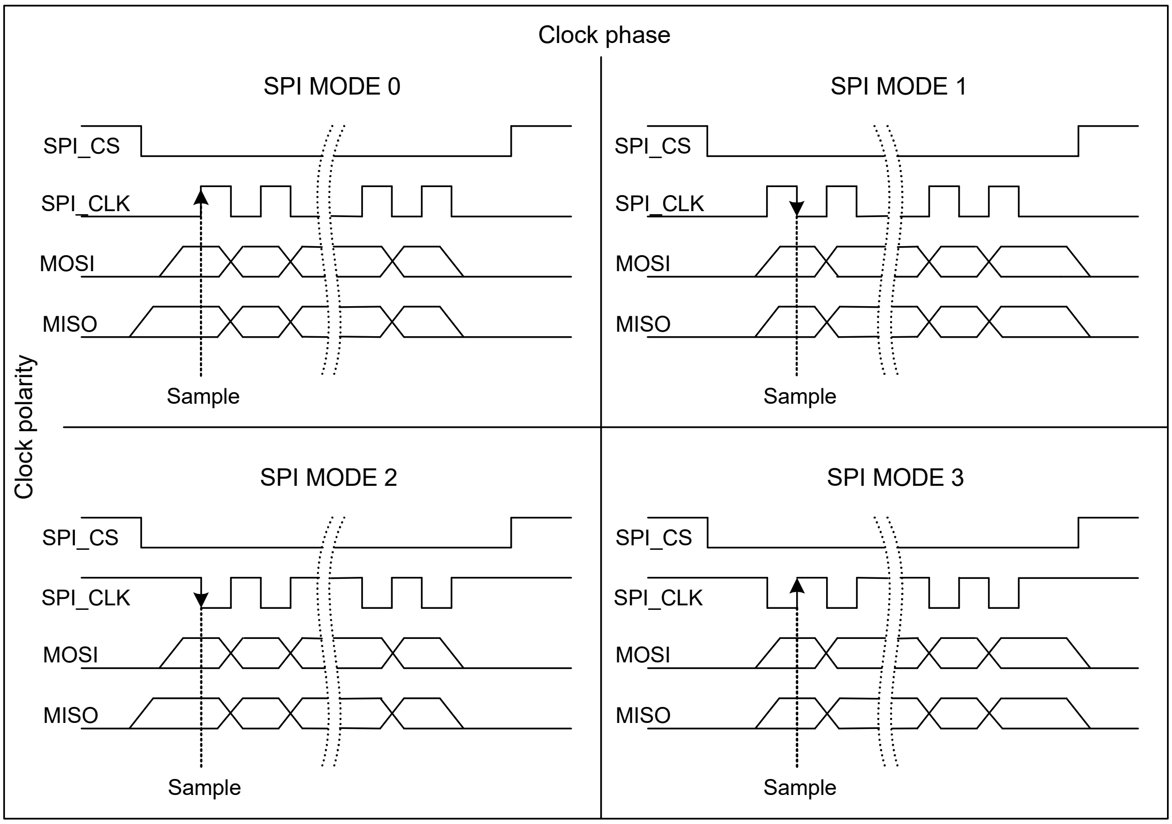


Figure 21: **Characteristics of SPI modes**

3.1.12 Control Signals

3.1.12.1 Status LED

The STATUS line can be configured to drive a status LED that indicates different operating modes of the module. For details on how to configure status signaling please refer to [1].

To take advantage of this function connect an LED to the STATUS line as shown in Figure 22. The sample circuit is not optimized for low current consumption.

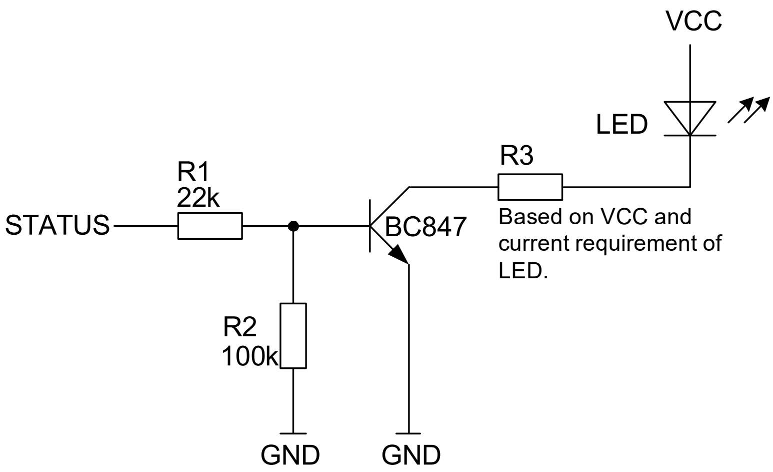


Figure 22: **Status signaling with LED driver**

3.1.12.2 Power Indication Circuit

In Power Down mode the maximum voltage at any digital or analog interface line must not exceed +0.3V (see also Section 3.1.2.1). Exceeding this limit for any length of time might cause permanent damage to the module.

It is therefore recommended to implement a power indication signal that reports the module's power state and shows whether it is active or in Power Down mode. While the module is in Power Down mode all signals with a high level from an external application need to be set to low state or high impedance state. The sample power indication circuit illustrated in Figure 23 denotes the module's active state with a low signal and the module's Power Down mode with a high signal or high impedance state. The sample circuit is not optimized for low current consumption.

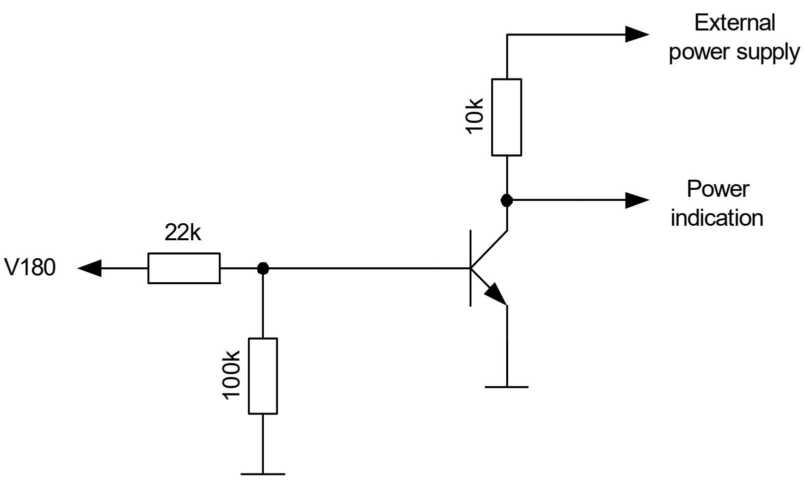


Figure 23: **Power indication circuit**

3.1.12.3 Fast Shutdown

The FST_SHDN line triggers the module's fast shutdown procedure. The fast shutdown procedure ensures data integrity during shutdown, but will no longer deregister gracefully from the network thus saving the time normally required for network deregistration. For fast shutdown behavior and timings see Figure 24 as well asTable 10.

The FST_SHDN line is an active low control signal and must be applied via an open drain or open collector circuit (see Table 5 for electrical characteristics). It is recommended to verify the behavior of the external application especially during the boot and initialization phase against back powering and unintentional fast shutdown.

It is recommended to keep the FST_SHDN line low until the module has shut down. A low level of the V180 signal indicates that the module has entered the Power Down mode. No shutdown URCs will be issued with a fast shutdown. Thus, it is recommended to monitor the V180 line by the external application. Other reference voltage lines may still show a high level.

Once V180 is low, the external application can cut the module's power supply.

In order not to trigger a restart loop of the module, the ON line shall be released by the external application, and not be actively driven continuously.

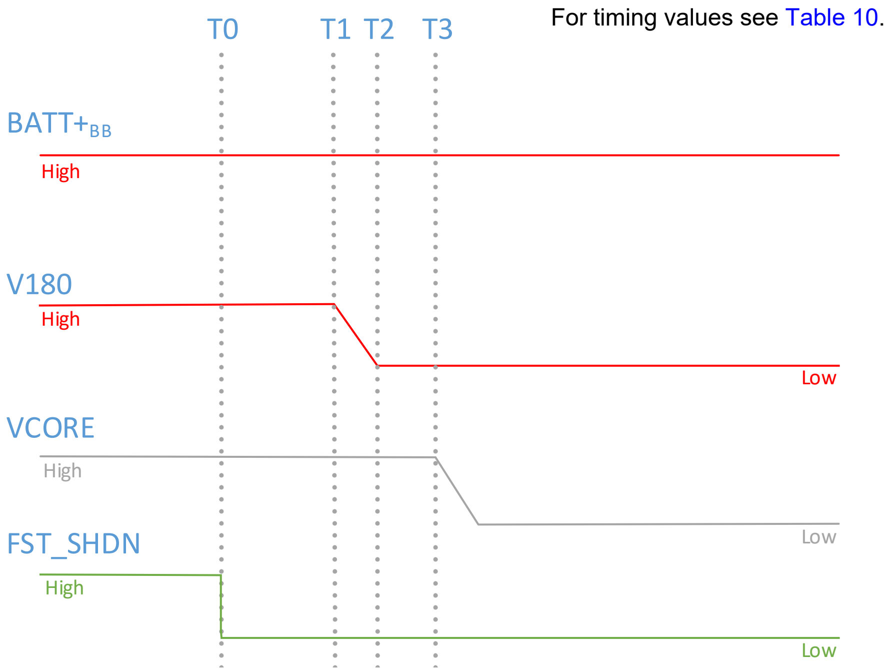


Figure 24: Fast shutdown timing

Table 10: Fast shutdown timing values

Timing	Description	Typical value	Unit
TX62-W			
T0 – T1	FST_SHDN - V180	7.38	ms
T1 – T2	V180 - V180 (low)	1.09	
T2 – T3	V180 (low) - VCORE	2.06	
TX62-W-B			
T0 – T1	FST_SHDN - V180	6.99	ms
T1 – T2	V180 - V180 (low)	0.71	
T2 – T3	V180 (low) - VCORE	2.48	
TX62-W-C			
T0 – T1	FST_SHDN - V180	15.47	ms
T1 – T2	V180 - V180 (low)	0.61	
T2 – T3	V180 (low) - VCORE	2.58	
TX82-W			
T0 – T1	FST_SHDN - V180	7.38	ms
T1 – T2	V180 - V180 (low)	1.09	
T2 – T3	V180 (low) - VCORE	2.06	
TX82-W-B			
T0 – T1	FST_SHDN - V180	TBD.	ms
T1 – T2	V180 - V180 (low)	TBD.	
T2 – T3	V180 (low) - VCORE	TBD.	

If the FST_SHDN functionality is not used, the FST_SHDN line can be left open because of a configured internal pull-up resistor.

If there is a reasonable probability for sudden power losses, Telit Cinterion recommends to implement a circuit using the FST_SHDN line with a capacitor to buffer sufficient energy to complete the fast shutdown - see below for sample capacity calculation.

Please note that the normal software controlled shutdown via AT^SMSO can also be configured as a fast shutdown, i.e., without network deregistration. For details see [1].

Sample Capacity Calculation:

The following formula is a guideline for a capacitor required in a fast shutdown circuit to buffer enough energy to complete the fast shutdown process.

Depending on the application design and use case the factors may vary.

Capacitor energy:

E[J]= 0.5xC * (V^2max-V^2-min) * Aging factor

Example:

Aging factor e.g = 0.7

E[J]= 0.5 * 2500uF * ((4.5V)^2 - (3.0V)^2) * 0.7 = 0.0098J

Note: Vmax can be limited by the module supply and/or capacitor voltage. Same applies for the Vmin which is mostly limited by VBATT min.

Module energy usage:

Vavg = Vmax + Vmin / 2

Aavg = See typical values in Section 4.4.1

E[W] = Vavg * Aavg * Efficiency

Example:

Power Circuit Efficiency factor, e.g = 0.8

E[W] = ((4.5V + 3.0V) / 2) * 0.2A * 0.8 = 0.6375W (assuming the Cat NB1/2 worst case)

Discharge time:

T[s] = E[J] / E[W]

Example:

T[s] = 0.0098J / 0.6375W = 0.015s

3.1.12.4 SIM Switch

The UICC/USIM/SIM interface lines may be connected to an external SIM card multiplexer controlled by the SIM_SWITCH signal as shown in Figure 25. Thus, it becomes possible to switch

between two networks/subscriptions each with their own UICC, and maybe different connection speeds. Please note that hot SIM insert/removal is only possible on the first SIM interface.

Also note that the SIM_SWITCH can be used to switch between a SIM and the embedded optional eUICC interface as described in Section 3.1.8.

The SIM_SWITCH signal is controlled by AT command (see [1]).

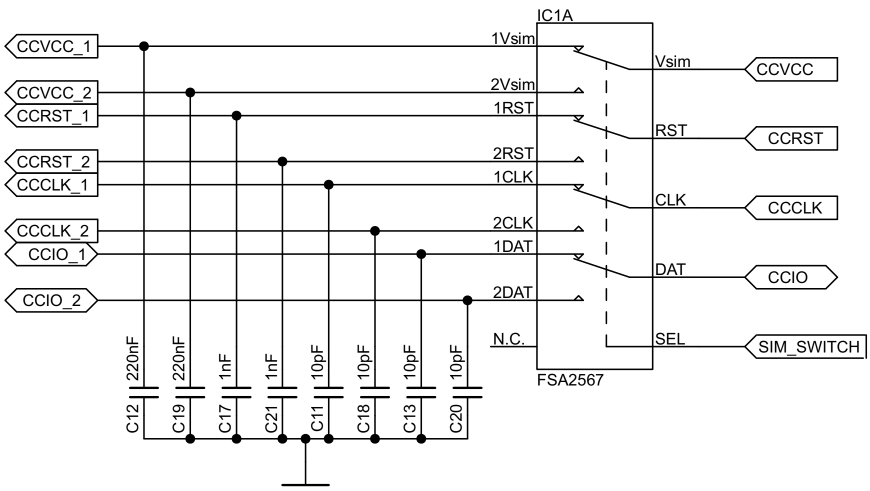


Figure 25: **SIM switch circuit**

3.1.12.5 SUSPEND Mode Indicator

When all conditions for entering into SUSPEND mode are fulfilled, the SUSPEND_MON signal changes from high to low, indicating that the module has entered its SUSPEND mode.

When leaving the SUSPEND mode, the URC “^SYSRESUME” is triggered, and the SUSPEND_MON signal is set to high again.

SUSPEND_MON usage can be enabled/disabled by AT command (see [1]: AT^SCFG “GPIO/Mode/Suspend”).

3.2 RF Antenna Interface

The RF interface has an impedance of 50Ω. Cinterion® TX62/TX82 is capable of sustaining a total mismatch at the antenna line without any damage, even when transmitting at maximum RF power.

The external antenna must be matched properly to achieve best performance regarding radiated power, modulation accuracy and harmonic suppression. Antenna matching networks are not included on the Cinterion® TX62/TX82 module and should be placed in the host application if the antenna does not have an impedance of 50Ω.

Regarding the return loss Cinterion® TX62/TX82 provides the following values in the active band:

Table 11: Return loss in the active band

State of module	Return loss of module	Recommended return loss of application
Receive	≥ 8dB	≥ 12dB
Transmit	not applicable	≥ 12dB
Idle	≤5dB	not applicable

3.2.1 Antenna Interface Specifications

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W	TX82-W	
LTE connectivity (Cat M1)	Band 1, 2, 3, 4, 5, 8, 12, 13, 18, 19, 20, 25, 26, 27, 28, 66, 85				

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W	TX82-W	
LTE Cat M1: Receiver Input Sensitivity @NTNV BW: 5 MHz, UL: Modulation: QPSK; N_{RB}=6; DL: Modulation: QPSK; N_{RB}=4;	LTE 2100 Band 1	-103	-107	-106	dBm
	LTE 1800 Band 2	-101	-107	-106	dBm
	LTE 1900 Band 3	-100	-107	-105.5	dBm
	LTE AWS-1 Band 4	-103	-107	-106	dBm
	LTE 850 Band 5	-101.5	-107.5	-106	dBm
	LTE 900 Band 8	-100.5	-107	-106.5	dBm
	LTE 700 Band 12	-100	-107	-106.3	dBm
	LTE 700 Band 13	-100	-107	-106.3	dBm
	LTE 800 Band 18	-103	-107.5	-106.2	dBm
	LTE 800 Band 19	-103	-107.5	-106	dBm
	LTE 800 Band 20	-100.5	-107	-106	dBm
	LTE 1900 Band 25	-101	-107.5	-106.2	dBm
	LTE 800 Band 26	-101	-107.5	-106.3	dBm
	LTE 800 Band 27	-101.5	-107.5	-106	dBm
	LTE 700 Band 28	-101.5	-107.5	-106	dBm
	LTE AWS-3 Band 66	-99	-107	-106	dBm
	LTE 700 Band 85	-99.2	-107	-106	dBm

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W	TX82-W	
LTE Cat M1: Power @ ARP with 50Ω Load, NTNV BW: 5 MHz, UL: Modulation: QPSK; N_{RB}=1;	LTE 2100 Band 1	+18	+20	+20	dBm
	LTE 1800 Band 2	+18	+20	+20	dBm
	LTE 1900 Band 3	+18	+20	+20	dBm
	LTE AWS-1 Band 4	+18	+20	+20	dBm
	LTE 850 Band 5	+18	+20	+20	dBm
	LTE 900 Band 8	+18	+20	+20	dBm
	LTE 700 Band 12	+18	+20	+20	dBm
	LTE 700 Band 13	+18	+20	+20	dBm
	LTE 800 Band 18	+18	+20	+20	dBm
	LTE 800 Band 19	+18	+20	+20	dBm
	LTE 800 Band 20	+18	+20	+20	dBm
	LTE 1900 Band 25	+18	+20	+20	dBm
	LTE 800 Band 26	+18	+20	+20	dBm
	LTE 800 Band 27	+18	+20	+20	dBm
	LTE 700 Band 28	+18	+20	+20	dBm
	LTE AWS-3 Band 66	+18	+20	+20	dBm
	LTE 700 Band 85	+18	+20	+20	dBm
LTE connectivity (Cat NB1/2)	Band 1, 2, 3, 4, 5, 8, 12, 13, 18, 19, 20, 25, 26, 28, 66, 71, 85				

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W	TX82-W	
LTE Cat NB1/2: Receiver Input Sensitivity @NTNV DL: Modulation: QPSK; Subcarriers: 12; UL: Modulation: BPSK; Subcarrier spacing: 15KHz; N_{tones}: 1@0	LTE 2100 Band 1	-108.2	-115	-114	dBm
	LTE 1800 Band 2	-108.2	-115.5	-114	dBm
	LTE 1900 Band 3	-108.2	-114.5	-114	dBm
	LTE AWS-1 Band 4	-108.2	-115	-114	dBm
	LTE 850 Band 5	-108.2	-116	-114.5	dBm
	LTE 900 Band 8	-108.2	-115.5	-115	dBm
	LTE 700 Band 12	-108.2	-116	-115	dBm
	LTE 700 Band 13	-108.2	-116	-115	dBm
	LTE 800 Band 18	-108.2	-115.5	-115	dBm
	LTE 800 Band 19	-108.2	-115.5	-114.5	dBm
	LTE 800 Band 20	-108.2	-115.5	-115	dBm
	LTE 1900 Band 25	-108.2	-115.5	-114.5	dBm
	LTE 800 Band 26	-108.2	-116	-115	dBm
	LTE 700 Band 28	-108.2	-116	-115	dBm
	LTE AWS-3 Band 66	-108.2	-115.5	-114	dBm
	LTE 600 Band 71	-108.2	-116	-115	dBm
	LTE 700 Band 85	-108.2	-116	-115.5	dBm

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W	TX82-W	
LTE Cat NB1/2: Power @ ARP with 50Ω Load, NTNV Configuration ID: 1, UL: Modulation: BPSK; Subcarrier: 1;Subcarrier space: 3.75 kHz; N_{tones}: 1@0	LTE 2100 Band 1	+18	+20	+20	dBm
	LTE 1800 Band 2	+18	+20	+20	dBm
	LTE 1900 Band 3	+18	+20	+20	dBm
	LTE AWS-1 Band 4	+18	+20	+20	dBm
	LTE 850 Band 5	+18	+20	+20	dBm
	LTE 900 Band 8	+18	+20	+20	dBm
	LTE 700 Band 12	+18	+20	+20	dBm
	LTE 700 Band 13	+18	+20	+20	dBm
	LTE 800 Band 18	+18	+20	+20	dBm
	LTE 800 Band 19	+18	+20	+20	dBm
	LTE 800 Band 20	+18	+20	+20	dBm
	LTE 1900 Band 25	+18	+20	+20	dBm
	LTE 800 Band 26	+18	+20	+20	dBm
	LTE 700 Band 28	+18	+20	+20	dBm
	LTE AWS-3 Band 66	+18	+20	+20	dBm
	LTE 600 Band 71	+18	+20	+20	dBm
	LTE 700 Band 85	+18	+20	+20	dBm
GPRS coding schemes	Class 10, CS1 to CS4				
EGPRS	Class 10, MCS1 to MCS9				
GSM Class	Small MS				
GPRS Static Receiver input Sensitivity @ PDTCH/CS-1	GSM 850/900	-104		-110	dBm
	GSM 1800/1900	-104		-109	dBm

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter		Conditions	Min.[2]	Typical		Unit
				TX62-W	TX82-W	
RF Power @ ARP with 50Ω Load, (ROPR = 4, i.e. no reduction)	GSM 850/ 900	GPRS, 1 TX			32.5	dBm
		GPRS, 2 TX			32.5	dBm
		EDGE, 1 TX			27.0	dBm
		EDGE, 2TX			27.0	dBm
	GSM 1800/ 1900	GPRS, 1 TX			29.5	dBm
		GPRS, 2 TX			29.5	dBm
		EDGE, 1 TX			26.0	dBm
		EDGE, 2TX			26.0	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 5)	GSM 850/ 900	GPRS, 1 TX			32.5	dBm
		GPRS, 2 TX			23.5	dBm
		EDGE, 1 TX			27.0	dBm
		EDGE, 2TX			27.0	dBm
	GSM 1800/ 1900	GPRS, 1 TX			29.5	dBm
		GPRS, 2 TX			29.5	dBm
		EDGE, 1 TX			26.0	dBm
		EDGE, 2TX			26.0	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 6)	GSM 850/ 900	GPRS, 1 TX			32.5	dBm
		GPRS, 2 TX			31.5	dBm
		EDGE, 1 TX			27.0	dBm
		EDGE, 2TX			27.0	dBm
	GSM 1800/ 1900	GPRS, 1 TX			29.5	dBm
		GPRS, 2 TX			28.5	dBm
		EDGE, 1 TX			26.0	dBm
		EDGE, 2TX			26.0	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 7)	GSM 850/ 900	GPRS, 1 TX			32.5	dBm
		GPRS, 2 TX			29.5	dBm
		EDGE, 1 TX			27.0	dBm
		EDGE, 2TX			27.0	dBm
	GSM 1800/ 1900	GPRS, 1 TX			29.5	dBm
		GPRS, 2 TX			26.5	dBm
		EDGE, 1 TX			26.0	dBm
		EDGE, 2TX			26.0	dBm

Table 12: RF Antenna interface GSM / LTE[1] of TX82-W, and TX62-W

Parameter		Conditions	Min.[2]	Typical		Unit
				TX62-W	TX82-W	
RF Power @ ARP with 50Ω Load, (ROPR = 8, i.e. maximum reduction)	GSM 850/ 900	GPRS, 1 TX			32.5	dBm
		GPRS, 2 TX			29.5	dBm
		EDGE, 1 TX			27.0	dBm
		EDGE, 2TX			24.0	dBm
	GSM 1800/ 1900	GPRS, 1 TX			29.5	dBm
		GPRS, 2 TX			26.5	dBm
		EDGE, 1 TX			26.0	dBm
		EDGE, 2TX			23.0	dBm

1. GSM (2G) only supported by TX82-W.

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W-B	TX82-W-B	
LTE connectivity (Cat M1)	Band 1, 2, 3, 4, 5, 8, 12, 13, 18, 19, 20, 25, 26, 27, 28, 66, 85				

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W-B	TX82-W-B	
LTE Cat M1: Receiver Input Sensitivity @NTNV BW: 5 MHz, UL: Modulation: QPSK; N_{RB}=6; DL: Modulation: QPSK; N_{RB}=4;	LTE 2100 Band 1	-103	-106.5		dBm
	LTE 1800 Band 2	-101	-106.3		dBm
	LTE 1900 Band 3	-100	-105.7		dBm
	LTE AWS-1 Band 4	-103	-106.3		dBm
	LTE 850 Band 5	-101.5	-106.5		dBm
	LTE 900 Band 8	-100.5	-106.3		dBm
	LTE 700 Band 12	-100	-105.7		dBm
	LTE 700 Band 13	-100	-106		dBm
	LTE 800 Band 18	-103	-106.5		dBm
	LTE 800 Band 19	-103	-106.5		dBm
	LTE 800 Band 20	-100.5	-105.5		dBm
	LTE 1900 Band 25	-101	-106.2		dBm
	LTE 800 Band 26	-101	-106.5		dBm
	LTE 800 Band 27	-101.5	-106.4		dBm
	LTE 700 Band 28	-101.5	-105.8		dBm
	LTE AWS-3 Band 66	-99	-106.2		dBm
	LTE 700 Band 85	-99.2	-105.6		dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W-B	TX82-W-B	
LTE Cat M1: Power @ ARP with 50Ω Load, NTNV BW: 5 MHz, UL: Modulation: QPSK; N_{RB}=1;	LTE 2100 Band 1	+21	+23		dBm
	LTE 1800 Band 2	+21	+23		dBm
	LTE 1900 Band 3	+21	+23		dBm
	LTE AWS-1 Band 4	+21	+23		dBm
	LTE 850 Band 5	+21	+23		dBm
	LTE 900 Band 8	+21	+23		dBm
	LTE 700 Band 12	+21	+23		dBm
	LTE 700 Band 13	+21	+23		dBm
	LTE 800 Band 18	+21	+23		dBm
	LTE 800 Band 19	+21	+23		dBm
	LTE 800 Band 20	+21	+23		dBm
	LTE 1900 Band 25	+21	+23		dBm
	LTE 800 Band 26	+21	+23		dBm
	LTE 800 Band 27	+21	+23		dBm
	LTE 700 Band 28	+21	+23		dBm
	LTE AWS-3 Band 66	+21	+23		dBm
	LTE 700 Band 85	+21	+23		dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W-B	TX82-W-B	
LTE connectivity (Cat NB1/ 2)	Band 1, 2, 3, 4, 5, 8, 12, 13, 18, 19, 20, 25, 26, 28, 66, 71[3], 85				
LTE Cat NB1/2: Receiver Input Sensitivity @NTNV DL: Modulation: QPSK; Subcarriers: 12; UL: Modulation: BPSK; Subcarrier spacing: 15KHz; N_{tones}: 1@0	LTE 2100 Band 1	-108.2	-114		dBm
	LTE 1800 Band 2	-108.2	-114.3		dBm
	LTE 1900 Band 3	-108.2	-114		dBm
	LTE AWS-1 Band 4	-108.2	-114.5		dBm
	LTE 850 Band 5	-108.2	-115		dBm
	LTE 900 Band 8	-108.2	-115		dBm
	LTE 700 Band 12	-108.2	-115.5		dBm
	LTE 700 Band 13	-108.2	-115.5		dBm
	LTE 800 Band 18	-108.2	-115		dBm
	LTE 800 Band 19	-108.2	-115		dBm
	LTE 800 Band 20	-108.2	-115		dBm
	LTE 1900 Band 25	-108.2	-114		dBm
	LTE 800 Band 26	-108.2	-115		dBm
	LTE 700 Band 28	-108.2	-115.5		dBm
	LTE AWS-3 Band 66	-108.2	-114		dBm
	LTE 600 Band 71[3]	-108.2	-115	--	dBm
	LTE 700 Band 85	-108.2	-115		dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter	Conditions	Min.[2]	Typical		Unit
			TX62-W-B	TX82-W-B	
LTE Cat NB1/2: Power @ ARP with 50Ω Load, NTNV Configuration ID: 1, UL: Modulation: BPSK; Subcarrier: 1;Subcarrier space: 3.75 kHz; N_{tones}: 1@0	LTE 2100 Band 1	+21	+23		dBm
	LTE 1800 Band 2	+21	+23		dBm
	LTE 1900 Band 3	+21	+23		dBm
	LTE AWS-1 Band 4	+21	+23		dBm
	LTE 850 Band 5	+21	+23		dBm
	LTE 900 Band 8	+21	+23		dBm
	LTE 700 Band 12	+21	+23		dBm
	LTE 700 Band 13	+21	+23		dBm
	LTE 800 Band 18	+21	+23		dBm
	LTE 800 Band 19	+21	+23		dBm
	LTE 800 Band 20	+21	+23		dBm
	LTE 1900 Band 25	+21	+23		dBm
	LTE 800 Band 26	+21	+23		dBm
	LTE 700 Band 28	+21	+23		dBm
	LTE AWS-3 Band 66	+21	+23		dBm
	LTE 600 Band 71[3]	+21	+23	--	dBm
	LTE 700 Band 85	+21	+23		dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter		Conditions	Min.[2]	Typical		Unit
				TX62-W-B	TX82-W-B	
GPRS coding schemes		Class 10, CS1 to CS4				
EGPRS		Class 10, MCS1 to MCS9				
GSM Class		Small MS				
GPRS Static Receiver input Sensitivity @ PDTCH/CS-1		GSM 850/900	TBD.		TBD.	dBm
		GSM 1800/1900	TBD.		TBD.	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 4, i.e. no reduction)	GSM 850/ 900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
	GSM 1800/ 1900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 5)	GSM 850/ 900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
	GSM 1800/ 1900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter		Conditions	Min.[2]	Typical		Unit
				TX62-W-B	TX82-W-B	
RF Power @ ARP with 50Ω Load, (ROPR = 6)	GSM 850/ 900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
	GSM 1800/ 1900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
RF Power @ ARP with 50Ω Load, (ROPR = 7)	GSM 850/ 900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
	GSM 1800/ 1900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm

Table 13: RF Antenna interface GSM / LTE[1] of TX62-W-B and TX82-W-B

Parameter		Conditions	Min.[2]	Typical		Unit
				TX62-W-B	TX82-W-B	
RF Power @ ARP with 50Ω Load, (ROPR = 8, i.e. maximum reduction)	GSM 850/ 900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm
	GSM 1800/ 1900	GPRS, 1 TX			TBD.	dBm
		GPRS, 2 TX			TBD.	dBm
		EDGE, 1 TX			TBD.	dBm
		EDGE, 2TX			TBD.	dBm

1. GSM (2G) only supported by TX82-W-B.
2. "Min." signifies 3GPP limit in case of Receiver Input Sensitivity.

3. LTE Band 71 not supported with TX82-W-B.

Table 14: RF Antenna interface LTE of TX62-W-C

Parameter	Condition	Min[1]	Typical	Unit
LTE connectivity (Cat M1)	Band 1, 3, 8, 20, 28, 31, 72			
LTE Cat M1: Receiver Input Sensitivity @NTNV BW: 5 MHz, UL: Modulation: QPSK; N_{RB}=6; DL: Modulation: QPSK; N_{RB}=4;	LTE 2100 Band 1	-103		dBm
	LTE 1900 Band 3	-100		dBm
	LTE 900 Band 8	-100.5		dBm
	LTE 800 Band 20	-100.5		dBm
	LTE 700 Band 28	-101.5		dBm
	LTE 450 Band 31	-97.3		dBm
	LTE 450 Band 72	-97.3		dBm

Table 14: RF Antenna interface LTE of TX62-W-C

Parameter	Condition	Min[1]	Typical	Unit
LTE Cat M1: Power @ ARP with 50Ω Load, NTNV BW: 5 MHz, UL: Modulation: QPSK; NRB=1;	LTE 2100 Band 1	+21	+23	dBm
	LTE 1900 Band 3	+21	+23	dBm
	LTE 900 Band 8	+21	+23	dBm
	LTE 800 Band 20	+21	+23	dBm
	LTE 700 Band 28	+20.5	+23	dBm
	LTE 450 Band 31	+24	+26	dBm
	LTE 450 Band 72	+24	+26	dBm
LTE connectivity (Cat NB1/2)[2]	Band 1, 3, 8, 20, 28, 31, 72			
LTE Cat NB1/2: Receiver Input Sensitivity @NTNV DL: Modulation: QPSK; Subcarriers: 12; UL: Modulation: BPSK; Subcarrier spacing: 15KHz; Ntones: 1@0	LTE 2100 Band 1	-108.2		dBm
	LTE 1900 Band 3	-108.2		dBm
	LTE 900 Band 8	-108.2		dBm
	LTE 800 Band 20	-108.2		dBm
	LTE 700 Band 28	-108.2		dBm
	LTE 450 Band 31	-108.2		dBm
	LTE 450 Band 72	-108.2		dBm
LTE Cat NB1/2: Power @ ARP with 50Ω Load, NTNV Configuration ID: 1, UL: Modulation: BPSK; Subcarrier: 1;Subcarrier space: 3.75 kHz; Ntones: 1@0	LTE 2100 Band 1	+21	+23	dBm
	LTE 1900 Band 3	+21	+23	dBm
	LTE 900 Band 8	+21	+23	dBm
	LTE 800 Band 20	+21	+23	dBm
	LTE 700 Band 28	+21	+23	dBm
	LTE 450 Band 31	+21	+23	dBm
	LTE 450 Band 72	+21	+23	dBm

1. "Min." signifies 3GPP limit in case of Receiver Input Sensitivity.
2. With TX62-W-C support for LTE Cat NB1/2 is by default deactivated, but may be activated on demand.

3.2.2 Antenna Installation

The antennas is connected by soldering the antenna pads (RF_OUT, ANT_GNSS) and its neighboring ground pads directly to the application's PCB.

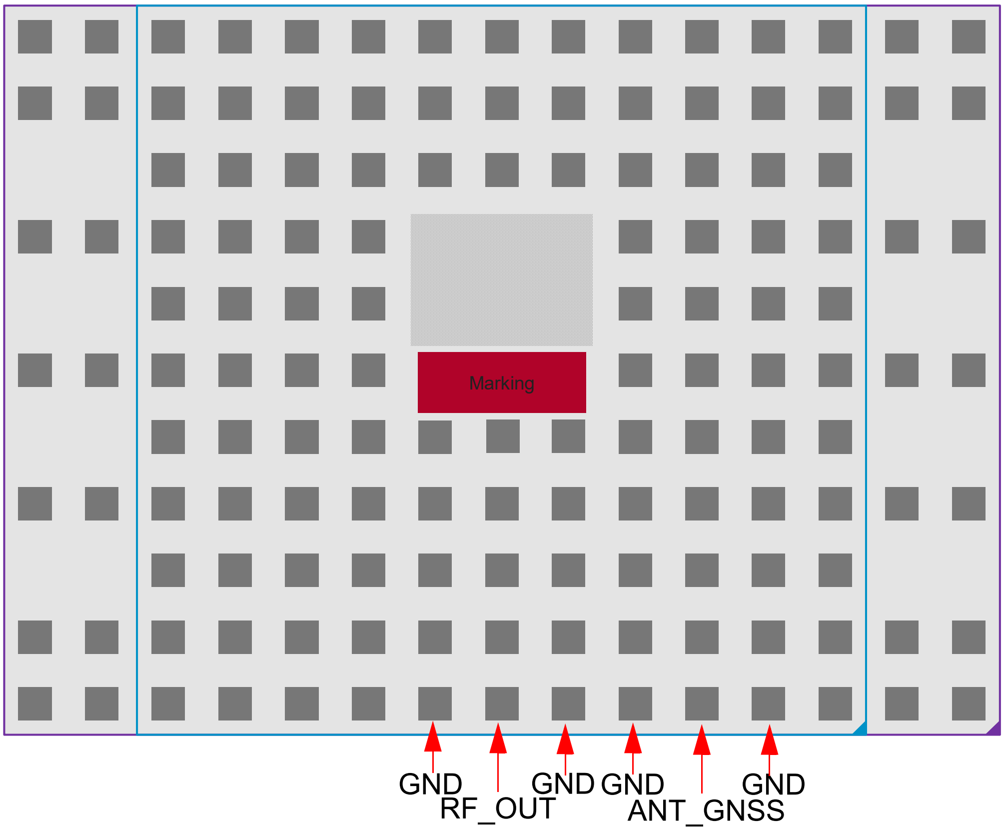


Figure 26: **Antenna pads (top view)**

The distance between the antenna pads and their neighboring GND pads has been optimized for best possible impedance. On the application PCB, special attention should be paid to these pads, in order to prevent mismatch.

The wiring of the antenna connection line, starting from the antenna pad to the application antenna should result in a 50Ω line impedance. Line width and distance to the GND plane needs to be optimized with regard to the PCB's layer stack. Some examples are given in Section 3.3.2.

To prevent receiver desensitization due to interferences generated by fast transients like high speed clocks on the application PCB, it is recommended to realize the antenna connection line using embedded Stripline rather than Micro-Stripline technology. Please see Section 3.2.3.2 for an example.[3]

3. Please note that because of KDB 447498.GNSS, it is required to get a dedicated FCC ID, if using a PCB printed antenna.

For type approval purposes, the use of a 50Ω coaxial antenna connector (U.FL-R-SMT) might be necessary. In this case the U.FL-R-SMT connector should be placed as close as possible to Cinterion® TX62/TX82's antenna pad.

3.2.3 RF Line Routing Design

3.2.3.1 Line Arrangement Examples

Several dedicated tools are available to calculate line arrangements for specific applications and PCB materials - for example from http://www.polarinstruments.com/ (commercial software) or from https://www.awr.com/software/options/tx-line (free software).

Embedded Stripline

This figure below shows a line arrangement example for embedded stripline with 65µm FR4 prepreg (type: 1080) and 710µm FR4 core (4-layer PCB).

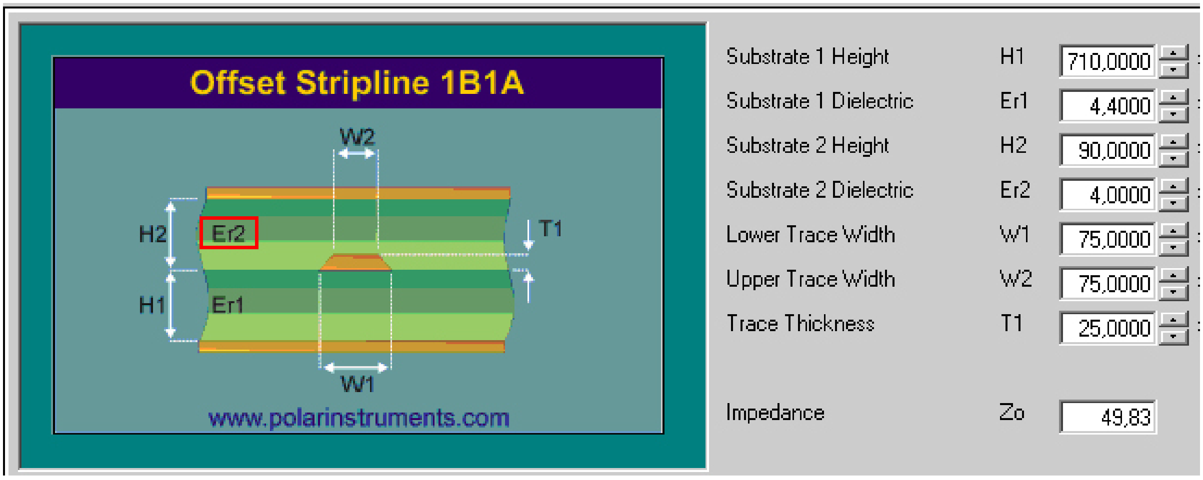


Figure 27: **Embedded Stripline with 65µm prepreg (1080) and 710µm core**

Micro-Stripline

This section gives two line arrangement examples for micro-stripline.

Micro-Stripline on 1.0mm Standard FR4 2-Layer PCB

The following two figures show examples with different values for D1 (ground strip separation).

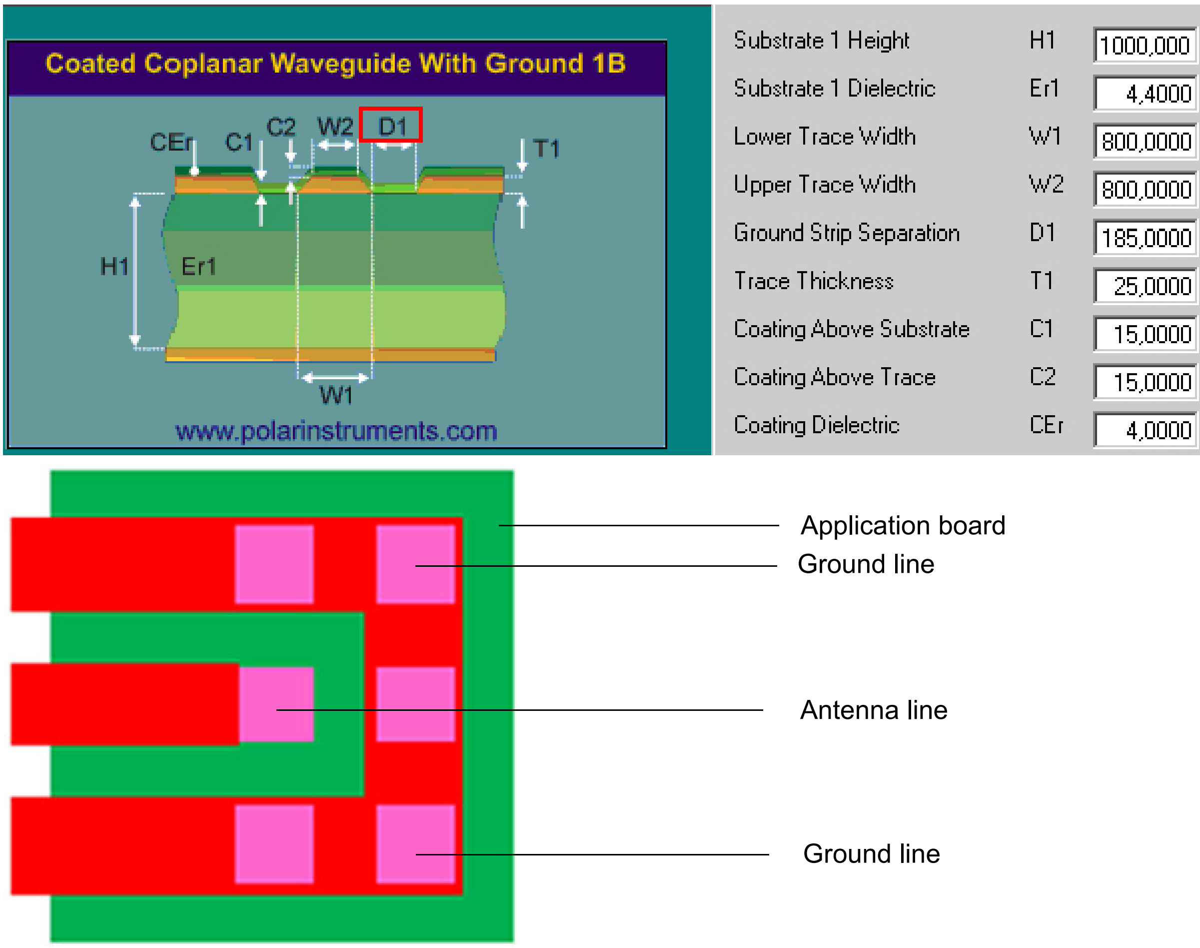


Figure 28: Micro-Stripline on 1.0mm Standard FR4 2-layer PCB - example 1

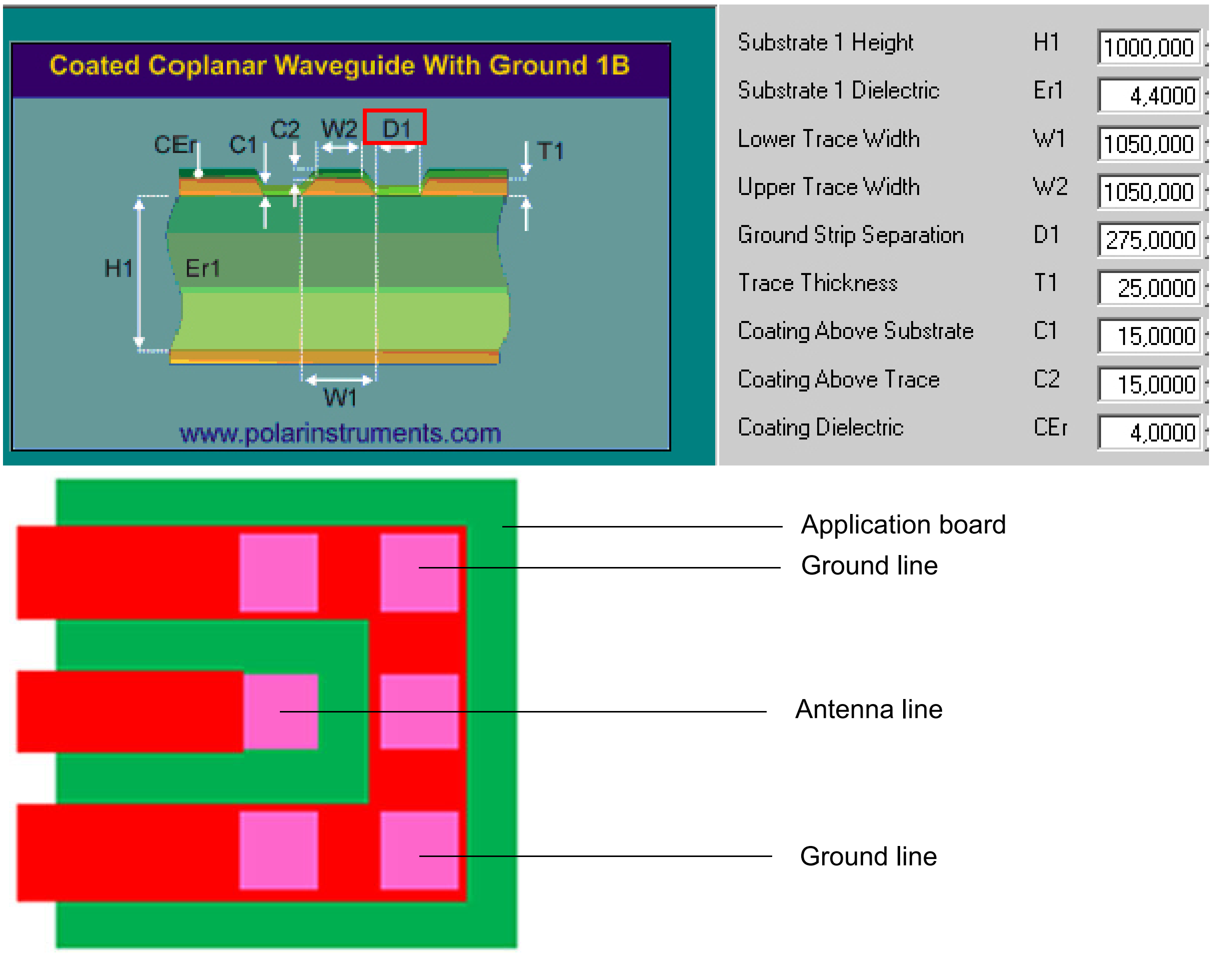


Figure 29: **Micro-Stripline on 1.0mm Standard FR4 2-layer PCB - example 2**

Micro-Stripline on 1.5mm Standard FR4 2-Layer PCB

The following two figures show examples with different values for D1 (ground strip separation).

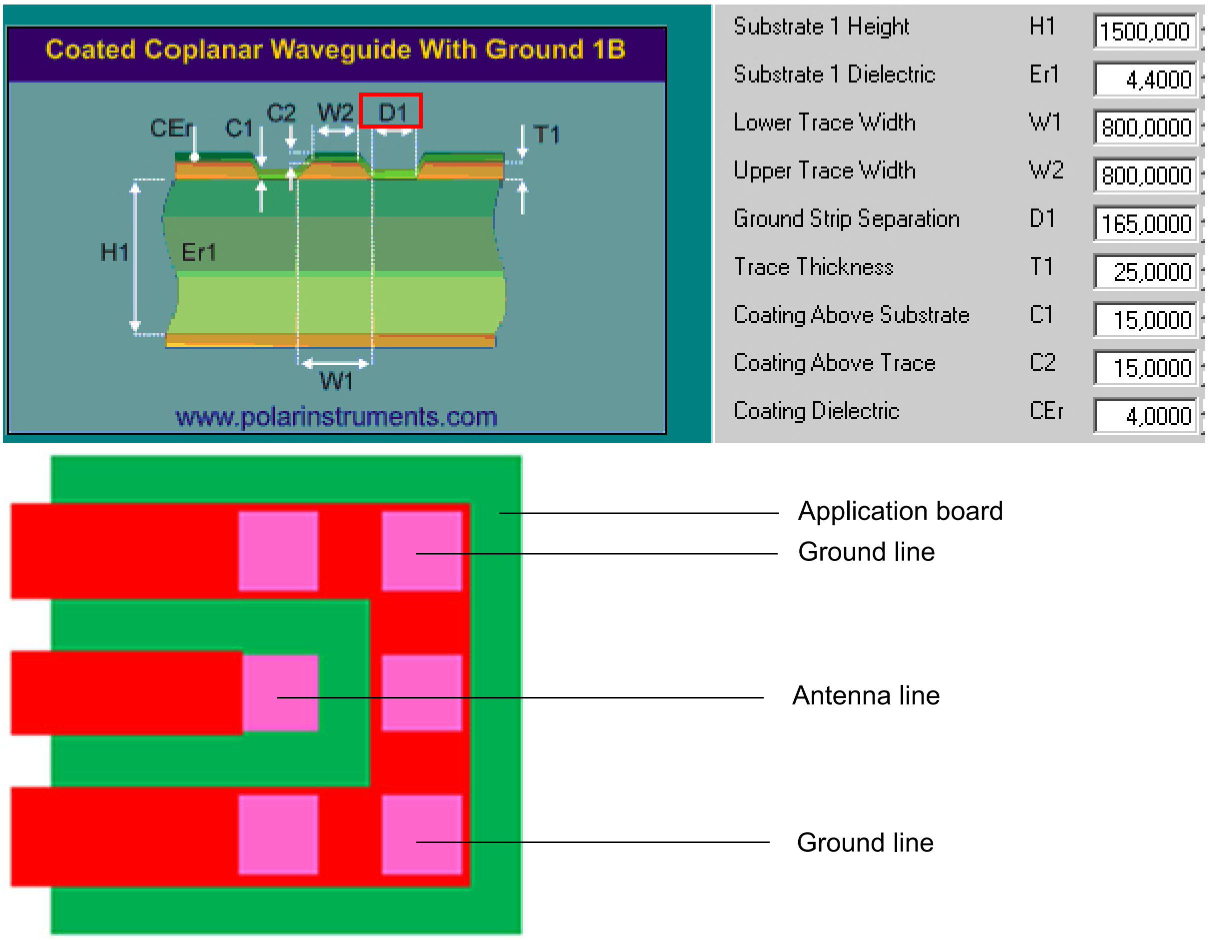


Figure 30: Micro-Stripline on 1.5mm Standard FR4 2-layer PCB - example 1

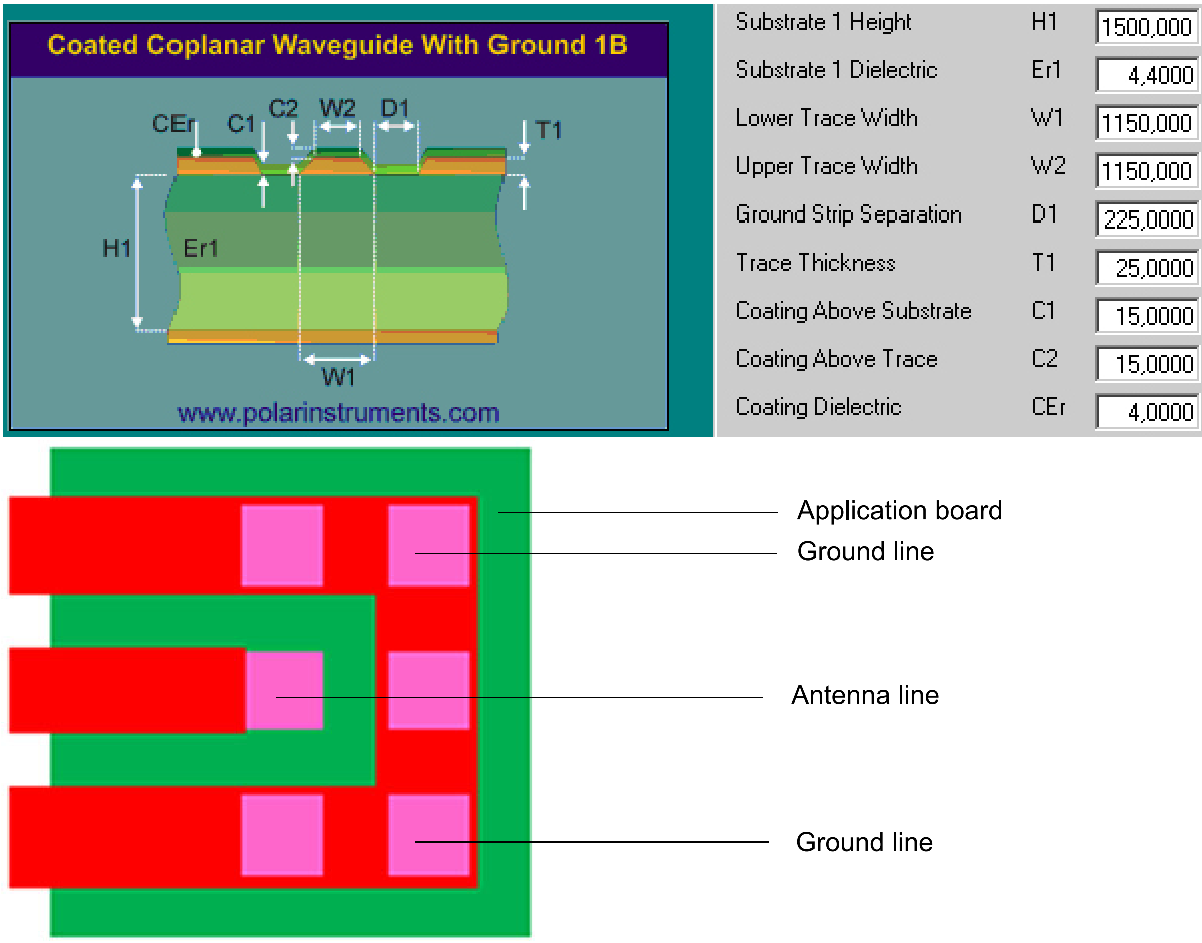


Figure 31: Micro-Stripline on 1.5mm Standard FR4 2-layer PCB - example 2

3.2.3.2 Routing Example

Interface to RF Connector

Figure 32 shows the connection of the module's antenna pad with an application PCB's co-axial antenna connector. Please note that the Cinterion® TX62/TX82 bottom plane appears mirrored, since it is viewed from Cinterion® TX62/TX82 top side. By definition the top of customer's board shall mate with the bottom of the Cinterion® TX62/TX82 module.

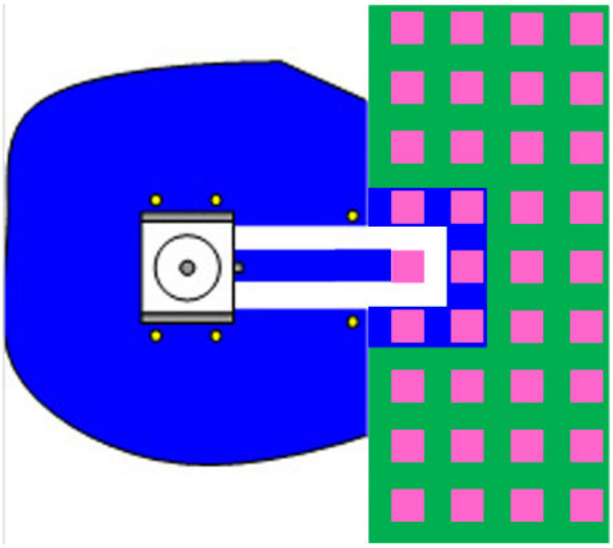


Figure 32: Routing to application's RF connector - top view

3.3 GNSS Interface

3.3.1 GNSS Receiver

Cinterion® TX62/TX82 integrates a GNSS receiver that offers the full performance of GPS/GLONASS/BeiDou/Galileo technology. The GNSS receiver is able to continuously track all satellites in view, thus providing accurate satellite position data.

The integrated GNSS receiver supports the NMEA protocol via ASC0 interface. NMEA is a combined electrical and data specification for communication between various (marine) electronic devices including GNSS receivers. It has been defined and controlled by the US based National Marine Electronics Association. For more information on the NMEA Standard please refer to http://www.nmea.org.

Depending on the receiver's knowledge of last position, current time and ephemeris data, the receiver's startup time (i.e., TTFF = Time-To-First-Fix) may vary: If the receiver has no knowledge of its last position or time, a startup takes considerably longer than if the receiver still has knowledge of its last position, time and almanac or has still access to valid ephemeris data and the precise time. For more information see Section 3.2.2. Often, 2D measurements will be used over 3D depending on space vehicle (SV) locations as this will be just as accurate and faster.

By default, the GNSS receiver is switched off. It has to be switched on and configured using AT commands (AT^SGPSC; see [1]). Please note that concurrent GNSS and GSM/LTE operations are not supported (AT^SCFG= "MEopMode/RscMgmt/Rrc"; see [1]).

3.3.2 GNSS Antenna

In addition to the RF antenna interface Cinterion® TX62/TX82 also has a GNSS antenna interface. See Section 3.1.11 to find out where the GNSS antenna pad is located. The GNSS installation is the same as for the RF antenna interface - see Section 3.2.2.

It is possible to connect active or passive GNSS antennas. In either case the antennas must have 50Ω impedance. Please note that if an active GNSS antenna is selected, the voltage for it has to be supplied by the external application, and a capacitor must be added to avoid voltage back-feeding (see Figure 33). If a passive GNSS antenna is selected, this capacitor is optional.

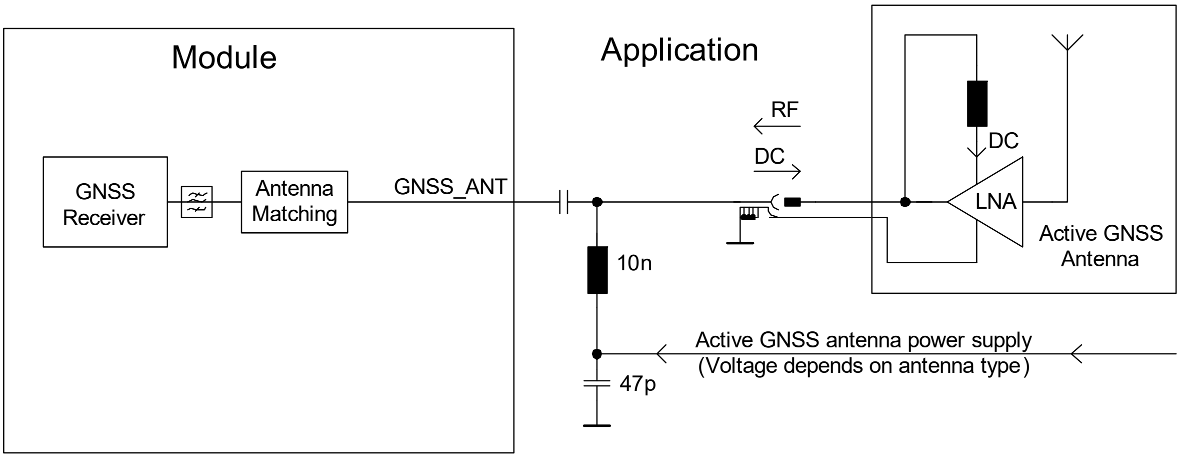


Figure 33: **Sample supply voltage circuit for active GNSS antenna**

3.3.3 GNSS Antenna Interface Characteristics

Table 15: **GNSS properties**

Parameter	Conditions	Min.	Typical	Max.	Unit
Horizontal accuracy	50% CEP, open sky		2		m
Maximal update rate			1		Hz
Frequency	GPS	1573.397	1575.420	1576.443	MHz
	GLONASS	1598.563	1602.563	1606.563	
	Beidou	1559.052	1561.098	1563.144	
	Galileo	1573.397	1575.420	1576.443	
Tracking Sensitivity	Open sky Active antenna or LNA Passive antenna: GPS GLONASS Beidou Galileo		-162		dBm

Table 15: GNSS properties

Parameter	Conditions	Min.	Typical	Max.	Unit
Acquisition Sensitivity	Open sky Active antenna or LNA Passive antenna: GPS GLONASS Beidou Galileo		-159		dBm
Time-to-First-Fix (TTFF)[1]	Hot (average at -130dBm)		3		s
	Cold (average at -130dBm)		35		s

1. Test conditions: open sky environment

3.4 Sample Application

Figure 34 shows a typical example of how to integrate a Cinterion® TX62/TX82 module with an application. Usage of the various host interfaces depends on the desired features of the application.

Note:

The sample application is not optimized for low current consumption.

Because of the very low power consumption design, current flowing from any other source into the module circuit must be avoided, for example reverse current from high state external control lines. Therefore, the controlling application must be designed to prevent reverse current flow. Otherwise there is the risk of undefined states of the module during startup and shutdown or even of damaging the module.

Because of the high RF field density inside the module, it cannot be guaranteed that no self interference might occur, depending on frequency and the applications grounding concept. The potential interferers may be minimized by placing small capacitors (47pF) at suspected lines (e.g. RXD0, TXD0, and ON).

While developing SMT applications it is strongly recommended to provide test points for certain signals, i.e., lines to and from the module - for debug and/or test purposes. The SMT application should allow for an easy access to these signals. For details on how to implement test points see [5] and [6]. Possible test points are mentioned in Section 3.1.2

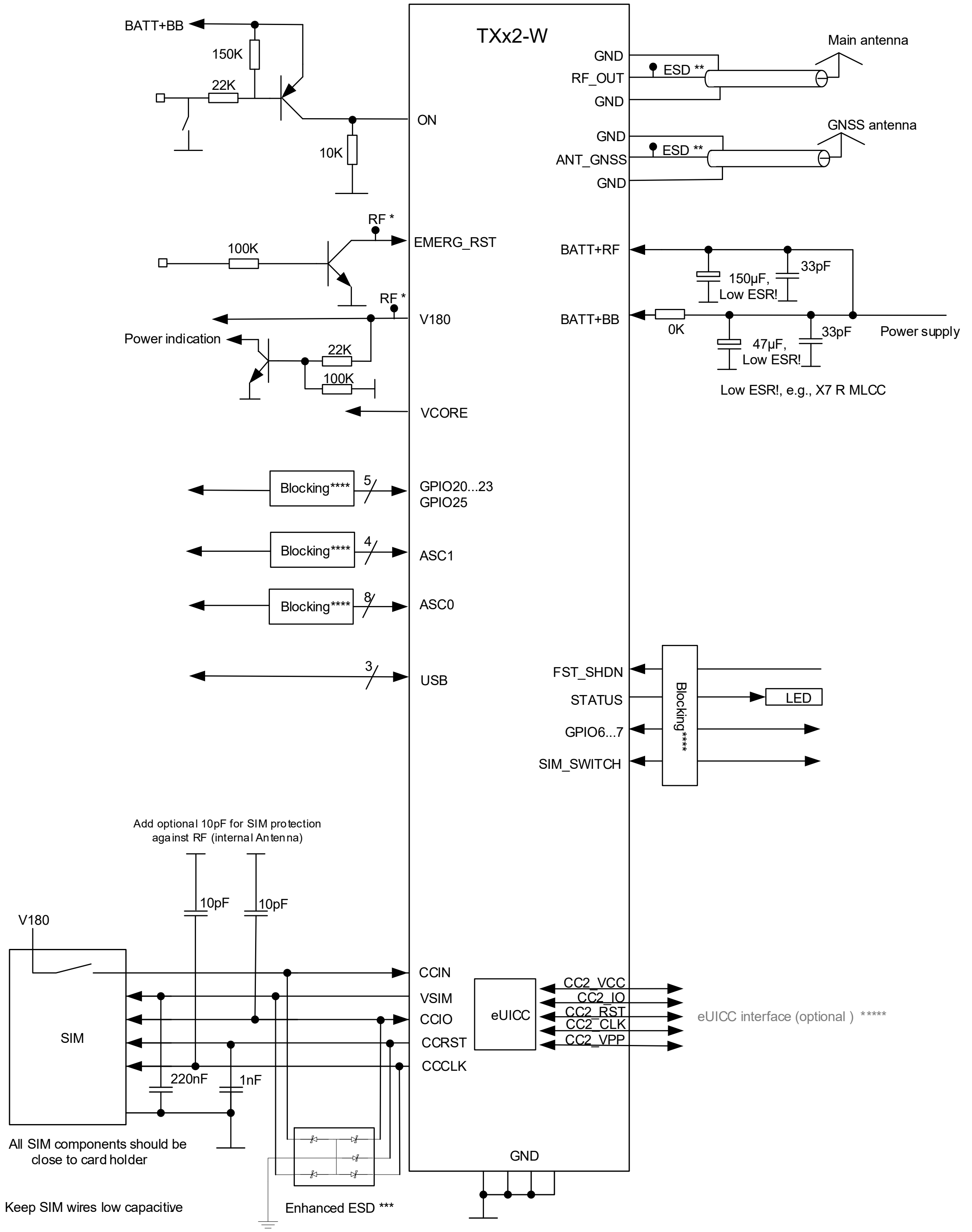
The EMC measures are best practice recommendations. In fact, an adequate EMC strategy for an individual application is very much determined by the overall layout and, especially, the position of components. For example, mounting the internal acoustic transducers directly on the PCB eliminates the need to use the ferrite beads shown in the sample schematic.

Depending on the micro controller used by an external application Cinterion® TX62/TX82's digital input and output lines may require level conversion. Section 3.4.1 shows a possible sample level conversion circuit.

Disclaimer

No warranty, either stated or implied, is provided on the sample schematic diagram shown in Figure 34 and the information detailed in this section. Functionality and compliance with national regulations depend to a great amount on the used electronic components, and the individual application layout manufacturers are required to ensure adequate design and operating safeguards for their products using Cinterion® TX62/TX82 modules. Because of the number of frequencies used it is recommended to involve antenna vendors already quite early to maximize performance of the external application's layout.

For switch on circuit see Section 4.2.1



RF* = Optional 47pF against self-interference. See also Section 4.6 for measures against RF interference
ESD** = ESD protection for RF antenna interface. For more details see Section 4.6.1
Enhanced ESD*** = Enhanced ESD protection for SIM interface. For more details see Section 3.1.7
Blocking**** = For more details see Section 4.6
eUICC interface***** = For more details see Section 3.1.8

Figure 34: Schematic diagram of Cinterion® TX62/TX82 sample application

3.4.1 Sample Level Conversion Circuit

Depending on the micro controller used by an external application Cinterion® TX62/TX82's digital input and output lines (i.e., ASC0, ASC1) may require level conversion. The following Figure 35 shows a sample circuit with recommended level shifters for an external application's micro controller (with VLOGIC between 3.0V...3.6V). The level shifters can be used for digital input and output lines with V_{OH}max=1.85V or V_{IH}max =1.85V. The sample circuit is not optimized for low current consumption.

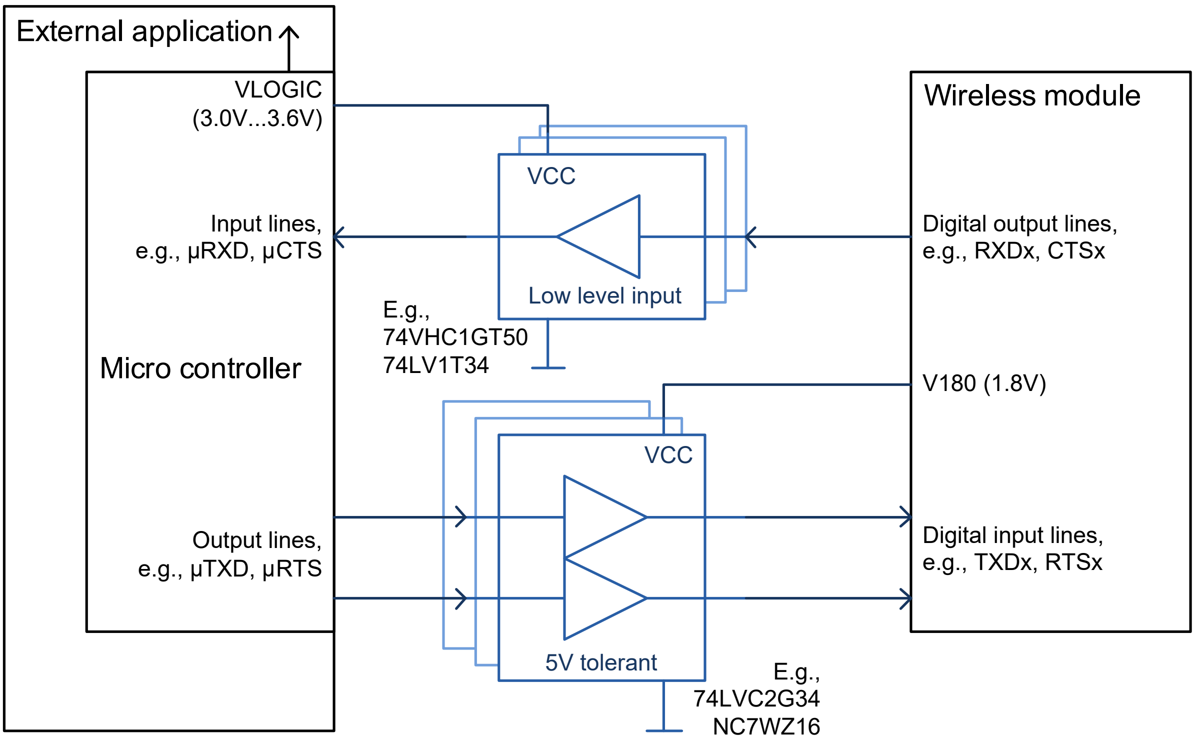


Figure 35: Sample level conversion circuit

4 Operating Characteristics

4.1 Operating Modes

The table below briefly summarizes the various operating modes referred to throughout the document.

Table 16: Overview of operating modes

Mode	Function	
Normal operation	Data transfer	GSM/(E)GPRS/LTE M1 NB1/2 data transfer in progress.
	Idle	Software and interfaces are active and ready to send and receive, but no GSM/(E)GPRS/LTE M1 NB1/2 data transfer is currently in progress.
SLEEP[1]	Low power mode when no call is in progress and there is no active communication on any serial interface (ASC0, ASC1). During SLEEP mode, the module is in a low power consumption state depending on paging cycles based on network defined DRX values, and optionally network negotiated eDRX (extended DRX) as well as 3GPP PSM values. The firmware is active to a minimum extent, and preserves the state it was in before entering the SLEEP mode. The module stays registered to the network. SLEEP mode option can be enabled/disabled by AT command (see : AT^SCFG parameter “MEopMode/PwrSave”).	
SUSPEND[1]	Low power mode when almost all components are switched off - except for the internal RTC and interrupt triggered wake up mechanisms. The module keeps registered to the network. The module is in its lowest power consumption state. The module can only be woken up by the ON or EMERG_RST signal, or it may wake up and be reachable again after expiration of a 3GPP PSM (Power Saving Mode) periodic TAU cycle (i.e., network timer) that may include DRX and/or eDRX paging cycles for a certain inactivity period. The module wakes up with its signal states being the same as for the first startup configuration, and does not preserve the signal states it had in before entering SUSPEND mode. The SUSPEND mode option can be enabled/disabled by AT commands (see MEopMode/PowerMgmt/Suspend”).	
Airplane	Restricted operating mode where the module’s radio part is shut down, causing the module to log off from the GSM/(E)GPRS/LTE M1 NB1/2 network, and to disable all AT commands whose execution requires a radio connection. Airplane mode can be controlled by AT command (see : AT+CFUN).	
POWER DOWN	State after normal shutdown by sending the switch off command (see : AT^SMSO). Software is not active. Interfaces are not accessible. Operating voltage remains applied.	

1. For details on the module’s low power modes and their configuration, please refer to Section 4.3.

4.2 Power Up/Power Down Scenarios

Do not turn on Cinterion® TX62/TX82 while it is beyond the safety limits of voltage stated in Section 3.1.2.1. Cinterion® TX62/TX82 immediately switches off after having started and detected these inappropriate conditions. In extreme cases this can cause permanent damage to the module.

4.2.1 Turn on TX62/TX82

Cinterion® TX62/TX82 can be turned on as described in the following sections:

Hardware driven switch on by ON signal: Starts Normal mode (see Section 4.2.1.1 as well as Section 4.2.1.2 (for automatic power on)).

After startup or restart, a high level of the V180 and VCORE lines, as well as the URC ^SYSSTART send by the module indicate that the module has started up (again). The URC notifies the host application that the first AT command can be sent to the module (see also [1]).

4.2.1.1 Switch on TX62/TX82 Using ON Signal

The ON signal switches the module on, if the module is in POWER DOWN mode (or in SUSPEND mode - see Section 4.3.1). This signal is a rising edge sensitive signal. The maximum input voltage can be BATT+. The module starts in the operating mode with a rising edge signal at the ON signal.

The following Figure 36 illustrates the recommended power on circuit, Figure 37 and Table 10 show the start-up behavior and timings if ON valid. Please note that the power on circuit is not optimized with regard to ultra low power consumption.

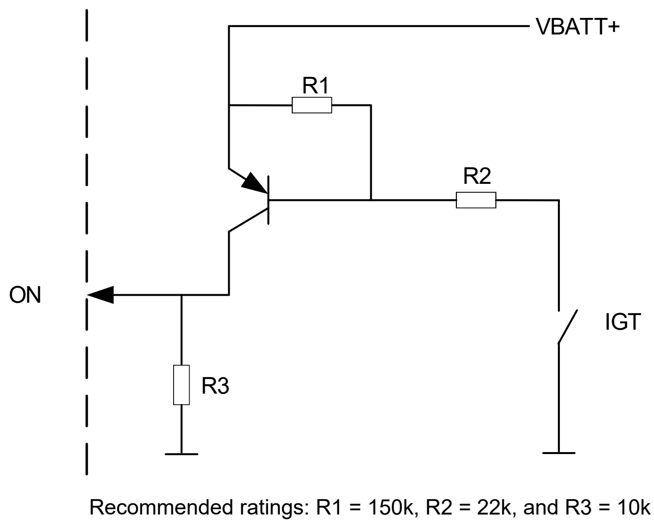


Figure 36: **Sample ON circuit**

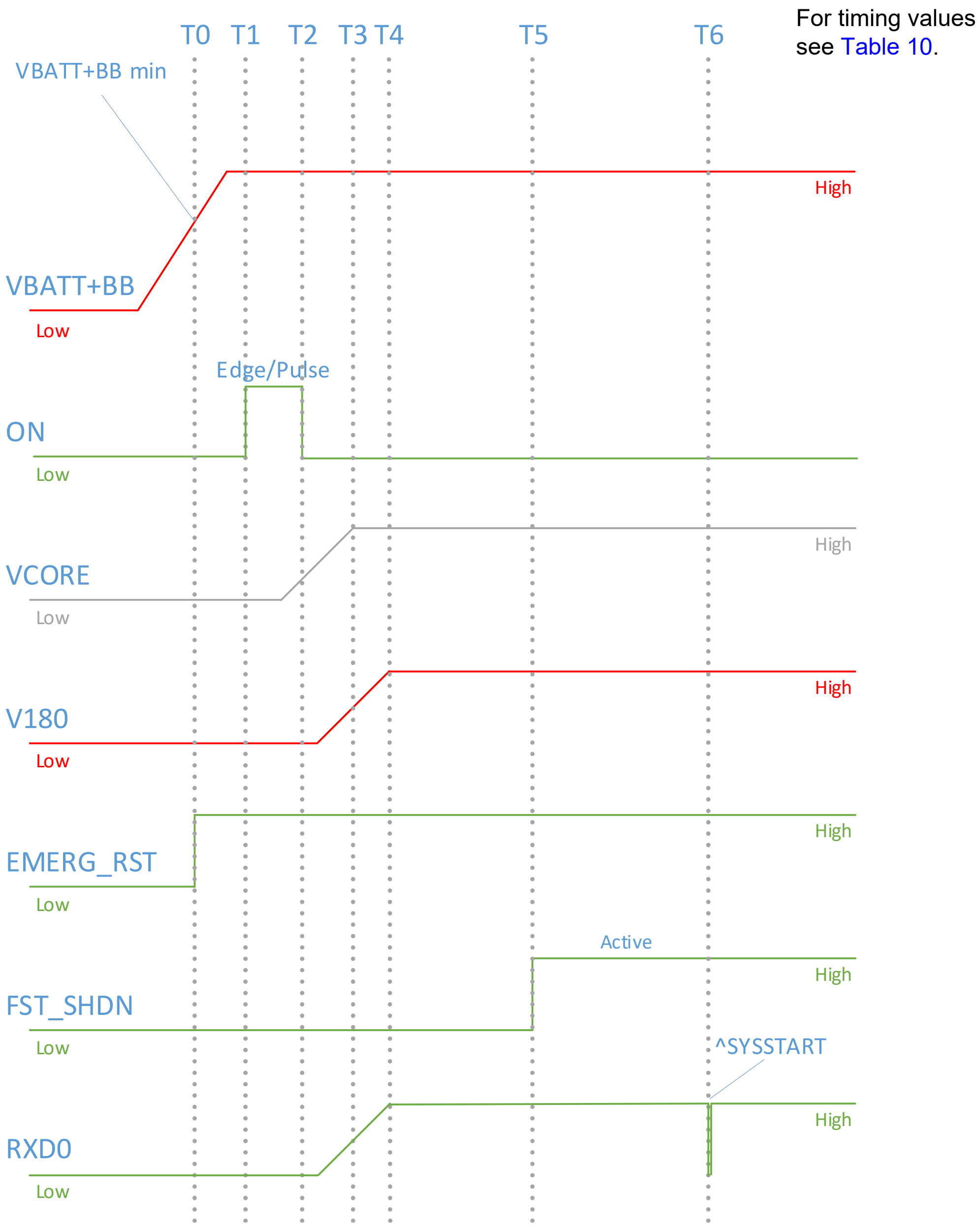


Figure 37: ON startup behavior

Table 17: ON startup timing values

Timing	Description	Typical value	Unit
TX62-W			
T0 - T1	Minimum startup time	50	ms
T1 - T2	Recommended ON pulse	30	ms
T2 - T3	ON – VCORE	23.51	ms
T3 - T4	VCORE – V180	262.15	µs
T4 - T5	V180 – FST_SHDN	1.42	s
T5 - T6	FST_SHDN - ^SYSSTART	1.81	s
TX62-W-B			

Table 17: ON startup timing values

Timing	Description	Typical value	Unit
T0 - T1	Minimum startup time	50	ms
T1 - T2	Recommended ON pulse	30	ms
T2 - T3	ON – VCORE	23.85	ms
T3 - T4	VCORE – V180	300.25	µs
T4 - T5	V180 – FST_SHDN	2.78	s
T5 - T6	FST_SHDN - ^SYSSTART	0.43	s
TX62-W-C			
T0 - T1	Minimum startup time	50	ms
T1 - T2	Recommended ON pulse	30	ms
T2 - T3	ON – VCORE	23.61	ms
T3 - T4	VCORE – V180	333.34	µs
T4 - T5	V180 – FST_SHDN	1.51	s
T5 - T6	FST_SHDN - ^SYSSTART	2.05	s
TX82-W			
T0 - T1	Minimum startup time	50	ms
T1 - T2	Recommended ON pulse	30	ms
T2 - T3	ON – VCORE	23.51	ms
T3 - T4	VCORE – V180	262.15	µs
T4 - T5	V180 – FST_SHDN	1.42	s
T5 - T6	FST_SHDN - ^SYSSTART	1.81	s
TX82-W-B			
T0 - T1	Minimum startup time	TBD.	ms
T1 - T2	Recommended ON pulse	TBD.	ms
T2 - T3	ON – VCORE	TBD.	ms
T3 - T4	VCORE – V180	TBD.	µs
T4 - T5	V180 – FST_SHDN	TBD.	s
T5 - T6	FST_SHDN - ^SYSSTART	TBD.	s

Note:

During the boot up phase of the module, the fast shutdown functionality, i.e., the use of the FST_SHDN line as described in Section 3.1.12.3, is not available. The FST_SHDN functionality becomes active only at T5 (see Table 10).

In case of a power loss before the FST_SHDN line is active (T5), the data integrity is ensured even without the FST_SHDN functionality. Once the FST_SHDN line becomes active, the line can be used to ensure data integrity during for example a power loss scenario.

4.2.1.2 Automatic Power On

When an automatic power on circuit is required for the module application, the ON pulse must be generated after BATT+ is applied. To achieve this, it is recommend to add a monoflop circuit.

With the initial switch on after BATT+ was applied, the pulse of the ON signal must be longer than 1ms. Afterwards, and if the module was already turned off at least once by AT^SMSO, the pulse of the ON signal to switch the module on must be longer than 30ms.

Figure 38 shows a suitable sample circuit.

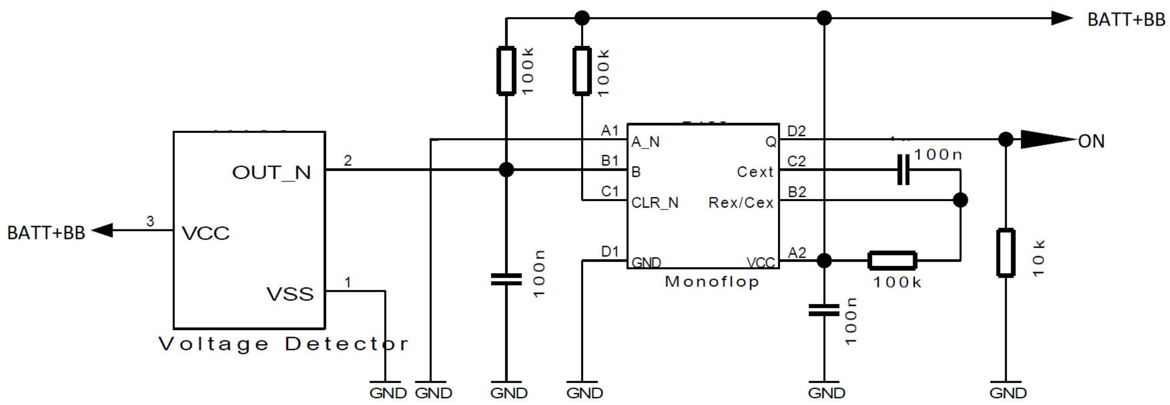


Figure 38: **Automatic switch ON circuit sample**

For the sample voltage detector circuit it is recommended to use the voltage detector NCP803SN232T1G from ON Semiconductor, and the monoflop 74LVC1G123 from Nexperia.

4.2.2 Restart TX62/TX82

After startup Cinterion® TX62/TX82 can be re-started as described in the following sections:

- Software controlled reset by AT+CFUN command: Starts Normal mode (see Section 4.2.2.1).
- Hardware controlled reset by EMERG_RST line: Starts Normal mode (see Section 4.2.2.2)

4.2.2.1 Restart TX62/TX82 via AT+CFUN Command

To reset and restart the Cinterion® TX62/TX82 module use the command AT+CFUN. See [1] for details.

4.2.2.2 Restart TX62/TX82 Using EMERG_RST

The EMERG_RST signal is internally connected to the baseband processor. A low level phase until V180 went low triggers the module restart process, and sets the processor and all signals to their respective reset states. With a shorter low level phase, i.e., V180 low state not reached, no module restart is triggered, and the module's state remains unchanged. The reset state is described in Section 4.2.3 as well as in the figures showing the startup behavior of an interface.

Note:

If the EMERG_RST signal is not released again as shown in Figure 39, i.e., changed from low to high after a restart/reset, the module will be repeatedly restarted.

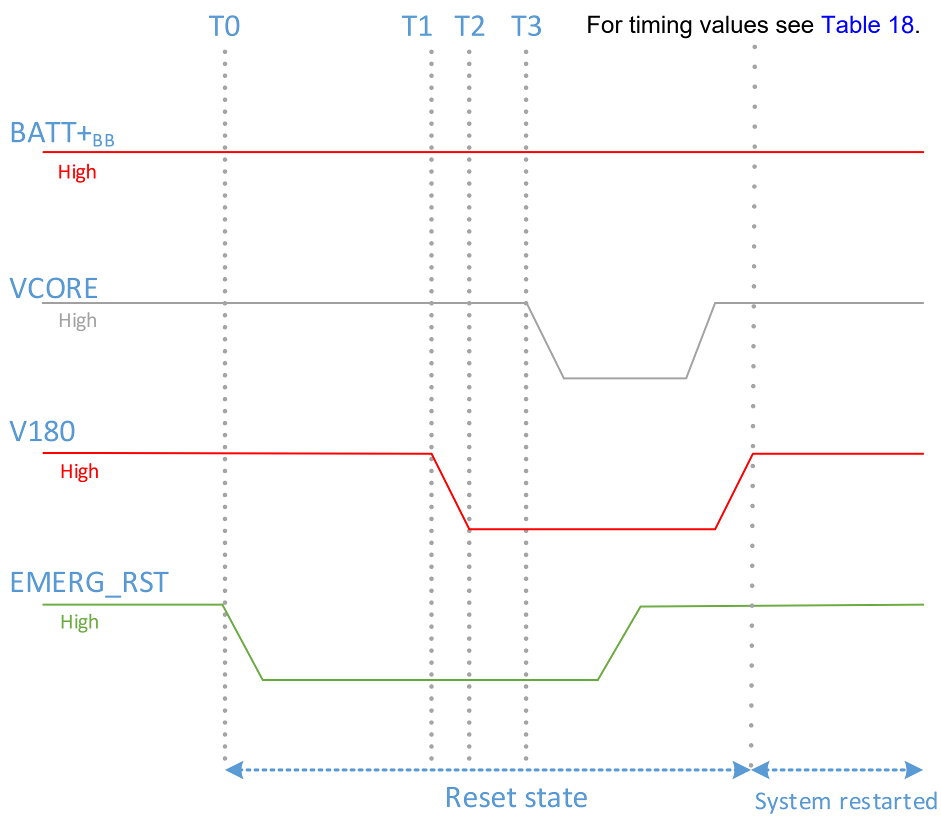


Figure 39: Emergency restart behavior

Table 18: EMERG_RST restart timing values

Timing	Description	Typical value	Unit
TX62-W			
T0 - T1	EMERG_RST - V180	763.42	ms
T1 - T2	V180 – V180 (low)	1.05	
T2 - T3	V180 (low) – VCORE	3.96	
TX62-W-B			
T0 - T1	EMERG_RST - V180	771.22	ms
T1 - T2	V180 – V180 (low)	0.94	
T2 - T3	V180 (low) – VCORE	4.09	
TX62-W-C			
T0 - T1	EMERG_RST - V180	767.14	ms
T1 - T2	V180 – V180 (low)	0.62	
T2 - T3	V180 (low) – VCORE	2.55	
TX82-W			
T0 - T1	EMERG_RST - V180	763.42	ms
T1 - T2	V180 – V180 (low)	1.05	
T2 - T3	V180 (low) – VCORE	3.96	
TX82-W-B			
T0 - T1	EMERG_RST - V180	TBD.	ms
T1 - T2	V180 – V180 (low)	TBD.	
T2 - T3	V180 (low) – VCORE	TBD.	

It is strongly recommended to control this EMERG_RST line with an open collector transistor or an open drain field-effect transistor.

Warning:

Use the EMERG_RST line only when, due to serious problems, the software is not responding for more than 5 seconds. Pulling the EMERG_RST line causes the loss of all information stored in the volatile memory. Therefore, this procedure is intended only for use in case of emergency, e.g. if Cinterion® TX62/TX82 does not respond, if reset or shutdown via AT command fails.

4.2.3 Signal States After Startup

Table 19 describes various states interface signals pass through after startup until the system is active.

Signals are in an initial state while the module is initializing. Once the startup initialization has

completed, i.e. when the software is running, all signals are in a defined state, the module is ready to receive and transmit data. The state of some signals may change again once a respective interface is activated or configured by AT command. For details on certain other signal

state changes during startup see also Section 4.2.1 (ON, VCORE, V180), Section 4.2.2 (EMERG_RST), and Section 3.1.4 (ASC0 signals).

Table 19: Signal states

Signal name	Reset state	First start up configuration
CCIO	PD	O / L
CCRST	PD	O / L
CCCLK	PD	O / L
CCIN	PD	I / PD
RXD0	PD	O / H
TXD0	PD	I / PD
CTS0	PD	O / H
RTS0	PD	I / PD
DTR0	PD	I / PU
DCD0	PD	O / H
DSR0	PD	O / H
RING0	PD	O / H
RXD1	PD	O / H
TXD1	PD	I / PD
CTS1	PD	O / H
RTS1	PD	I / PD
STATUS	PD	I / PD
FST_SHDN	PD	I / PU
I2CDAT[1]	PD	OD
I2CCLK[1]	PD	OD
SIM_SWITCH	PD	I / PD
SUSPEND_MON	PD	I / PD
GPIO6,7,20-23,25	PD	High-Z / PD

1. Available with embedded processing option only.

Abbreviations used in above Table 19:

L = Low level H = High level High-Z = High Impedance I = Input	O = Output OD = Open Drain PD = Pull down, 55kΩ ~390kΩ PU = Pull up, 55kΩ ~390kΩ

4.2.4 Turn off TX62/TX82

To switch the module off the following procedures may be used:

- Software controlled shutdown procedure: Software controlled by sending an AT command over the serial application interface. See Section 4.2.4.1.
- Hardware controlled shutdown procedure: Hardware controlled by setting the FST_SHDN line to low. See Section 3.1.12.3.
- Automatic shutdown (software controlled): See Section 4.2.5
  - Takes effect if Cinterion® TX62/TX82 board temperature exceeds a critical limit, or if
  - Undervoltage or overvoltage is detected.

With any shutdown scenario it is recommended to monitor the V180 line. If V180 is low, it is safe to cut the module power supply. Cutting the power supply too early, i.e., if V180 is not low, might harm the module.

4.2.4.1 Switch off TX62/TX82 Using AT Command

The best and safest approach to powering down the module is to issue the AT^SMSO command. This procedure lets the module log off from the network and allows the software to enter into a secure state and to save data before disconnecting the power supply. Any AT commands after AT^SMSO are ignored, and the shutdown procedure continues undisturbed. The shutdown procedure is an active process (depending on environmental conditions such as network states) until the module switches off - for signal behavior and timings see Figure 40 and Table 20. It cannot be specified how long the shutdown procedure may take at the worst.

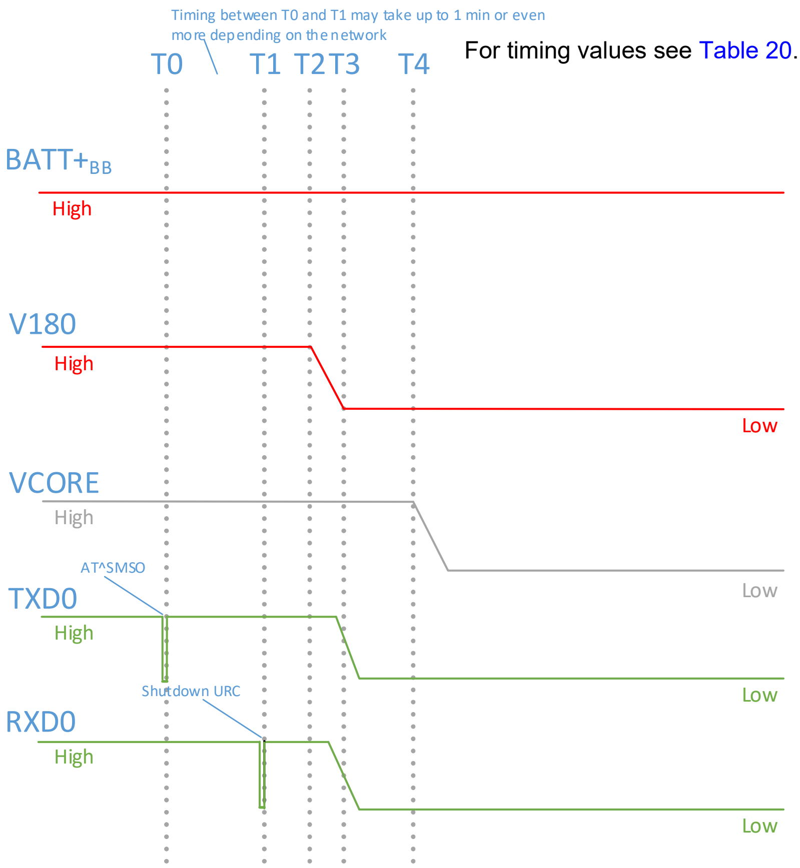


Figure 40: Switch off behavior

Table 20: **Switch off timing values (AT^SMSO)**

Timing	Description	Typical value	Unit
TX62-W			
T0 - T1	AT^SMSO – URC	294.13[1] (depending on network)	ms
T1 - T2	URC – V180	13.68	
T2 - T3	V180 – V180 (low)	0.83	
T3 - T4	V180 (low) – VCORE	2.41	
TX62-W-B			
T0 - T1	AT^SMSO – URC	302.63[1] (depending on network)	ms
T1 - T2	URC – V180	13.70	
T2 - T3	V180 – V180 (low)	0.70	
T3 - T4	V180 (low) – VCORE	2.50	
TX62-W-C			
T0 - T1	AT^SMSO – URC	234.92[1] (depending on network)	ms
T1 - T2	URC – V180	37.17	
T2 - T3	V180 – V180 (low)	0.65	
T3 - T4	V180 (low) – VCORE	2.53	
TX82-W			
T0 - T1	AT^SMSO – URC	294.13[1] (depending on network)	ms
T1 - T2	URC – V180	13.68	
T2 - T3	V180 – V180 (low)	0.83	
T3 - T4	V180 (low) – VCORE	2.41	
TX82-W-B			
T0 - T1	AT^SMSO – URC	TBD.	ms
T1 - T2	URC – V180	TBD.	
T2 - T3	V180 – V180 (low)	TBD.	
T3 - T4	V180 (low) – VCORE	TBD.	

1. Value is dependent on network, and may take up to 5 seconds for LTE Cat NB1/2 networks.

A low level of the V180 signal - in addition to the regular "^SHUTDOWN" URC issued by the module - indicates that the switch off procedure has completed and the module has entered the Power Down mode.

Note:

Using the command AT^SMSO=”fast”, it is possible to trigger a fast shutdown procedure, i.e., shutting down without gracefully deregistering from the network thus saving time between T0 - T2. Same as with the fast shutdown procedure triggered with the FST_SHDN line (see Section 3.1.12.3), there will be no ^SHUTDOWN URC in this case. The timings for module switch off using AT^SMSO=”fast” are listed below in Table 21.

Table 21: Switch off timing values (AT^SMSO="fast")

Timing	Description	Typical value	Unit
TX62-W			
T0 - T1	AT^SMSO="fast" – URC	3.34	ms
T1 - T2	URC – V180	1.51	
T2 - T3	V180 – V180 (low)	0.83	
T3 - T4	V180 (low) – VCORE	2.39	
TX62-W-B			
T0 - T1	AT^SMSO="fast" – URC	3.48	ms
T1 - T2	URC – V180	8.87	
T2 - T3	V180 – V180 (low)	0.67	
T3 - T4	V180 (low) – VCORE	2.53	
TX62-W-C			
T0 - T1	AT^SMSO="fast" – URC	4.62	ms
T1 - T2	URC – V180	12.28	
T2 - T3	V180 – V180 (low)	0.59	
T3 - T4	V180 (low) – VCORE	2.62	
TX82-W			
T0 - T1	AT^SMSO="fast" – URC	3.34	ms
T1 - T2	URC – V180	1.51	
T2 - T3	V180 – V180 (low)	0.83	
T3 - T4	V180 (low) – VCORE	2.39	
TX82-W-B			
T0 - T1	AT^SMSO="fast" – URC	TBD.	ms
T1 - T2	URC – V180	TBD.	
T2 - T3	V180 – V180 (low)	TBD.	
T3 - T4	V180 (low) – VCORE	TBD.	

4.2.5 Automatic Shutdown

Automatic shutdown takes effect if the following event occurs:

- The Cinterion® TX62/TX82 board is exceeding the critical limits of overtemperature or undertemperature (see Section 4.2.5.1)
- Undervoltage or overvoltage is detected (see Section 4.2.5.2 and Section 4.2.5.3)

The automatic shutdown procedure is equivalent to the power-down initiated with an AT command, i.e. Cinterion® TX62/TX82 logs off from the network and the software enters a secure state avoiding loss of data.

4.2.5.1 Thermal Shutdown

The board temperature is constantly monitored by an internal NTC resistor located on the PCB. The values detected by the NTC resistor are measured directly on the board and therefore, are not fully identical with the ambient temperature.

Each time the board temperature goes out of range or back to normal, Cinterion® TX62/TX82 instantly displays an alert (if enabled).

- URCs indicating the level "1" or "-1" allow the user to take appropriate precautions, such as protecting the module from exposure to extreme conditions. The presentation of the URCs depends on the settings selected with the AT^SCTM write command (for details see):

  AT^SCTM=1: Presentation of URCs is always enabled.

  AT^SCTM=0 (default): Presentation of URCs is enabled during the 2 minute guard period after start-up of Cinterion® TX62/TX82. After expiry of the 2 minute guard period, the presentation of URCs will be disabled, i.e. no URCs with alert levels "1" or "-1" will be generated.
- URCs indicating the level "2" or "-2" are followed by an orderly shutdown after 5 seconds unless the temperature returns to a valid operating level ("1", "0", "-1") or or the shutdown ability was disabled with AT^SCFG, "MEopMode/ShutdownOnCritTemp",<sdoct>. The presentation of these URCs is always enabled, i.e. they will be output even though the factory setting AT^SCTM=0 was never changed.

The maximum temperature ratings are stated in Section 4.5. Refer to Table 22 for the associated URCs.

Table 22: **Temperature dependent behavior**

Sending temperature alert (2min after module start-up, otherwise only if URC presentation enabled)	
^SCTM_B: 1	Board close to overtemperature limit.
^SCTM_B: -1	Board close to undertemperature limit.
^SCTM_B: 0	Board back to non-critical temperature range.
Automatic shutdown after 5 seconds (URC appears no matter whether presentation was enabled or not)	
^SCTM_B: 2	Alert: Board equal or beyond overtemperature limit. Cinterion® TX62/TX82 switches off.
^SCTM_B: -2	Alert: Board equal or below undertemperature limit. Cinterion® TX62/TX82 switches off.

4.2.5.2 Undervoltage Shutdown

The undervoltage shutdown threshold is the specified minimum supply voltage V_{BATT+} given in Table 4. When the average supply voltage measured by Cinterion® TX62/TX82 approaches the undervoltage shutdown threshold (i.e., 0.05V offset) the module will send the following URC:

^SBC: Undervoltage

If the undervoltage persists the module will send the URC several times before switching off automatically.

This type of URC does not need to be activated by the user. It will be output automatically when fault conditions occur.

Note:

For battery powered applications it is strongly recommended to implement a BATT+ connecting circuit in order to not only be able save power, but also to restart the module after an undervoltage shutdown where the battery is deeply discharged. Also note that the undervoltage threshold is calculated for max. 400mV voltage drops during transmit burst. Power supply sources for external applications should be designed to tolerate 400mV voltage drops without crossing the lower limit of 3.3V. For external applications operating at the limit of the allowed tolerance the default undervoltage threshold may be adapted by subtracting an offset. For details see [1]: AT^SCFG= "MEShutdown/sVsup/threshold".

4.2.5.3 Overvoltage Shutdown

The overvoltage shutdown threshold is the specified maximum supply voltage V_{BATT+} given in Table 4. When the average supply voltage measured by Cinterion® TX62/TX82 approaches the overvoltage shutdown threshold (i.e., 0.05V offset) the module will send the following URC:

^SBC: Overvoltage Warning

The overvoltage warning is sent only once - until the next time the module is close to the overvoltage shutdown threshold.

If the voltage continues to rise above the specified overvoltage shutdown threshold, the module will send the following URC:

^SBC: Overvoltage Shutdown

This alert is sent only once before the module shuts down cleanly without sending any further messages.

This type of URC does not need to be activated by the user. It will be output automatically when fault conditions occur.

Keep in mind that several Cinterion® TX62/TX82 components are directly linked to BATT+ and, therefore, the supply voltage remains applied at major parts of Cinterion® TX62/TX82. Especially the power amplifier linked to $BATT+_{RF}$ is sensitive to high voltage and might even be destroyed.

4.3 Power Saving

Cinterion® TX62/TX82 can control its power consumption through specific features as summarized in Table 23, and further detailed in the following sections. The mentioned operating modes are detailed in Section 4.1. For typical power supply ratings during power saving please refer to Section 4.4.1.

Table 23: **Power saving features**

Module operation mode	Network actions	Power Saving Features
Normal operation (network connected)		
Data transfer	Active transfer	Radio Output Power Reduction (ROPR) for GSM only
IDLE	DRX paging	Paging cycles based on DRX values provided by network
	eDRX paging	Paging cycles based on eDRX values negotiated with network
	3GPP PSM paging	Paging cycles based on 3GPP PSM values negotiated with network
Low power operation (network connected)		
SLEEP	DRX paging	Serial interface (ASC0, ASC1) shut down - except for RTS0/1 available as possible wakeup signal Paging cycles based on DRX values provided by network
	eDRX paging	Serial interface (ASC0, ASC1) shut down - except for RTS0/1 available as possible wakeup signal Paging cycles based on provided DRX and negotiated eDRX values
	3GPP PSM paging	Serial interface (ASC0, ASC1) shut down - except for RTS0/1 available as possible wakeup signal Paging cycles based on provided DRX, negotiated optional eDRX, as well as 3GPP PSM values
SUSPEND	DRX paging	All components shut down - except for RTC and certain signal triggered wake-up mechanisms Paging cycles based on DRX values provided by network
	eDRX paging	All components shut down - except for RTC and certain signal triggered wake-up mechanisms Paging cycles based on provided DRX and negotiated eDRX values
	3GPP PSM paging	All components shut down - except for RTC and certain signal triggered wake-up mechanisms Paging cycles based on provided DRX, negotiated optional eDRX, as well as 3GPP PSM values

Table 23: Power saving features

Module operation mode	Network actions	Power Saving Features
No network connection		
Airplane	--	Module radio part shut down
POWER DOWN	--	Module switched off. Standby state with BATT+ connected
Power off	--	Module switched off. BATT+ not connected

4.3.1 Low Power Modes

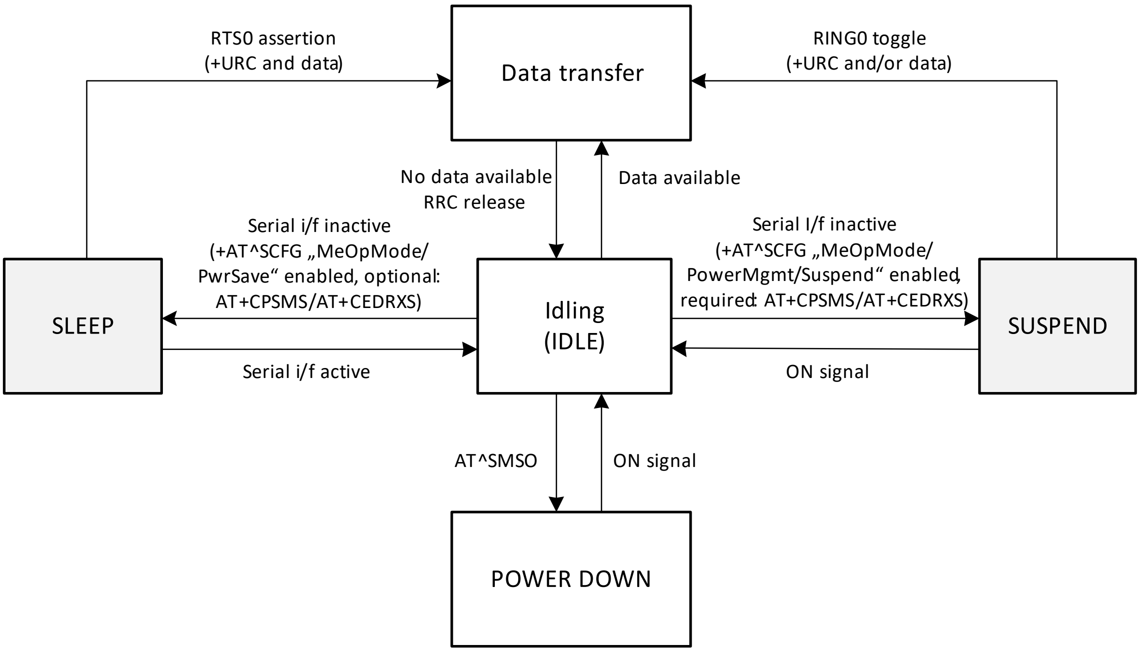


Figure 41: **Low power modes with state transitions**

Notes:

- When all serial interfaces (i.e. ASC0, and ASC1) are idle, the module can enter SLEEP or SUSPEND mode depending on additional configuration settings.
- The serial interfaces are not idle if there is any response message not read out from any of them.

4.3.1.1 Sleep Mode

SLEEP mode is a module's low power mode when no call is in progress and there is no active communication on any serial interface (ASC0, ASC1). During SLEEP mode, the serial interfaces are shut down except for RTS0 that may be used to wake up Cinterion® TX62/TX82 from SLEEP mode (see below). The module is in a low power consumption state depending on paging cycles based on network defined DRX values, and possibly network negotiated eDRX (extended DRX) as well as 3GPP PSM values - if configured.

The firmware is active to a minimum extent, and preserves the state it was in before entering the SLEEP mode. The module stays registered to the network.

For details on the network based DRX values see Section 4.3.2.1 (GSM/(E)GPRS) and Section 4.3.3.1 (LTE M1 NB1/2). For details on the network negotiated eDRX values see Section

4.3.3.2, for network negotiated 3GPP PSM values see Section 4.3.3.2.

The SLEEP mode option an be enabled/disabled by AT command (see [1]: AT^SCFG parameter "MEopMode/PwrSave").

RTS0 can be used to wake up Cinterion® TX62/TX82 from SLEEP mode between paging cycles. Assertion of RTS0 (i.e., toggle from inactive high to active low) serves as wake up event, thus allowing an external application to almost immediately terminate power saving. After RTS0 assertion, the CTS0 line signals module wake up, i.e., readiness of the AT command interface. It is therefore recommended to enable RTS/CTS flow control (default setting). Figure 42 shows the described RTS0 wake up mechanism.

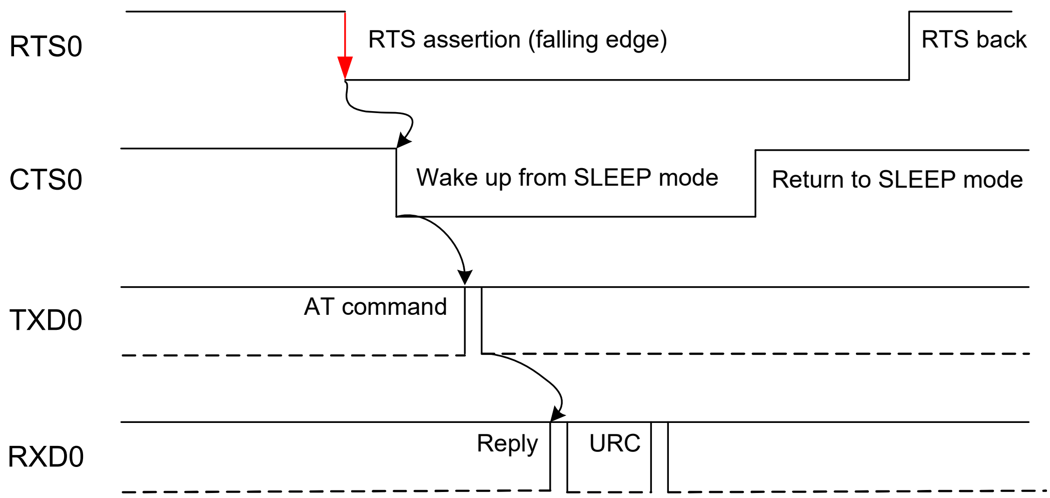


Figure 42: **Wake-up via RTS0**

4.3.1.2 Suspend Mode

In contrast to SLEEP mode, SUSPEND mode is a module's low power mode with almost all components switched off - except for the internal RTC and interrupt triggered wake up mechanisms. The module stays registered to the network, and the RRC connection is released. The module is in its lowest power consumption state.

Once the SUSPEND mode is enabled via AT command (see MEopMode/PowerMgmt/Suspend"), and the appropriate SUSPEND mode indicators are enabled (see PSM and possible eDRX settings need to be negotiated with the network. eDRX and PSM network settings are described in more detail in Section 4.3.2.2 and Section 4.3.3.2.

If the PSM settings are agreed upon with the network, Cinterion® TX62/TX82 is able to enter SUSPEND mode, and the following AT^SIND URC is generated:

+CIEV: “suspendAvailable”,1

In addition, if there is no further communication with the network, and the module is ready to enter SUSPEND mode, the following AT^SIND URC is generated:

+CIEV: "suspendReady",1

Also, the SUSPEND_MON signal will turn low as soon as the module enters SUSPEND mode (see Section 3.1.12.5).

Figure 43 shows the handshake between external application, module and the network for entering SUSPEND mode or possibly SLEEP mode depending on configuration and network response.

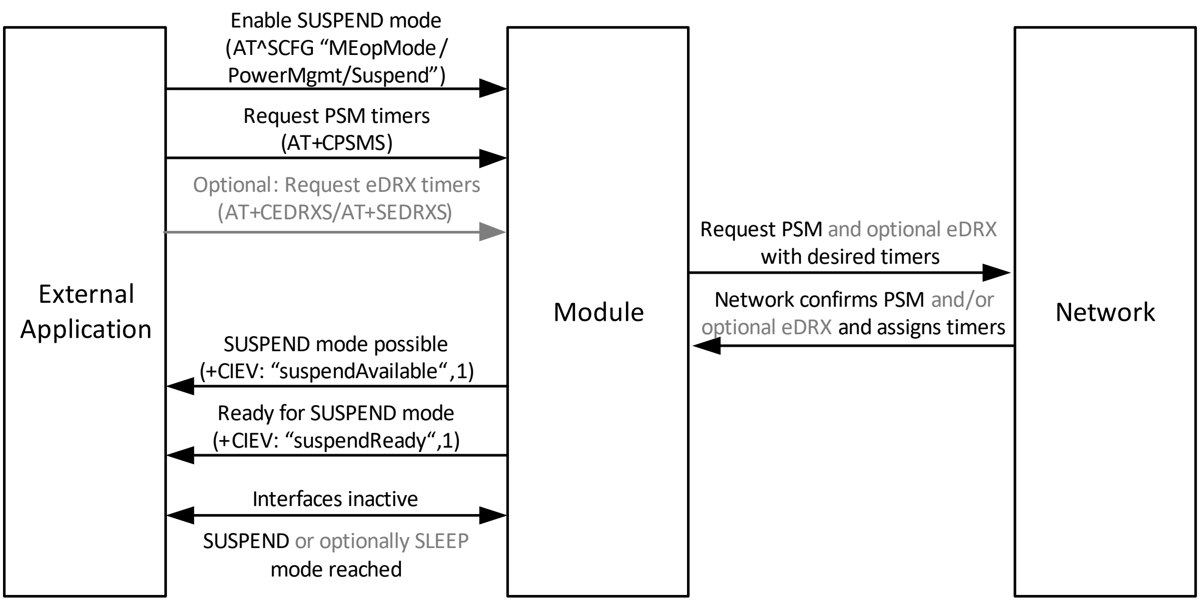


Figure 43: Handshake for entering the module’s SUSPEND mode

From SUSPEND mode the module can only be woken up by the ON or EMERG_RST signals, or it may wake up and be reachable again after expiration of a negotiated 3GPP PSM periodic TAU cycle (i.e., network timer) that may include DRX as well as eDRX paging cycles for an inactivity period (see Section 4.3.2.2 for details).

The module wakes up with its signal states being the same as the first startup configuration (see Section 4.2.3), and does not preserve the signal states it had in before entering SUSPEND mode.

Figure 44 shows the handshake between external application, module and network for waking up the module via ON/EMERG_RST signal.

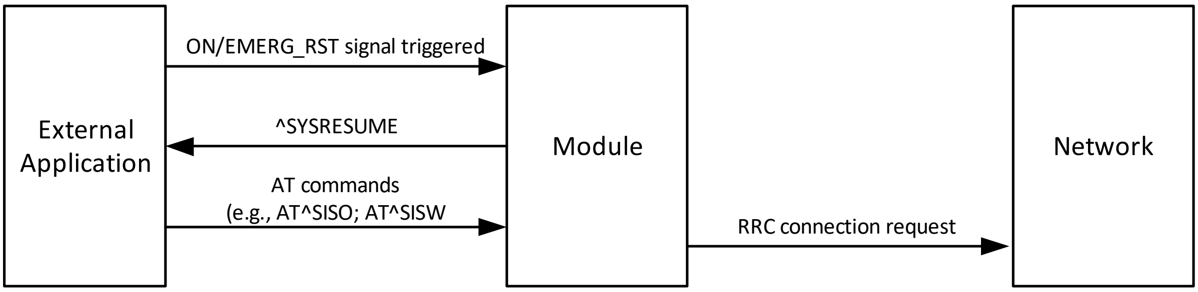


Figure 44: **Handshake for module wake up via ON signal**

Figure 45 shows the handshake between external application, module and network for waking up the module after expiry of the 3GPP PSM periodic TAU cycle (Tracking Area Update).

In this case the module automatically wakes up, and is reachable by the network to receive data (e.g., an SMS). The module wakeup can be indicated to the external application by toggling the RING0 line. See [1] for the AT^SGPICFG command to control the RING0 logic level. The external application should now activate the appropriate communication interfaces to wake up the module from SUSPEND mode, to receive the ^SYSRESUME URC, and to be able to transfer data.

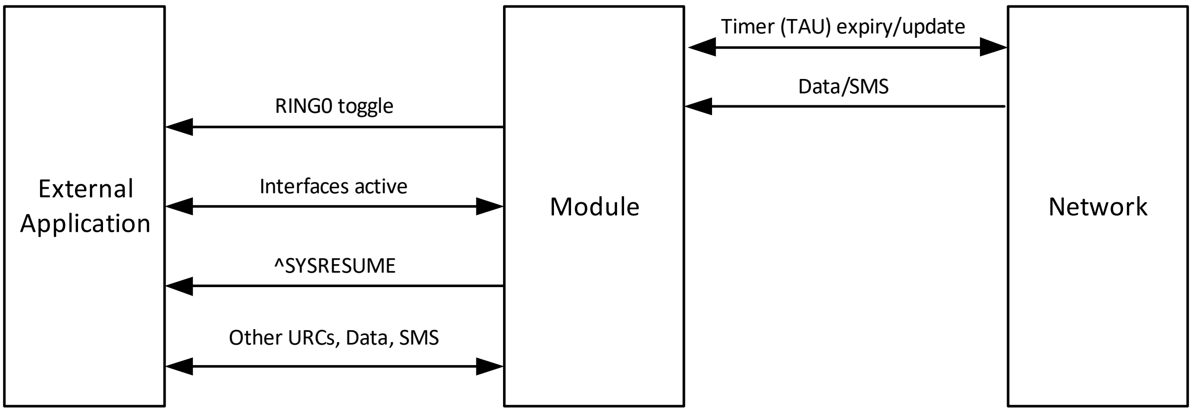


Figure 45: **Handshake for module wake up after eDRX/PSM timer expiry**

4.3.2 Power Saving while Attached to GSM Networks (TX82-W only)

Power saving while attached to GSM networks is based on standard DRX values defined for the network (see Section 4.3.2.1).

Apart from network based power saving it is possible to use the AT command AT^SCFG="Radio/OutputPowerReduction" for the module in (E)GPRS multislot scenarios to reduce its output power according to 3GPP 45.005. By default a maximum power reduction is enabled. For details on this AT command see [1].

4.3.2.1 DRX (Standard Configuration)

The power saving possibilities while attached to a GSM network depend on the paging timing cycle of the base station. The duration of a power saving interval can be calculated using the following formula:

t = 4.615 ms (TDMA frame duration) * 51 (number of frames) * DRX value.

DRX (Discontinuous Reception) is a value from 2 to 9, resulting in paging intervals between 0.47 and 2.12 seconds. The DRX value of the base station is assigned by the GSM network operator.

In the pauses between listening to paging messages, the module resumes power saving, i.e., SLEEP mode, as shown in Figure 46.

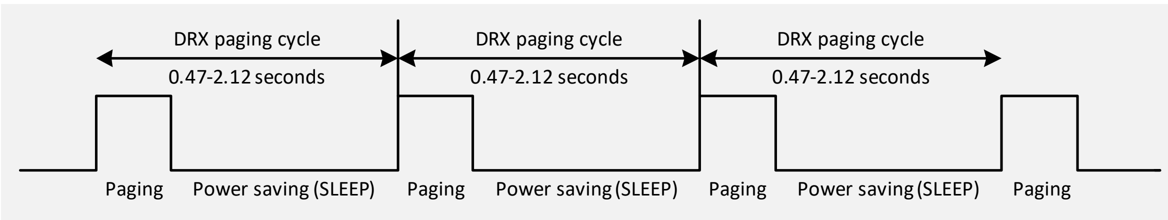


Figure 46: DRX based paging and power saving (SLEEP) in GSM networks

The varying pauses explain the different potential for power saving during SLEEP mode. The longer the pause the less power is consumed.

Generally, power saving depends on the module's application scenario and may differ from the above mentioned normal operation. The power saving interval may be shorter than 0.47 seconds or longer than 2.12 seconds.

4.3.2.2 eDRX (Extended DRX Configuration)

Note:

eDRX support for GSM networks is disabled. eDRX support is only available for LTE Cat M1 and Cat NB1/2 networks (see Section 4.3.3.2).

4.3.3 Power Saving while Attached to LTE M1 NB1/2 Networks

Cinterion® TX62/TX82 can be enabled to use DRX (Discontinuous Reception) in RRC idle mode to reduce power consumption (see also Section 4.3.1.1). The power saving possibilities while attached to an LTE Cat M1 or LTE Cat NB1/2 network depend on the paging timing cycle of the base station.

During normal operation, i.e., the module is connected to an LTE Cat M1 or LTE Cat NB1/2 network, the duration of power saving period varies. It may be calculated using the following formula:

t = DRX Cycle Value * 10 ms

DRX cycle value in LTE Cat M1 or LTE Cat NB1/2 networks is any of the four values: 128, 256, 512 and 1024, thus resulting power saving intervals between 1.28 and 10.24 seconds. The DRX cycle value of the base station is assigned by the network operator.

In the pauses between listening to paging messages, the module resumes power saving, as shown in Figure 47.

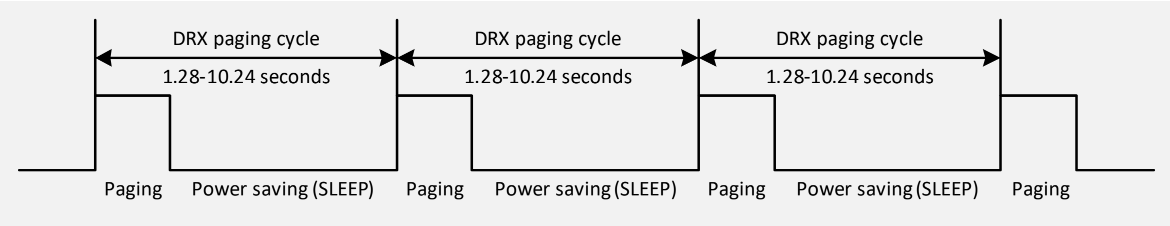


Figure 47: **DRX based paging and power saving (SLEEP) in LTE Cat M1 and Cat NB1/2 networks**

The varying pauses explain the different potential for power saving (SLEEP mode). The longer the pause the less power is consumed.

4.3.3.1 eDRX (Extended DRX Configuration)

Cinterion® TX62/TX82 and the network may negotiate the use of eDRX (extended DRX) to reduce power consumption, while being available for mobile terminating data and/or network originated procedures within a certain delay dependent on the network negotiated eDRX cycle value (see also Section 4.3.1.2). If the network supports eDRX, the module monitors the paging messages during a periodic Paging Time Window (PTW) configured for Cinterion® TX62/TX82.

The possible eDRX paging cycle length (PCL) ranges from 5.12s up to a maximum of 10485.76s (almost 3 hours).

The PTW length can be calculated using the following formula:

t_ptw = (PTW value +1)* 2560 ms

Figure 48 shows the eDRX timings, with the module listening to paging messages during a paging time window (PTW).

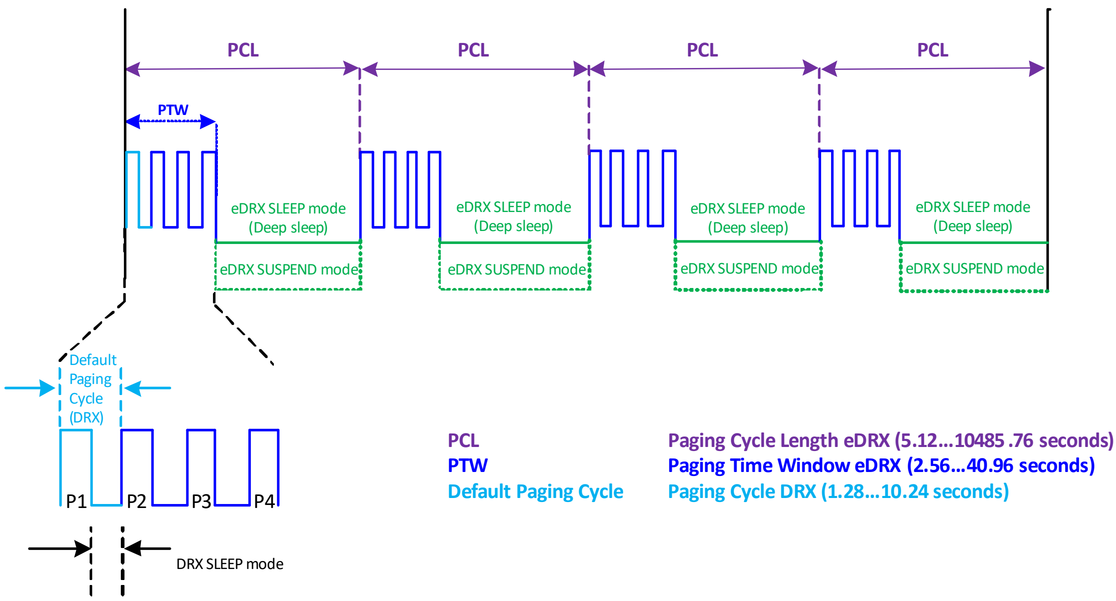


Figure 48: **eDRX based paging and power saving in LTE Cat M1 and Cat NB1/2 networks**

The eDRX timer can be configured with AT+CEDRXS (or AT^SEDRXS with a PTW timer request) that negotiates the eDRX settings with the network. The dynamic parameters are readable with AT+CEDRXRDP. For more information on these AT commands see [1].

Note:

If SUSPEND mode is enabled in addition to SLEEP mode and eDRX settings, the module is able to reduce its current consumption even further during the eDRX paging cycle (see Figure 48). This optional so-called eDRX SUSPEND mode can be enabled with the AT^SCFG command “Radio/Suspend,<suspendmode>”. However, the module will in this case not longer be able to change into the regular 3GPP PSM SUSPEND mode.

Note:

eDRX can be configured together with 3GPP PSM (AT+CPSMS) as it will not only affect SLEEP mode (deep sleep) and eDRX SUSPEND mode, but also the 3GPP PSM SUSPEND mode - see Section 4.3.3.2.

4.3.3.2 3GPP PSM Configuration

Cinterion® TX62/TX82 can be configured to use 3GPP PSM to reduce power consumption. PSM is similar to power off, while Cinterion® TX62/TX82 remains registered with the network. There is no need to re-attach or re-establish PDN connections. Cinterion® TX62/TX82 in PSM is not immediately reachable for mobile terminating services (see also SUSPEND mode in Section 4.3.1.2)

The network accepts and negotiates the use of PSM by providing specific values for periodic TAU cycles (T3412) as well as an active timer (T3324). Upon expiry of the active timer, or if the value provided by the network is zero, Cinterion® TX62/TX82 may activate PSM.

Note:

If Cinterion® TX62/TX82 negotiates to enable both PSM (requesting an active timer and possibly a periodic TAU cycle value) as well as eDRX (requesting a specific extended idle mode DRX cycle value and possibly a paging time window), it is up to the network to decide whether to:

1. Enable only PSM, i.e. not accept the request for eDRX.
2. Enable only eDRX, i.e. not accept the request for an active timer.
3. Enable both PSM (i.e. negotiate and provide requested PSM timers) and eDRX (i.e. negotiate and provide extended DRX parameters).

Figure 49 shows the module’s eDRX and PSM timings for the third case where module and network negotiate PSM and eDRX simultaneously (for eDRX see also Section 4.3.3.1). For the second case the module will not reach SUSPEND mode and will continue with the eDRX paging cycles. For the first case the module will not extend the DRX paging cycles, but will continue with the DRX paging cycles until the active timer (T3324) expires.

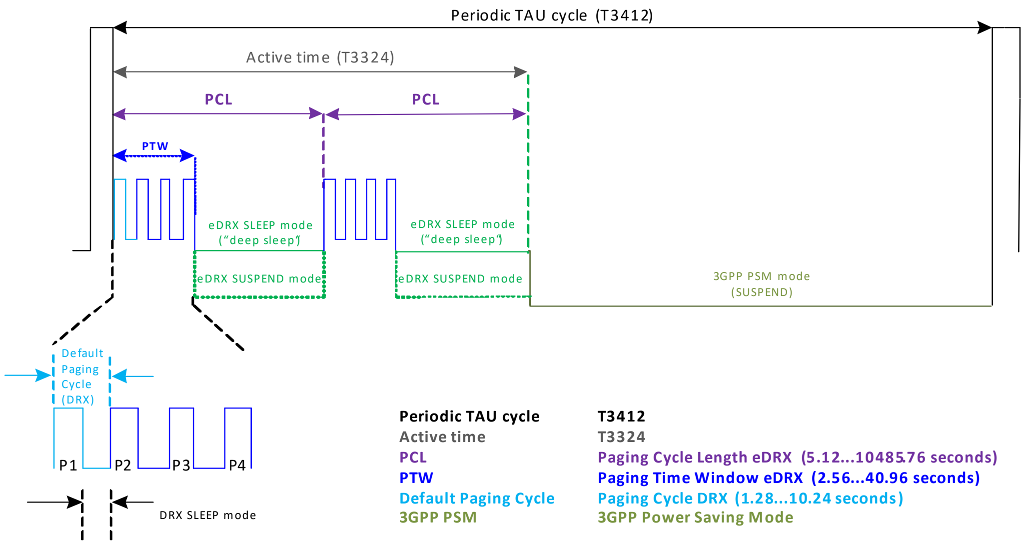


Figure 49: eDRX/PSM based paging and power saving in LTE Cat M1 or Cat NB1/2 networks

Cinterion® TX62/TX82 monitors paging message only while the active timer (T3324) has not expired. If the module has uplink data or signal, it will not change to PSM.

The active timer (T3324) and periodic tracking area update (TAU) timer (T3412) can be negotiated/requested with AT+CPSMS. For more information on this AT command see [1].

4.4 Power Supply

Cinterion® TX62/TX82 needs to be connected to a power supply at the SMT application interface - 2 lines BATT+, and GND. There are two separate voltage domains for BATT+:

- $BATT+_{BB}$ with a line mainly for the baseband power supply.
- $BATT+_{RF}$ with a line for the GSM/LTE power amplifier supply. Please note that this line does not have to be connected with TX62-W.

Note:

BATT+ in this document refers to both voltage domains and power supply lines - $BATT+_{BB}$ and $BATT+_{RF}$.

The power supply of Cinterion® TX62/TX82 has to be a single voltage source at $BATT+_{BB}$ and $BATT+_{RF}$. It should be of type PS1, according to IEC 62368-1, and must be able to provide the peak current during the uplink transmission.

Suitable low ESR capacitors should be placed as close as possible to the BATT+ pads, e.g., X7R MLCC (see also Section 3.1.2).

All key functions for supplying power to the device are handled by the power management IC. It provides the following features:

- Stabilizes the supply voltages for the baseband using low drop linear voltage regulators and a DC-DC step down switching regulator.
- Switches the module's power voltages for the power-up and -down procedures.
- SIM switch to provide SIM power supply.

4.4.1 Power Supply Ratings

Table 24, Table 25, Table 26, Table 27, and Table 28 assemble various voltage supply and current consumption ratings (GSM, Cat M1 and Cat NB1/2) of the module.

Table 24: **Voltage supply ratings**

	Description	Conditions	Min	Typ	Max	Unit
BATT+	TX82-W Supply voltage (LTE and GSM)	Directly measured at Module. Voltage must stay within the min/max values, including voltage drop, ripple, spikes For every BATT+ transition/ reinsertion from 0V, BATT+ should be at least 2.65V to power on the module.	3.1		4.6	V
	TX82-W Supply voltage (GSM deactivated)		2.8		4.6	V
	TX82-W-B Supply voltage (LTE and GSM)		3.1		4.5	V
	TX82-W-B Supply voltage (GSM deactivated)		2.9		4.5	V
	TX62-W Supply voltage		2.55		4.8	V
	TX62-W-B Supply voltage		2.5		4.5	V
	TX62-W-C Supply voltage		3.2		4.2	V
	Maximum allowed voltage drop during transmit burst	Normal condition, power control level for Pout max			400	mV
	Voltage ripple	Normal condition, power control level for Pout max @ f <= 250 kHz @ f > 250 kHz			70 20	mV_{pp} mV_{pp}

Table 25: General current consumption ratings (Cinterion® TX62/TX82)

Description		Conditions	Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., sum of $BATT+_{BB}$ and $BATT+_{RF}$[2])	OFF state supply current	State after initially connecting V_{BATT+} and/or after a fast shutdown triggered via FST_SHDN	14	14	32.5	14	TBD.	µA
		State after switching a running module off via AT^SMSO	4.5	4.5	22.7	4.5	TBD.	
	Airplane mode (CFUN = 4)	UART (RTS) active	12	12	9.59	12	TBD.	mA
		UART (RTS) inactive	0.55	0.54	0.49	0.49	TBD.	mA

1. With an impedance of Z_{LOAD}=50Ω at the antenna connector, Measured at 25°C at 3.8V.
2. $BATT+_{RF}$ is available with TX82-W only.

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description		Conditions			Typical rating					Unit
					TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE CAT-M1 supply current	SUSPEND[2]	Power save mode		3.8	4.5	21.5	4.5	TBD.	µA
		RRC idle (SLEEP)[2]	DRX=256		0.90	0.93	0.87	0.97	TBD.	mA
			DRX=128		1.34	1.40	1.24	1.44	TBD.	mA
			DRX=64		2.21	2.28	1.96	2.30	TBD.	mA
			20,48s eDRX	3,84s paging window (DRX=1.28s)	0.65	0.69	0.66	0.70	TBD.	mA
			81,92s eDRX	2,56s paging window (DRX=1.28s)	0.44	0.48	0.48	0.49	TBD.	mA
			163,84 s eDRX	3,84s paging window (DRX=1.28s)	0.43	0.46	0.47	0.47	TBD.	mA
			163,84 s eDRX	10,24s paging window (DRX=1.28s)	0.46	0.50	0.50	0.51	TBD.	mA
		RRC idle (SUS-PEND)[2]	81,92s eDRX	2,56s paging window (DRX=1.28s)	0.62	0.64	0.64	0.66	TBD.	mA
			163,84 s eDRX	3,84s paging window (DRX=1.28s)	0.33	0.34	0.34	0.35	TBD.	mA
			163,84 s eDRX	10,24s paging window (DRX=1.28s)	0.39	0.41	TBD.	0.42	TBD.	mA
		Con-nected DRX[3]	Short C-DRX		40	44	42	45	TBD.	mA
			Long C-DRX		10	13	12	14	TBD.	mA

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE CAT-M1 supply current	RRC connected Active Transmission[3] TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	226	242	-	TBD.	mA
			Band2, 23dBm	-	224	-	-	TBD.	mA
			Band3, 23dBm	-	230	198	-	TBD.	mA
			Band4, 23dBm	-	235	-	-	TBD.	mA
			Band5, 23dBm	-	228	-	-	TBD.	mA
			Band8, 23dBm	-	235	193	-	TBD.	mA
			Band12, 23dBm	-	208	-	-	TBD.	mA
			Band13, 23dBm	-	220	-	-	TBD.	mA
			Band18, 23dBm	-	216	-	-	TBD.	mA
			Band19, 23dBm	-	224	-	-	TBD.	mA
			Band20, 23dBm	-	225	187	-	TBD.	mA
			Band25, 23dBm	-	226	-	-	TBD.	mA
			Band26, 23dBm	-	226	-	-	TBD.	mA
			Band27, 23dBm	-	213	-	-	TBD.	mA
			Band28, 23dBm	-	214	199	-	TBD.	mA
			Band31, 26dBm (TX62-W-C)	-	-	235	-	-	mA
			Band66, 23dBm	-	231	-	-	TBD.	mA
			Band72, 26dBm (TX62-W-C)	-	-	229	-	-	mA
			Band85, 23dBm	-	201	-	-	TBD.	mA

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
		RRC connected Active Transmission[3]	Band1, 20dBm	168	-	-	177	-	mA
			Band2, 20dBm	177	-	-	171	-	mA
			Band3, 20dBm	167	-	-	172	-	mA
			Band4, 20dBm	167	-	-	169	-	mA
			Band5, 20dBm	175	-	-	187	-	mA
			Band8, 20dBm	180	-	-	180	-	mA
			Band12, 20dBm	173	-	-	164	-	mA
			Band13, 20dBm	183	-	-	185	-	mA
			Band18, 20dBm	179	-	-	184	-	mA
			Band19, 20dBm	180	-	-	186	-	mA
			Band20, 20dBm	179	-	-	185	-	mA
			Band25, 20dBm	176	-	-	173	-	mA
			Band26, 20dBm	183	-	-	180	-	mA
			Band27, 20dBm	181	-	-	183	-	mA
			Band28, 20dBm	170	-	-	185	-	mA
			Band66, 20dBm	166	-	-	169	-	mA
			Band85, 20dBm	168	-	-	164	-	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE CAT-M1 supply current	RRC connected Active Transmission[3]	Band1, 0dBm	103	131	112	105	TBD.	mA
			Band2, 0dBm	102	123	-	105	TBD.	mA
			Band3, 0dBm	103	120	110	105	TBD.	mA
			Band4, 0dBm	103	121	-	105	TBD.	mA
			Band5, 0dBm	102	129	-	105	TBD.	mA
			Band8, 0dBm	103	135	111	105	TBD.	mA
			Band12, 0dBm	102	126	-	104	TBD.	mA

Table 26: Current consumption ratings Cat M1 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
			Band13, 0dBm	102	135	-	105	TBD.	mA
			Band18, 0dBm	103	130	-	105	TBD.	mA
			Band19, 0dBm	102	130	-	106	TBD.	mA
			Band20, 0dBm	103	130	110	106	TBD.	mA
			Band25, 0dBm	103	124	-	105	TBD.	mA
			Band26, 0dBm	103	130	-	105	TBD.	mA
			Band27, 0dBm	103	130	-	105	TBD.	mA
			Band28, 0dBm	103	127	110	105	TBD.	mA
			Band31, 0dBm (TX62-W-C)	-	-	102	-	-	mA
			Band66, 0dBm	103	121	-	106	TBD.	mA
			Band72, 0dBm (TX62-W-C)	-	-	102	-	-	mA
			Band85, 0dBm	103	117	-	105	TBD.	mA

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
	Peak Current @ RRC connected Active Transmission[3] VBATT = 3.8V TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	576	644	-	TBD.	mA
		Band2, 23dBm	-	564	-	-	TBD.	mA
		Band3, 23dBm	-	576	520	-	TBD.	mA
		Band4, 23dBm	-	576	-	-	TBD.	mA
		Band5, 23dBm	-	552	-	-	TBD.	mA
		Band8, 23dBm	-	588	480	-	TBD.	mA
		Band12, 23dBm	-	516	-	-	TBD.	mA
		Band13, 23dBm	-	552	-	-	TBD.	mA
		Band18, 23dBm	-	528	-	-	TBD.	mA
		Band19, 23dBm	-	552	-	-	TBD.	mA
		Band20, 23dBm	-	552	488	-	TBD.	mA
		Band25, 23dBm	-	564	-	-	TBD.	mA
		Band26, 23dBm	-	552	-	-	TBD.	mA
		Band27, 23dBm	-	516	-	-	TBD.	mA
		Band28, 23dBm	-	516	482	-	TBD.	mA
		Band31, 26dBm (TX62-W-C)	-	-	596	-	-	mA
		Band66, 23dBm	-	564	-	-	TBD.	mA
		Band72, 26dBm (TX62-W-C)	-	-	600	-	-	mA
		Band85, 23dBm	-	504	-	-	TBD.	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Peak Current @ RRC connected Active Transmission[3] VBATT = 3.8V	Band1, 20dBm	412	-	-	408	-	mA
		Band2, 20dBm	396	-	-	388	-	mA
		Band3, 20dBm	388	-	-	376	-	mA
		Band4, 20dBm	376	-	-	396	-	mA
		Band5, 20dBm	396	-	-	456	-	mA
		Band8, 20dBm	452	-	-	428	-	mA

Table 26: Current consumption ratings Cat M1 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
			Band12, 20dBm	388	-	-	372	-	mA
			Band13, 20dBm	420	-	-	452	-	mA
			Band18, 20dBm	432	-	-	452	-	mA
			Band19, 20dBm	440	-	-	456	-	mA
			Band20, 20dBm	448	-	-	452	-	mA
			Band25, 20dBm	388	-	-	400	-	mA
			Band26, 20dBm	420	-	-	436	-	mA
			Band27, 20dBm	440	-	-	444	-	mA
			Band28, 20dBm	404	-	-	404	-	mA
			Band66, 20dBm	368	-	-	384	-	mA
			Band85, 20dBm	384	-	-	372	-	mA

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description		Conditions	Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
	Peak Current @ RRC connected Active Transmission[3] VBATT= 2.5V (TX62-W-B) VBATT= 3.2V (TX62-W-C) VBATT= TBD.V (TX82-W-B) TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	624	672	-	TBD.	mA
		Band2, 23dBm	-	648	-	-	TBD.	mA
		Band3, 23dBm	-	636	592	-	TBD.	mA
		Band4, 23dBm	-	624	-	-	TBD.	mA
		Band5, 23dBm	-	660	-	-	TBD.	mA
		Band8, 23dBm	-	612	528	-	TBD.	mA
		Band12, 23dBm	-	564	-	-	TBD.	mA
		Band13, 23dBm	-	648	-	-	TBD.	mA
		Band18, 23dBm	-	624	-	-	TBD.	mA
		Band19, 23dBm	-	612	-	-	TBD.	mA
		Band20, 23dBm	-	648	552	-	TBD.	mA
		Band25, 23dBm	-	612	-	-	TBD.	mA
		Band26, 23dBm	-	612	-	-	TBD.	mA
		Band27, 23dBm	-	588	-	-	TBD.	mA
		Band28, 23dBm	-	624	544	-	TBD.	mA
		Band31, 26dBm (TX62-W-C)	-	-	608	-	-	mA
		Band66, 23dBm	-	648	-	-	TBD.	mA
		Band72, 26dBm (TX62-W-C)	-	-	616	-	-	mA
		Band85, 23dBm	-	588	-	-	TBD.	mA

Table 26: **Current consumption ratings Cat M1 (Cinterion® TX62/TX82)**

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Peak Current @ RRC connected Active Transmission[3] VBATT = 2.55V (TX62-W) VBATT = 2.8V (TX82-W)	Band1, 20dBm	572	-	-	528	-	mA
		Band2, 20dBm	556	-	-	516	-	mA
		Band3, 20dBm	564	-	-	492	-	mA
		Band4, 20dBm	528	-	-	500	-	mA
		Band5, 20dBm	576	-	-	608	-	mA
		Band8, 20dBm	656	-	-	556	-	mA
		Band12, 20dBm	548	-	-	476	-	mA
		Band13, 20dBm	604	-	-	592	-	mA
		Band18, 20dBm	624	-	-	596	-	mA
		Band19, 20dBm	632	-	-	588	-	mA
		Band20, 20dBm	636	-	-	580	-	mA
		Band25, 20dBm	556	-	-	516	-	mA
		Band26, 20dBm	604	-	-	560	-	mA
		Band27, 20dBm	640	-	-	596	-	mA
		Band28, 20dBm	588	-	-	508	-	mA
		Band66, 20dBm	520	-	-	496	-	mA
		Band85, 20dBm	556	-	-	496	-	mA
	Average idle supply current (GNSS on)	CAT-M1 active (UART active) @ DRX=128 GNSS NMEA output off	65	66	65	64	TBD.	mA
		GPRS active (UART active) @ DRX=128 GNSS NMEA output on	64	66	65	64	TBD.	mA

1. With an impedance of Z_{LOAD}=50Ω at the antenna connector, Measured at 25°C at 3.8V.

2. Measurements start 6 minutes after switching ON the module,
   Averaging times:
   SUSPEND mode: 3 minutes (T3324 = 3s, T3412 = 14400s)
   SLEEP mode: 10 minutes, (PSM disabled, eDRX disabled)
   Idle eDRX mode: 30 minutes, (PSM disabled, eDRX enabled)
   Connected DRX mode: 10 minutes,
   RRC connected modes: 3 minutes,
   Communication tester settings: no neighbor cells, no cell re-selection etc, RMC (reference measurement channel), SUSPEND/SLEEP (with PSM/eDRX) is enabled via AT command
3. Communication tester settings:
   RMC mode, Half Duplex,
   Cat M1 Channel Bandwidth: 10MHz
   Modulation: QPSK.
   RB setting: 1 UL RBs, 4 DL RBs
4. TX62-W-C supports the following LTE Cat M1 bands only: Band 1, 3, 8, 20, 28, 31, 72

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description		Conditions			Typical rating					Unit
					TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	SUSPEND[2]	Power save mode		3.8	4.5	21.5	4.5	TBD.	µA
		RRC idle (SLEEP)[2]	DRX=1024		0.77	0.79	0.79	0.88	TBD.	mA
			DRX=512		1.08	1.09	1.17	1.17	TBD.	mA
			DRX=256		0.85	0.86	0.85	0.94	TBD.	mA
			DRX=128		1.23	1.26	1.20	1.35	TBD.	mA
			20,48s eDRX	2,56s paging window (DRX=1.28s)	1.15	1.14	1.20	1.19	TBD.	mA
			81,92s eDRX	2,56s paging window (DRX=1.28s)	0.49	0.49	0.58	0.55	TBD.	mA
			163,84s eDRX	2,56s paging window (DRX=1.28s)	0.45	0.44	0.53	0.50	TBD.	mA
			163,84s eDRX	10,24s paging window (DRX=1.28s)	0.51	0.50	0.56	0.59	TBD.	mA
		RRC idle (SUSPEND)[2]	81,92s eDRX	2,56s paging window (DRX=1.28s)	0.62	0.66	0.67	0.66	TBD.	mA
			163,84s eDRX	2,56s paging window (DRX=1.28s)	0.31	0.33	0.34	0.34	TBD.	mA
			163,84s eDRX	10,24s paging window (DRX=1.28s)	0.38	0.40	0.41	0.40	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
		RRC connected Active Transmission DL RMC[3] TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	77	79	-	TBD.	mA
			Band2, 23dBm	-	78	-	-	TBD.	mA
			Band3, 23dBm	-	77	75	-	TBD.	mA
			Band4, 23dBm	-	76	-	-	TBD.	mA
			Band5, 23dBm	-	76	-	-	TBD.	mA
			Band8, 23dBm	-	79	68	-	TBD.	mA
			Band12, 23dBm	-	74	-	-	TBD.	mA
			Band13, 23dBm	-	76	-	-	TBD.	mA
			Band18, 23dBm	-	75	-	-	TBD.	mA
			Band19, 23dBm	-	76	-	-	TBD.	mA
			Band20, 23dBm	-	77	70	-	TBD.	mA
			Band25, 23dBm	-	77	-	-	TBD.	mA
			Band26, 23dBm	-	75	-	-	TBD.	mA
			Band28, 23dBm	-	74	74	-	TBD.	mA
			Band31, 23dBm (TX62-W-C)	-	-	68	-	-	mA
			Band66, 23dBm	-	78	-	-	TBD.	mA
			Band71, 23dBm (TX62-W-B)	-	73	-	-	-	mA
			Band72, 23dBm (TX62-W-C)	-	-	68	-	-	mA
			Band85, 23dBm	-	74	-	-	TBD.	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	RRC connected Active Transmission DL RMC[3]	Band1, 20dBm	63	-	-	65	-	mA
			Band2, 20dBm	62	-	-	64	-	mA
			Band3, 20dBm	62	-	-	63	-	mA
			Band4, 20dBm	61	-	-	63	-	mA
			Band5, 20dBm	65	-	-	70	-	mA
			Band8, 20dBm	66	-	-	68	-	mA
			Band12, 20dBm	63	-	-	64	-	mA

Table 27: Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
			Band13, 20dBm	66	-	-	69	-	mA
			Band18, 20dBm	66	-	-	69	-	mA
			Band19, 20dBm	66	-	-	69	-	mA
			Band20, 20dBm	66	-	-	70	-	mA
			Band25, 20dBm	63	-	-	65	-	mA
			Band26, 20dBm	66	-	-	69	-	mA
			Band28, 20dBm	64	-	-	68	-	mA
			Band66, 20dBm	62	-	-	63	-	mA
			Band71, 20dBm	61	-	-	62	-	mA
			Band85, 20dBm	63	-	-	64	-	mA
			Band1, 0dBm	44	57	50	45	TBD.	mA
			Band2, 0dBm	43	55	-	45	TBD.	mA
			Band3, 0dBm	44	54	50	45	TBD.	mA
			Band4, 0dBm	44	54	-	44	TBD.	mA
			Band5, 0dBm	44	57	-	45	TBD.	mA
			Band8, 0dBm	44	58	50	45	TBD.	mA
			Band12, 0dBm	44	56	-	44	TBD.	mA
			Band13, 0dBm	44	58	-	45	TBD.	mA
			Band18, 0dBm	44	57	-	45	TBD.	mA
			Band19, 0dBm	44	57	-	45	TBD.	mA
			Band20, 0dBm	44	56	50	45	TBD.	mA
			Band25, 0dBm	44	55	-	44	TBD.	mA
			Band26, 0dBm	44	57	-	45	TBD.	mA
			Band28, 0dBm	44	56	50	45	TBD.	mA
			Band31, 0dBm (TX62-W-C)	-	-	50	-	-	mA
			Band66, 0dBm	44	54	-	45	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
			Band71, 0dBm (TX62-W-B)	43	55	-	43	-	mA
			Band72, 0dBm (TX62-W-C)	-	-	49	-	-	mA
			Band85, 0dBm	44	56	-	44	TBD.	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3] TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	232	259	-	TBD.	mA
			Band2, 23dBm	-	237	-	-	TBD.	mA
			Band3, 23dBm	-	228	213	-	TBD.	mA
			Band4, 23dBm	-	221	-	-	TBD.	mA
			Band5, 23dBm	-	219	-	-	TBD.	mA
			Band8, 23dBm	-	240	187	-	TBD.	mA
			Band12, 23dBm	-	207	-	-	TBD.	mA
			Band13, 23dBm	-	218	-	-	TBD.	mA
			Band18, 23dBm	-	217	-	-	TBD.	mA
			Band19, 23dBm	-	223	-	-	TBD.	mA
			Band20, 23dBm	-	224	187	-	TBD.	mA
			Band25, 23dBm	-	235	-	-	TBD.	mA
			Band26, 23dBm	-	219	-	-	TBD.	mA
			Band28, 23dBm	-	207	207	-	TBD.	mA
			Band31, 23dBm (TX62-W-C)	-	-	177	-	-	mA
			Band66, 23dBm	-	258	-	-	TBD.	mA
			Band71, 23dBm (TX62-W-B)	-	203	-	-	-	mA
			Band72, 23dBm (TX62-W-C)	-	-	178	-	-	mA
			Band85, 23dBm	-	208	-	-	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
		RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3]	Band1, 20dBm	178	-	-	186	-	mA
			Band2, 20dBm	172	-	-	180	-	mA
			Band3, 20dBm	166	-	-	170	-	mA
			Band4, 20dBm	164	-	-	168	-	mA
			Band5, 20dBm	183	-	-	203	-	mA
			Band8, 20dBm	191	-	-	196	-	mA
			Band12, 20dBm	168	-	-	171	-	mA
			Band13, 20dBm	191	-	-	202	-	mA
			Band18, 20dBm	186	-	-	200	-	mA
			Band19, 20dBm	187	-	-	203	-	mA
			Band20, 20dBm	189	-	-	203	-	mA
			Band25, 20dBm	180	-	-	182	-	mA
			Band26, 20dBm	192	-	-	200	-	mA
			Band28, 20dBm	175	-	-	190	-	mA
			Band66, 20dBm	167	-	-	166	-	mA
			Band71, 20dBm	164	-	-	169	-	mA
			Band85, 20dBm	173	-	-	169	-	mA

Table 27: Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3]	Band1, 0dBm	61	111	80	62	TBD.	mA
			Band2, 0dBm	61	98	-	61	TBD.	mA
			Band3, 0dBm	61	93	77	61	TBD.	mA
			Band4, 0dBm	61	92	-	61	TBD.	mA
			Band5, 0dBm	60	107	-	60	TBD.	mA
			Band8, 0dBm	60	116	78	61	TBD.	mA
			Band12, 0dBm	58	102	-	59	TBD.	mA
			Band13, 0dBm	60	116	-	60	TBD.	mA
			Band18, 0dBm	60	107	-	60	TBD.	mA
			Band19, 0dBm	60	107	-	61	TBD.	mA
			Band20, 0dBm	60	107	77	61	TBD.	mA
			Band25, 0dBm	61	98	-	61	TBD.	mA
			Band26, 0dBm	60	107	-	60	TBD.	mA
			Band28, 0dBm	60	103	77	60	TBD.	mA
			Band31, 0dBm (TX62-W-C)	-	-	63	-	TBD.	mA
			Band66, 0dBm	61	92	-	62	TBD.	mA
			Band71, 0dBm	58	101	-	58	-	mA
			Band72, 0dBm (TX62-W-C)	-	-	63	-	TBD.	mA
			Band85, 0dBm	58	102	-	59	TBD.	mA

Table 27: Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	RRC connected Active Transmission UL RMC, multi-tone mode (12 subcarrier),15kHz spacing[3] TX62-W-B and TX62-W-C[4] only	Band1, 23dBm	-	71	73	-	TBD.	mA
			Band2, 23dBm	-	72	-	-	TBD.	mA
			Band3, 23dBm	-	72	69	-	TBD.	mA
			Band4, 23dBm	-	71	-	-	TBD.	mA
			Band5, 23dBm	-	70	-	-	TBD.	mA
			Band8, 23dBm	-	73	66	-	TBD.	mA
			Band12, 23dBm	-	69	-	-	TBD.	mA
			Band13, 23dBm	-	71	-	-	TBD.	mA
			Band18, 23dBm	-	70	-	-	TBD.	mA
			Band19, 23dBm	-	71	-	-	TBD.	mA
			Band20, 23dBm	-	72	66	-	TBD.	mA
			Band25, 23dBm	-	72	-	-	TBD.	mA
			Band26, 23dBm	-	70	-	-	TBD.	mA
			Band28, 23dBm	-	70	67	-	TBD.	mA
			Band31, 23dBm (TX62-W-C)	-	-	68	-	TBD.	mA
			Band66, 23dBm	-	72	-	-	TBD.	mA
			Band71, 23dBm (TX62-W-B)	-	68	-	-	-	mA
			Band72, 23dbm (TX62-W-C)	-	-	68	-	TBD.	mA
			Band85, 23dBm	-	70	-	-	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
		RRC connected Active Transmission UL RMC, multi-tone mode (12 subcarrier),15kHz spacing[3]	Band1, 20dBm	57	-	-	59	-	mA
			Band2, 20dBm	56	-	-	58	-	mA
			Band3, 20dBm	57	-	-	58	-	mA
			Band4, 20dBm	56	-	-	57	-	mA
			Band5, 20dBm	58	-	-	62	-	mA
			Band8, 20dBm	60	-	-	61	-	mA
			Band12, 20dBm	56	-	-	58	-	mA
			Band13, 20dBm	58	-	-	62	-	mA
			Band18, 20dBm	59	-	-	61	-	mA
			Band19, 20dBm	59	-	-	62	-	mA
			Band20, 20dBm	59	-	-	62	-	mA
			Band25, 20dBm	56	-	-	59	-	mA
			Band26, 20dBm	58	-	-	62	-	mA
			Band28, 20dBm	58	-	-	61	-	mA
			Band66, 20dBm	55	-	-	59	-	mA
			Band71, 20dBm	54	-	-	57	-	mA
			Band85, 20dBm	58	-	-	59	-	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Average LTE NB1/2 supply current	RRC connected Active Transmission UL RMC, multi-tone mode (12 subcarrier),15kHz spacing[3]	Band1, 0dBm	45	57	51	46	TBD.	mA
			Band2, 0dBm	45	55	-	46	TBD.	mA
			Band3, 0dBm	46	55	52	46	TBD.	mA
			Band4, 0dBm	45	55	-	46	TBD.	mA
			Band5, 0dBm	46	57	-	46	TBD.	mA
			Band8, 0dBm	46	58	52	46	TBD.	mA
			Band12, 0dBm	45	56	-	46	TBD.	mA
			Band13, 0dBm	46	58	-	46	TBD.	mA
			Band18, 0dBm	45	57	-	46	TBD.	mA

Table 27: Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)

Description		Conditions		Typical rating					Unit
				TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
			Band19, 0dBm	45	57	-	46	TBD.	mA
			Band20, 0dBm	46	57	52	47	TBD.	mA
			Band25, 0dBm	45	56	-	46	TBD.	mA
			Band26, 0dBm	46	57	-	46	TBD.	mA
			Band28, 0dBm	46	57	52	47	TBD.	mA
			Band31, 0dBm (TX62-W-C)	-	-	51	-	-	mA
			Band66, 0dBm	46	55	-	48	TBD.	mA
			Band71, 0dBm	44	55	-	45	-	mA
			Band72, 0dBm (TX62-W-C)	-	-	51	-	-	mA
			Band85, 0dBm	46	57	-	47	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
	Peak Current @ RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3] VBATT=3.8V TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	576	696	-	TBD.	mA
		Band2, 23dBm	-	588	-	-	TBD.	mA
		Band3, 23dBm	-	588	544	-	TBD.	mA
		Band4, 23dBm	-	588	-	-	TBD.	mA
		Band5, 23dBm	-	588	-	-	TBD.	mA
		Band8, 23dBm	-	600	432	-	TBD.	mA
		Band12, 23dBm	-	540	-	-	TBD.	mA
		Band13, 23dBm	-	576	-	-	TBD.	mA
		Band18, 23dBm	-	576	-	-	TBD.	mA
		Band19, 23dBm	-	588	-	-	TBD.	mA
		Band20, 23dBm	-	588	496	-	TBD.	mA
		Band25, 23dBm	-	576	-	-	TBD.	mA
		Band26, 23dBm	-	588	-	-	TBD.	mA
		Band28, 23dBm	-	552	496	-	TBD.	mA
		Band31, 23dBm (TX62-W-C)	-	-	456	-	-	mA
		Band66, 23dBm	-	588	-	-	TBD.	mA
		Band71, 23dBm (TX62-W-B)	-	540	-	-	-	mA
		Band72, 23dBm (TX62-W-C)	-	-	464	-	-	mA
		Band85, 23dBm	-	552	-	-	TBD.	mA
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Peak Current @ RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3] VBATT=3.8V	Band1, 20dBm	388	-	-	380	-	mA
		Band2, 20dBm	396	-	-	372	-	mA
		Band3, 20dBm	380	-	-	364	-	mA
		Band4, 20dBm	352	-	-	368	-	mA
		Band5, 20dBm	416	-	-	448	-	mA
		Band8, 20dBm	436	-	-	432	-	mA
		Band12, 20dBm	388	-	-	376	-	mA

Table 27: Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
		Band13, 20dBm	444	-	-	436	-	mA
		Band18, 20dBm	420	-	-	436	-	mA
		Band19, 20dBm	420	-	-	440	-	mA
		Band20, 20dBm	428	-	-	440	-	mA
		Band25, 20dBm	396	-	-	376	-	mA
		Band26, 20dBm	436	-	-	428	-	mA
		Band28, 20dBm	396	-	-	408	-	mA
		Band66, 20dBm	376	-	-	368	-	mA
		Band71, 20dBm	372	-	-	368	-	mA
		Band85, 20dBm	388	-	-	368	-	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
	Peak Current @ RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3] VBATT=2.5V (TX62-W-B) VBATT=3.2V (TX62-W-C) VBATT=TBD.V (TX82-W-B) TX62-W-B, TX82-W-B, and TX62-W-C[4] only	Band1, 23dBm	-	624	696	-	TBD.	mA
		Band2, 23dBm	-	612	-	-	TBD.	mA
		Band3, 23dBm	-	612	560	-	TBD.	mA
		Band4, 23dBm	-	612	-	-	TBD.	mA
		Band5, 23dBm	-	624	-	-	TBD.	mA
		Band8, 23dBm	-	648	464	-	TBD.	mA
		Band12, 23dBm	-	576	-	-	TBD.	mA
		Band13, 23dBm	-	624	-	-	TBD.	mA
		Band18, 23dBm	-	624	-	-	TBD.	mA
		Band19, 23dBm	-	624	-	-	TBD.	mA
		Band20, 23dBm	-	648	512	-	TBD.	mA
		Band25, 23dBm	-	612	-	-	TBD.	mA
		Band26, 23dBm	-	624	-	-	TBD.	mA
		Band28, 23dBm	-	588	536	-	TBD.	mA
		Band31, 23dBm (TX62-W-C)	-	-	464	-	-	mA
		Band66, 23dBm	-	624	-	-	TBD.	mA
		Band71, 23dBm (TX62-W-B)	-	576	-	-	-	mA
		Band72, 23dBm (TX62-W-C)	-	-	480	-	-	mA
		Band85, 23dBm	-	588	-	-	TBD.	mA

Table 27: **Current consumption ratings Cat NB1/2 (Cinterion® TX62/TX82)**

Description	Conditions		Typical rating					Unit
			TX62-W	TX62-W-B	TX62-W-C	TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., only $BATT+_{BB}$)	Peak Current @ RRC connected Active Transmission UL RMC, single tone mode (1subcarrier),15KHz spacing[3] VBATT = 2.55V (TX62-W) VBATT = 2.8V (TX82-W)	Band1, 20dBm	560	-	-	512	-	mA
		Band2, 20dBm	564	-	-	496	-	mA
		Band3, 20dBm	544	-	-	480	-	mA
		Band4, 20dBm	504	-	-	484	-	mA
		Band5, 20dBm	608	-	-	604	-	mA
		Band8, 20dBm	620	-	-	572	-	mA
		Band12, 20dBm	548	-	-	488	-	mA
		Band13, 20dBm	628	-	-	588	-	mA
		Band18, 20dBm	600	-	-	584	-	mA
		Band19, 20dBm	600	-	-	588	-	mA
		Band20, 20dBm	612	-	-	584	-	mA
		Band25, 20dBm	572	-	-	504	-	mA
		Band26, 20dBm	628	-	-	576	-	mA
		Band28, 20dBm	560	-	-	548	-	mA
		Band66, 20dBm	536	-	-	488	-	mA
		Band71, 20dBm	532	-	-	488	-	mA
		Band85, 20dBm	560	-	-	488	-	mA
	Average idle supply current (GNSS on)	LTE NB1/2 active (UART active) @ DRX=128 GNSS NMEA output off	57	65	65	64	TBD.	mA
		LTE NB1/2 active (UART active) @ DRX=128 GNSS NMEA output on	57	66	66	64	TBD.	mA

1. With an impedance of Z_{LOAD}=50Ω at the antenna connector, Measured at 25°C at 3.8V.

2. Measurements start 6 minutes after switching ON the module,
   Averaging times:
   SUSPEND mode: 3 minutes (T3324 = 3s, T3412 = 14400s)
   SLEEP mode: 10 minutes, (PSM disabled, eDRX disabled)
   Idle eDRX mode: 30 minutes, (PSM disabled, eDRX enabled)
   Connected DRX mode: 10 minutes,
   RRC connected modes: 3 minutes,
   Communication tester settings: no neighbor cells, no cell re-selection etc, RMC (reference measurement channel),
   SUSPEND/SLEEP (with PSM/eDRX) is enabled via AT command
3. Communication tester settings:
   Cat NB1/2 Channel Bandwidth: 10MHz
   Modulation: BPSK for 1 UL subcarrier mode, QPSK for multi-subcarrier mode
4. TX62-W-C supports the following LTE Cat NB1/2 bands only: Band 1, 3, 8, 20, 28, 31, 72. Note also that with TX62-W-C support for LTE Cat NB1/2 is by default deactivated, but may be activated on demand.

Table 28: Current consumption ratings General and GSM (TX82-W and TX82-W-B)

Description		Conditions		Typical rating		Unit
				TX82-W	TX82-W-B	
I_{BATT+}[1] (i.e., sum of $BATT+_{BB}$ and $BATT+_{RF}$)	Average GSM supply current (GNSS off)	SLEEP[2]@DRX=9 (no communication via UART)		0.78	TBD.	mA
		SLEEP[2]@DRX=5 (no communication via UART)		0.95	TBD.	mA
		SLEEP[2]@DRX=2 (no communication via UART)		1.58	TBD.	mA
		SLEEP[3]@DRX=2 (no communication via UART)		13	TBD.	mA
		GPRS Data transfer GSM850; PCL=5,1Tx/4RX	ROPR=8 (max. reduction)	230	TBD.	mA
			ROPR=4 (no reduction)	232	TBD.	mA
		GPRS Data transfer GSM850; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	323	TBD.	mA
			ROPR=4 (no reduction)	407	TBD.	mA
		EDGE Data transfer GSM850; PCL=5; 1Tx/4Rx	ROPR=8 (max. reduction)	155	TBD.	mA
			ROPR=4 (no reduction)	156	TBD.	mA
		EDGE Data transfer GSM850; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	231	TBD.	mA
			ROPR=4 (no reduction)	259	TBD.	mA
		GPRS Data transfer GSM900; PCL=5,1Tx/4RX	ROPR=8 (max. reduction)	260	TBD.	mA
			ROPR=4 (no reduction)	259	TBD.	mA

Table 28: Current consumption ratings General and GSM (TX82-W and TX82-W-B)

Description		Conditions		Typical rating TX82-W	Typical rating TX82-W-B	Unit
		GPRS Data transfer GSM900; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	328	TBD.	mA
			ROPR=4 (no reduction)	464	TBD.	mA
		EDGE Data transfer GSM900; PCL=5; 1Tx/4Rx	ROPR=8 (max. reduction)	153	TBD.	mA
			ROPR=4 (no reduction)	152	TBD.	mA
I_{BATT+}[1] (i.e., sum of $BATT+_{BB}$ and $BATT+_{RF}$)	Average GSM supply current (GNSS off)	EDGE Data transfer GSM900; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	229	TBD.	mA
			ROPR=4 (no reduction)	256	TBD.	mA
		GPRS Data transfer GSM1800; PCL=5,1Tx/4RX	ROPR=8 (max. reduction)	182	TBD.	mA
			ROPR=4 (no reduction)	183	TBD.	mA
		GPRS Data transfer GSM1800; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	248	TBD.	mA
			ROPR=4 (no reduction)	310	TBD.	mA
		EDGE Data transfer GSM1800; PCL=5; 1Tx/4Rx	ROPR=8 (max. reduction)	147	TBD.	mA
			ROPR=4 (no reduction)	145	TBD.	mA
		EDGE Data transfer GSM1800; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	226	TBD.	mA
			ROPR=4 (no reduction)	244	TBD.	mA
		GPRS Data transfer GSM1900; PCL=5,1Tx/4RX	ROPR=8 (max. reduction)	185	TBD.	mA
			ROPR=4 (no reduction)	184	TBD.	mA
		GPRS Data transfer GSM1900; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	250	TBD.	mA
			ROPR=4 (no reduction)	315	TBD.	mA

Table 28: Current consumption ratings General and GSM (TX82-W and TX82-W-B)

Description		Conditions		Typical rating TX82-W	Typical rating TX82-W-B	Unit
		EDGE Data transfer GSM1900; PCL=5; 1Tx/4Rx	ROPR=8 (max. reduction)	149	TBD.	mA
			ROPR=4 (no reduction)	147	TBD.	mA
		EDGE Data transfer GSM1900; PCL=5; 2Tx/3Rx	ROPR=8 (max. reduction)	229	TBD.	mA
			ROPR=4 (no reduction)	247	TBD.	mA
	Peak current during GSM transmit burst @ 3.8V	GPRS Data transfer GSM850; PCL=5; 2Tx/3Rx		1.79	TBD.	A
		GPRS Data transfer GSM900; PCL=5; 2Tx/3Rx		1.99	TBD.	A
		GPRS Data transfer GSM1800; PCL=0; 2Tx/3Rx		1.26	TBD.	A
		GPRS Data transfer GSM1900; PCL=0; 2Tx/3Rx		1.29	TBD.	A
I_{BATT+}[1] (i.e., sum of $BATT+_{BB}$ and $BATT+_{RF}$)	Peak current during GSM transmit burst @ 3.0V	GPRS Data transfer GSM850; PCL=5; 2Tx/3Rx		1.76	TBD.	A
		GPRS Data transfer GSM900; PCL=5; 2Tx/3Rx		1.76	TBD.	A
		GPRS Data transfer GSM1800; PCL=0; 2Tx/3Rx		1.23	TBD.	A
		GPRS Data transfer GSM1900; PCL=0; 2Tx/3Rx		1.26	TBD.	A
	Average GSM IDLE supply current (GNSS on)	GPRS active (UART active) @ DRX=2 GNSS NMEA output off		68	TBD.	mA
		GPRS active (UART active) @ DRX=2 GNSS NMEA output on		68	TBD.	mA

1. With an impedance of Z_{LOAD}=50Ω at the antenna connector, Measured at 25°C at 3.8V.
2. Measurements start 6 minutes after switching ON the module,
   Averaging times:
   OFF mode: 3 minutes
   SLEEP and IDLE mode - 10 minutes
   Transfer modes - 3 minutes
   Communication tester settings: no neighbor cells, no cell re-selection etc,, RMC (reference measurement channel), SLEEP mode is enabled via AT command
3. The power save mode (PSM) is disabled via AT command

4.4.2 Minimizing Power Losses

For TX82-W only: When designing the power supply for your application (and with GSM enabled) please pay specific attention to power losses. Ensure that the input voltage V_{BATT+} never drops below 3.1V on the TX82-W board, not even in a GSM transmit burst where current consumption can rise (for peak values see the power supply ratings listed in Section 4.4.1)

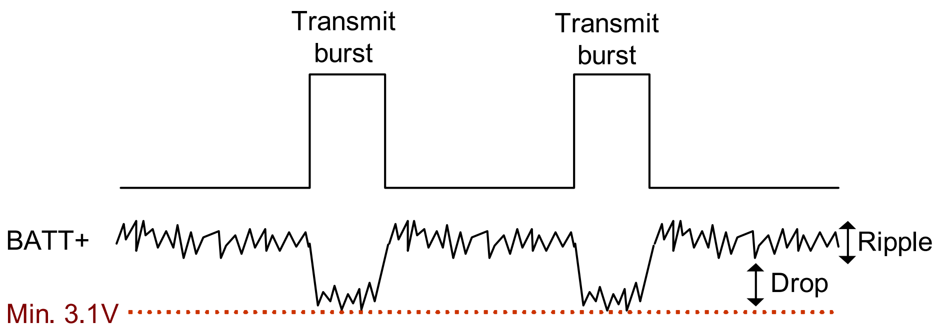


Figure 50: **Power supply limits during transmit burst**

4.4.3 Measuring the Supply Voltage (VBATT+)

To measure the supply voltage V_{BATT+} it is possible to define two reference points GND and BATT+. GND and BATT+ should be a test pad on the external application the module is mounted on. The eternal GND reference point has to be connected to and positioned close to the SMT application interface's GND pad F17 and the external BATT+ reference point has to be connected to and positioned close to the SMT application interface's BATT+ pads G15 and G16 ($BATT+_{RF}$) or H15 and H16 ($BATT+_{BB}$) as shown in Figure 51.

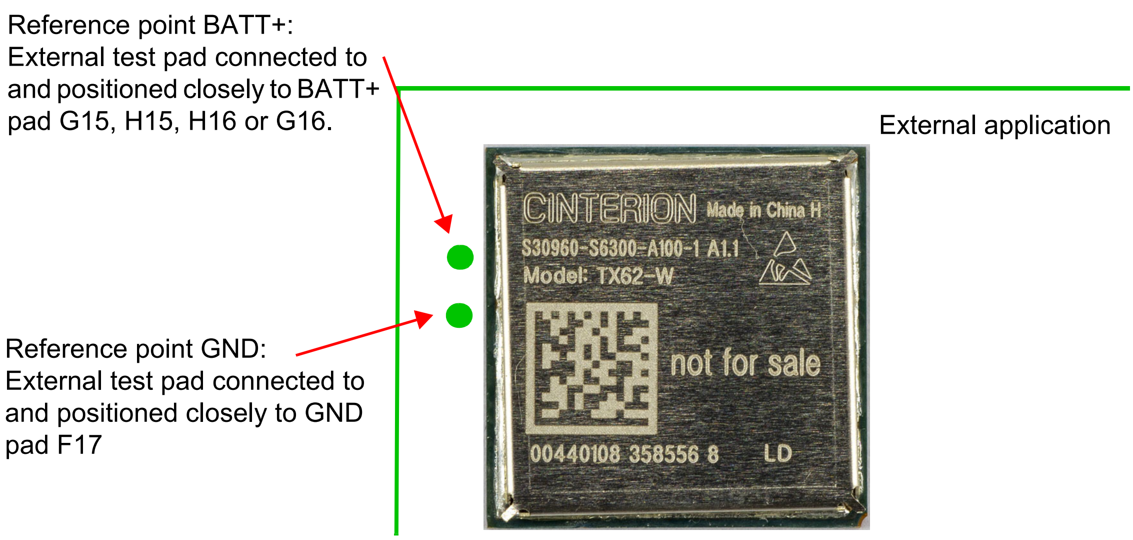


Figure 51: **Position of reference points BATT+ and GND**

4.4.4 Monitoring Power Supply by AT Command

To monitor the supply voltage you can also use the AT^SBV command which returns the value related to the reference points BATT+ and GND.

The module continuously measures the voltage at intervals depending on the operating mode of the RF interface. The duration of measuring ranges from 0.5 seconds in TALK/DATA mode to 50 seconds when Cinterion® TX62/TX82 is in IDLE mode or Limited Service (deregistered). The displayed voltage (in mV) is averaged over the last measuring period before the AT^SBV command was executed.

If the measured voltage drops below or rises above the voltage shutdown thresholds, the module will send an "^SBC" URC and shut down (for details see Section 4.2.5).

4.5 Operating Temperatures

Note:

The module's lifetime, i.e., the MTTF (mean time to failure) may be reduced, if operated outside the extended temperature range.

Table 29: **Board temperature**

Parameter	Min	Typ	Max	Unit
Normal operation	-30		+85	°C
Extended operation[1]	-40		+90	°C
Automatic shutdown[2] Temperature measured on Cinterion® TX62/TX82 board	<-40	---	>+90	°C

1. Extended operation allows normal mode speech calls or data transmission for limited time. Within the extended temperature range (outside the normal operating temperature range) the specified electrical characteristics may be in- or decreased.
2. Due to temperature measurement uncertainty, a tolerance of ±3°C on the thresholds may occur.

See also Section 4.2.5 for information about the NTC for on-board temperature measurement, automatic thermal shutdown and alert messages.

Note: Within the specified operating temperature ranges the board temperature may vary to a great extent depending on operating mode, used frequency band, radio output power and current supply voltage.

The below Table 30 lists the ambient temperature ranges the Cinterion® TX62/TX82 is able to operate in.

Table 30: **Ambient temperature**

Parameter	Min	Typ	Max	Unit
Normal operation (GSM)	-40		+60	°C
Normal operation (LTE)	-40		+70	°C

4.6 Electrostatic Discharge

The module is not protected against Electrostatic Discharge (ESD) in general. Consequently, it is subject to ESD handling precautions that typically apply to ESD sensitive components. Proper ESD handling and packaging procedures must be applied throughout the processing, handling and operation of any application that incorporates a Cinterion® TX62/TX82 module.

Special ESD protection provided on Cinterion® TX62/TX82:

Main antenna interface: Inductor/capacitor

BATT+: Inductor/capacitor

An example for an enhanced ESD protection for the SIM interface is given in Section 3.1.7.

Cinterion® TX62/TX82 has been tested according to group standard ETSI EN 301 489-1 (see Table 38). Electrostatic values can be gathered from the following table.

Table 31: **Electrostatic values**

Specification/Requirements	Contact discharge	Air discharge
ETSI EN 301 489-1		
Main antenna interface	± 4kV	± 8kV
BATT+	± 4kV	± 8kV
JEDEC JESD22-A114D (Human Body Model, Test conditions: 1.5 kΩ, 100 pF)		
All other interfaces	± 1kV	n.a.

Note:

The values may vary with the individual application design. For example, it matters whether or not the application platform is grounded over external devices like a computer or other equipment, such as the Telit Cinterion reference application described in Chapter 6.

4.6.1 ESD Protection for RF Antenna Interface

The following Figure 52 shows how to implement an external ESD protection for the RF antenna interface with either a T pad or PI pad attenuator circuit (for RF line routing design see also Section 3.2.3).

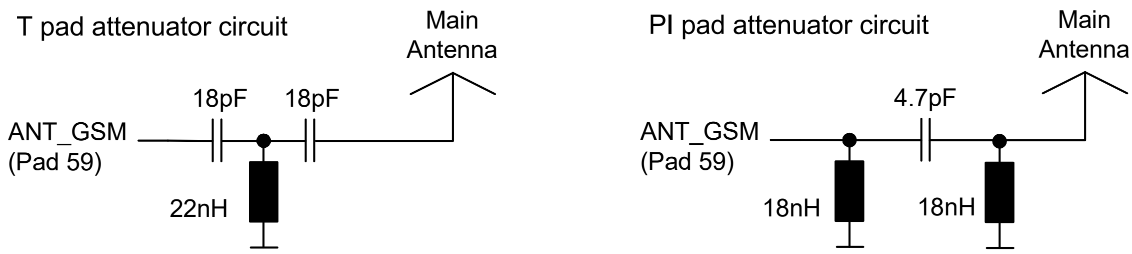


Figure 52: ESD protection for RF antenna interface

Recommended inductor types for the above sample circuits: Size 0402 SMD from Panasonic ELJRF series (22nH and 18nH inductors) or Murata LQW15AN18NJ00 (18nH inductors only).

4.7 Blocking against RF on Interface Lines

To reduce EMI issues there are serial resistors, or capacitors to GND, implemented on the module for the ignition, emergency restart, and SIM interface lines (Section 3.4). However, all other signal lines have no EMI measures on the module and there are no blocking measures at the module's interface to an external application.

Dependent on the specific application design, it might be useful to implement further EMI measures on some signal lines at the interface between module and application. These measures are described below.

There are five possible variants of EMI measures (A-E) that may be implemented between module and external application depending on the signal line (see Figure 53 and Table 32). Pay attention not to exceed the maximum input voltages and prevent voltage overshots if using inductive EMC measures.

The maximum value of the serial resistor should be lower than 1kΩ on the signal line. The maximum value of the capacitor should be lower than 50pF on the signal line. Please observe the electrical specification of the module's SMT application interface and the external application's interface.

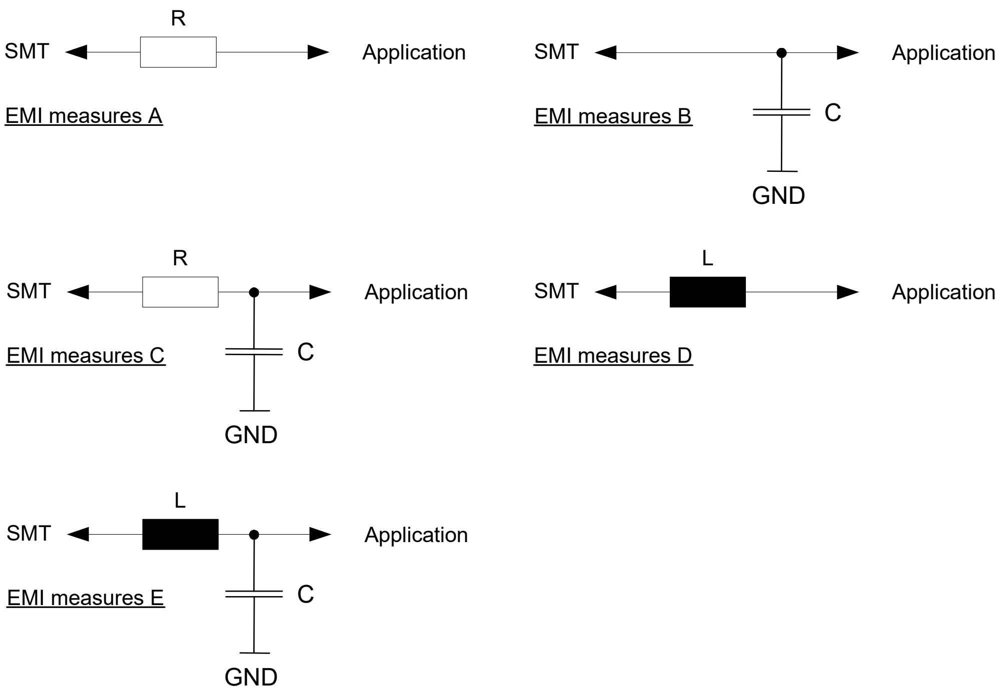


Figure 53: EMI circuits

Note:

In case the application uses an internal GSM/LTE antenna that is implemented close to the Cinterion® TX62/TX82 module, Telit Cinterion strongly recommends sufficient EMI measures, e.g. of type B or C, for each digital input or output.

The following table lists for each signal line at the module's SMT application interface the EMI measures that may be implemented.

Table 32: EMI measures on the application interface

Signal name	EMI measures					Remark
	A	B	C	D	E	
CCIN				x		
CCRST		x				The external capacitor should be not higher than 1nF. The value of the capacitor depends on the external application and should be placed close to SIM connector/eUICC.

Table 32: **EMI measures on the application interface**

Signal name	EMI measures					Remark
	A	B	C	D	E	
CCIO		x				The external capacitor should be not higher than 10pF. The value of the capacitor depends on the external application and should be placed close to SIM connector/ eUICC.
CCCLK		x				
VUSB		x		x	x	
RXD0	x	x	x	x	x	
TXD0	x	x	x	x	x	
CTS0	x	x	x	x	x	
RTS0	x	x	x	x	x	
DTR0	x	x	x	x	x	
DCD0	x	x	x	x	x	
DSR0	x	x	x	x	x	
RXD1	x	x	x	x	x	
TXD1	x	x	x	x	x	
RTS1	x	x	x	x	x	
CTS1	x	x	x	x	x	
RING0	x	x	x	x	x	
FST_SHDN	x	x	x	x	x	
STATUS	x	x	x	x	x	
SIM_SWITCH	x	x	x	x	x	
SUSPEND_MON	x	x	x	x	x	
GPIO6,7,20-23,25	x	x	x	x	x	
I2CDAT[1]		x		x		The rising signal edge is reduced with an additional capacitor.
I2CCLK[1]		x		x		
V180		x		x	x	
BATT+$_{RF}$ (pad G15, G16)		x	x			Measures required if BATT+$_{RF}$ is close to internal GSM antenna - e.g., 39pF blocking capacitor to ground
BATT+$_{BB}$ (pad H15, H16)		x	x			

1. Available with embedded processing option only.

4.8 Reliability Characteristics

The test conditions stated below are an extract of the complete test specifications.

Table 33: Summary of reliability test conditions

Type of test	Conditions	Standard
Vibration	Frequency range: 10-20Hz; acceleration: 5g Frequency range: 20-500Hz; acceleration: 20g Duration: 20h per axis; 3 axes	DIN IEC 60068-2-6[1]
Shock half-sinus	Acceleration: 500g Shock duration: 1ms 1 shock per axis 6 positions (± x, y and z)	DIN IEC 60068-2-27
Dry heat	Temperature: +70 ±2°C Test duration: 16h Humidity in the test chamber: < 50%	EN 60068-2-2 Bb ETS 300 019-2-7
Temperature change (shock)	Low temperature: -40°C ±2°C High temperature: +85°C ±2°C Changeover time: < 30s (dual chamber system) Test duration: 1h Number of repetitions: 100	DIN IEC 60068-2-14 Na ETS 300 019-2-7
Damp heat cyclic	High temperature: +55°C ±2°C Low temperature: +25°C ±2°C Humidity: 93% ±3% Number of repetitions: 6 Test duration: 12h + 12h	DIN IEC 60068-2-30 Db ETS 300 019-2-5
Cold (constant exposure)	Temperature: -40 ±2°C Test duration: 16h	DIN IEC 60068-2-1

1. For reliability tests in the frequency range 20-500Hz the Standard's acceleration reference value was increased to 20g.

5 Mechanical Dimensions, Mounting and Packaging

5.1 Mechanical Dimensions of TX62-W

Figure 54 shows the top and bottom view of TX62-W and provides an overview of the board's mechanical dimensions. For further details see Figure 55. Figure 56 shows the area at the module’s bottom side where possible markings might be printed.

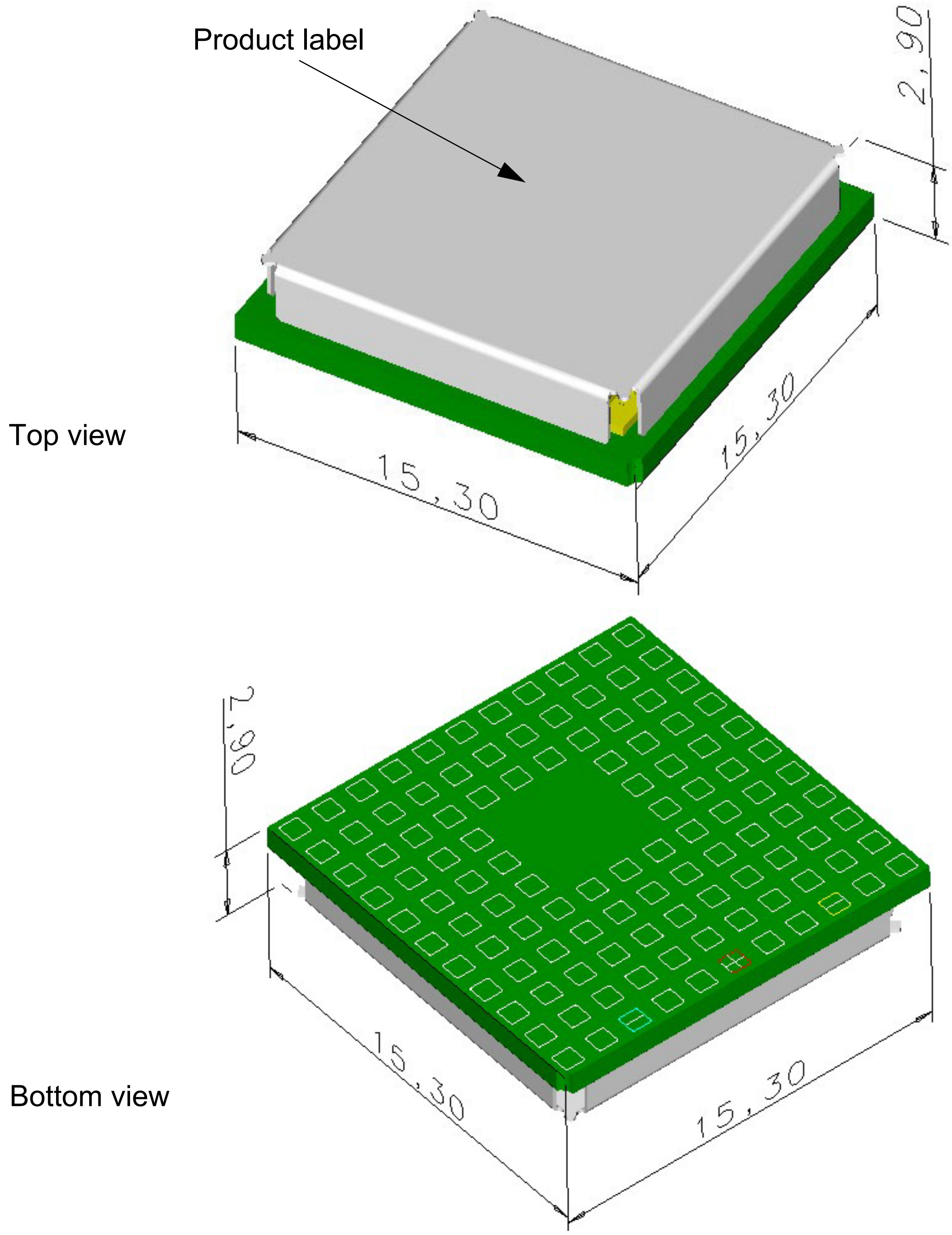


Figure 54: **TX62-W– top and bottom view**

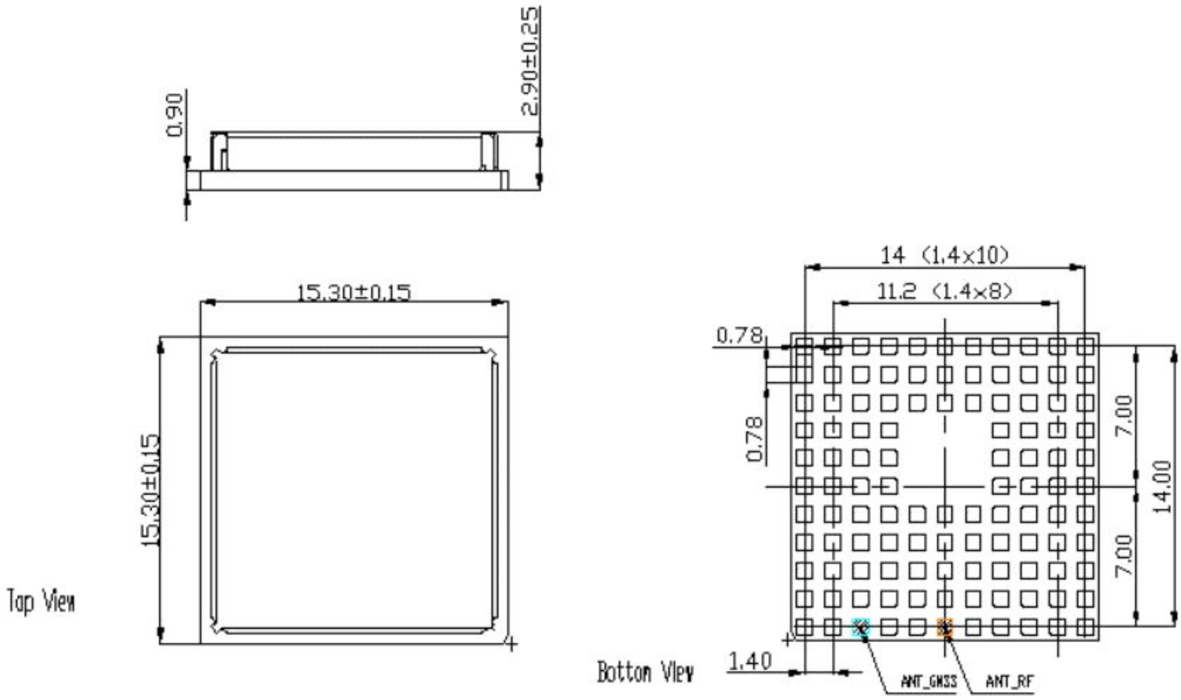


Figure 55: Dimensions of TX62-W (all dimensions in mm)

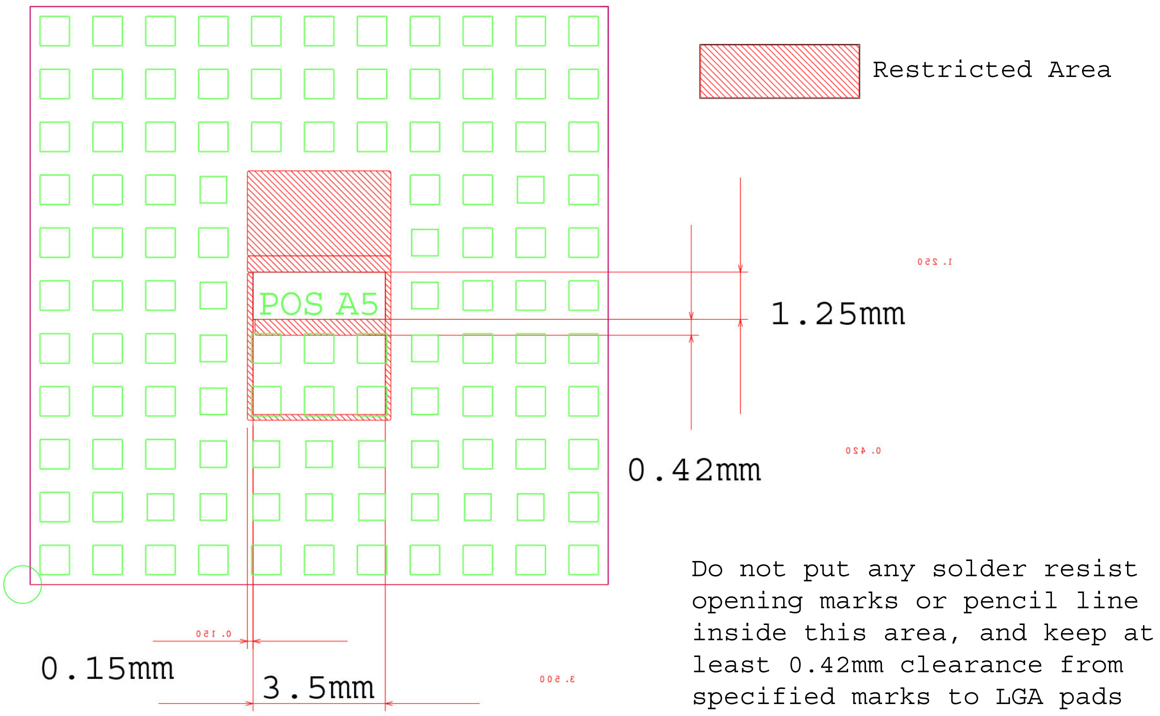


Figure 56: Dimensions of area for possible markings TX62-W (bottom view)

5.2 Mechanical Dimensions of TX82-W, TX82-W-B, TX62-W-B and TX62-W-C

Figure 57 shows the top and bottom view of TX82-W, TX82-W-B, TX62-W-B, and TX62-W-C, and provides an overview of the board's mechanical dimensions. For further details see Figure 59. Figure 60 shows the area at the module’s bottom side where possible markings might be printed.

Note:

Other then shown in the below 3D view (Figure 57) the module height for TX82-W and TX62-W-B is 2.30mm, and for TX82-W-B and TX62-W-C is 2.92mm.

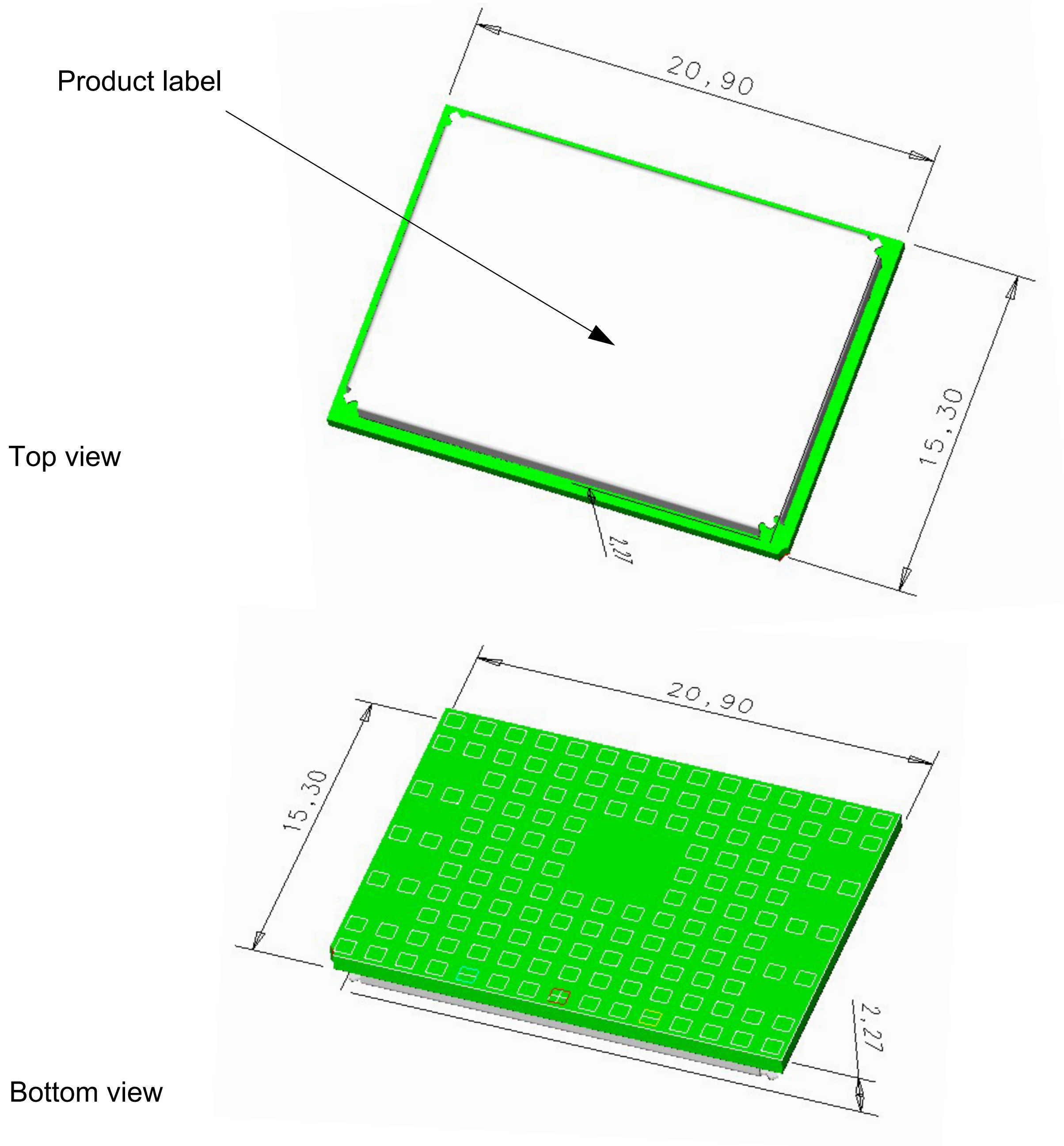


Figure 57: TX82– top and bottom view

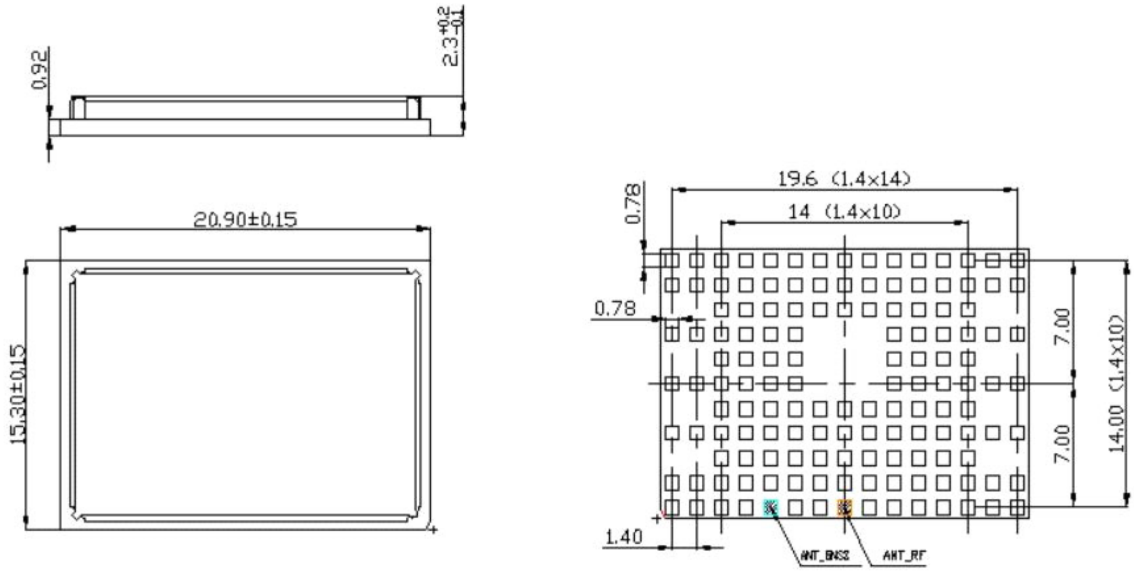


Figure 58: Dimensions of TX82-W and TX62-W-B (all dimensions in mm)

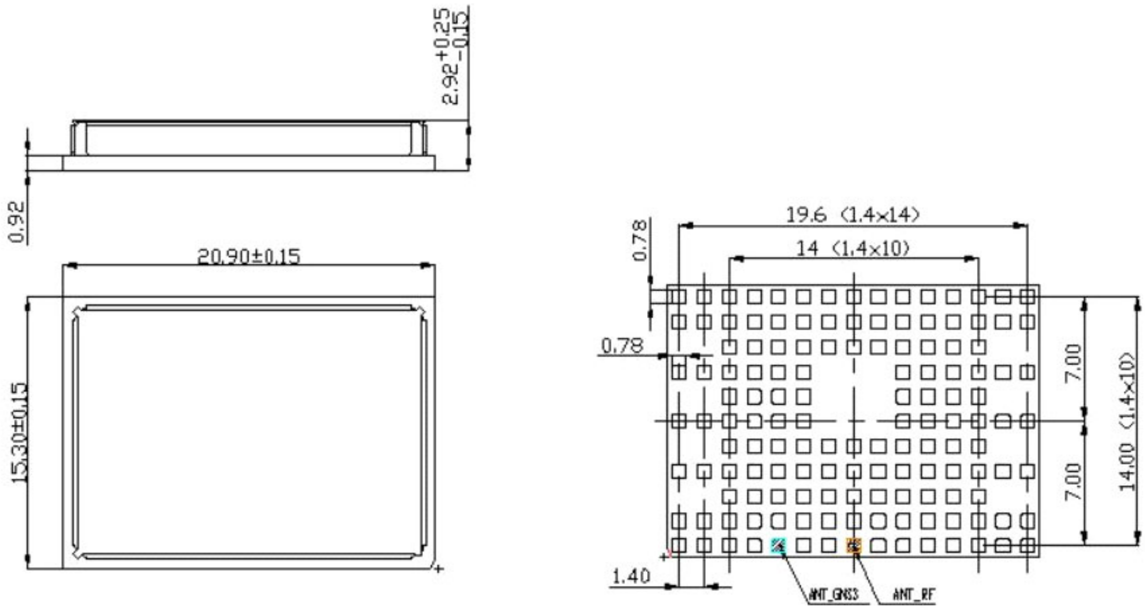


Figure 59: Dimensions of TX82-W-B and TX62-W-C (all dimensions in mm)

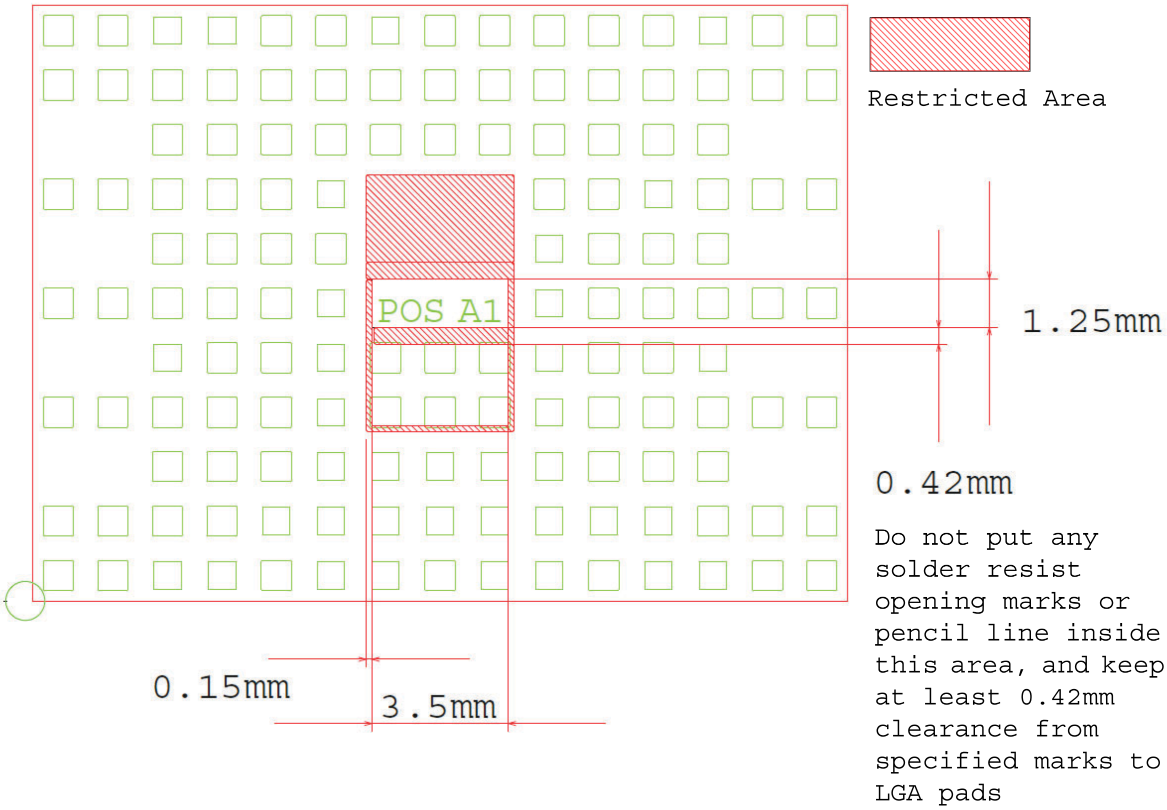


Figure 60: Dimensions of area for possible markings TX82-W, TX82-W-B, TX62-W-B and TX62-W-C (bottom view)

5.3 Mounting TX62/TX82 onto the Application Platform

This section describes how to mount Cinterion® TX62/TX82 onto the PCBs, including land pattern and stencil design, board-level characterization, soldering conditions, durability and mechanical handling. For more information on issues related to SMT module integration see also [5].

Note: To avoid short circuits between signal tracks on an external application's PCB and various markings at the bottom side of the module (see Figure 56 and Figure 60), it is recommended not to route the signal tracks on the top layer of an external PCB directly under the module, or at least to ensure that signal track routes are sufficiently covered with solder resist.

Note: Do not place external components or devices that might cause any pressure on the module's shielding. See [4] and [5] for further details of thermal and integration guidance.

5.3.1 SMT PCB Assembly

5.3.1.1 Land Pattern and Stencil

The land pattern and stencil design as shown below is based on Telit Cinterion characterizations for lead-free solder paste on a four-layer test PCB and a 110 micron thick stencil.

The land pattern given in Figure 61 and Figure 62 reflects the module's pad layout, including signal pads and ground pads (for pad assignment see Section 3.1.1)

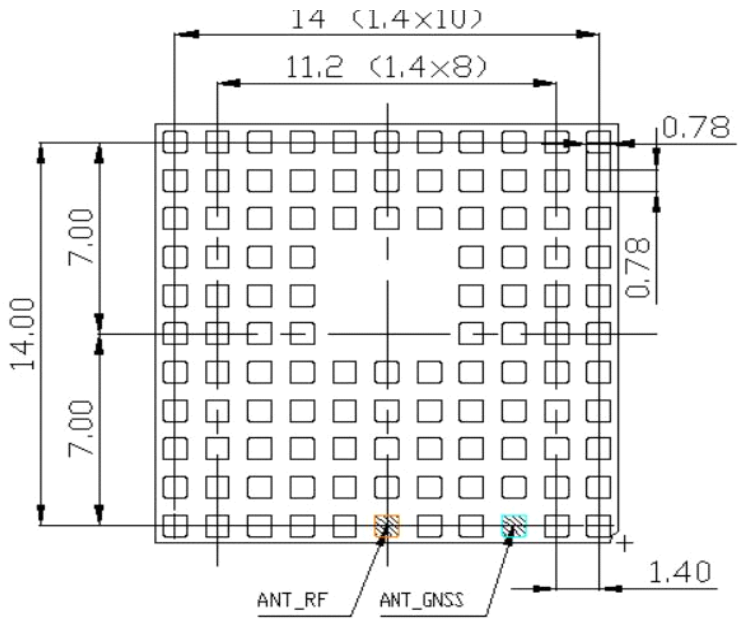


Figure 61: Land pattern TX62-W (top view)

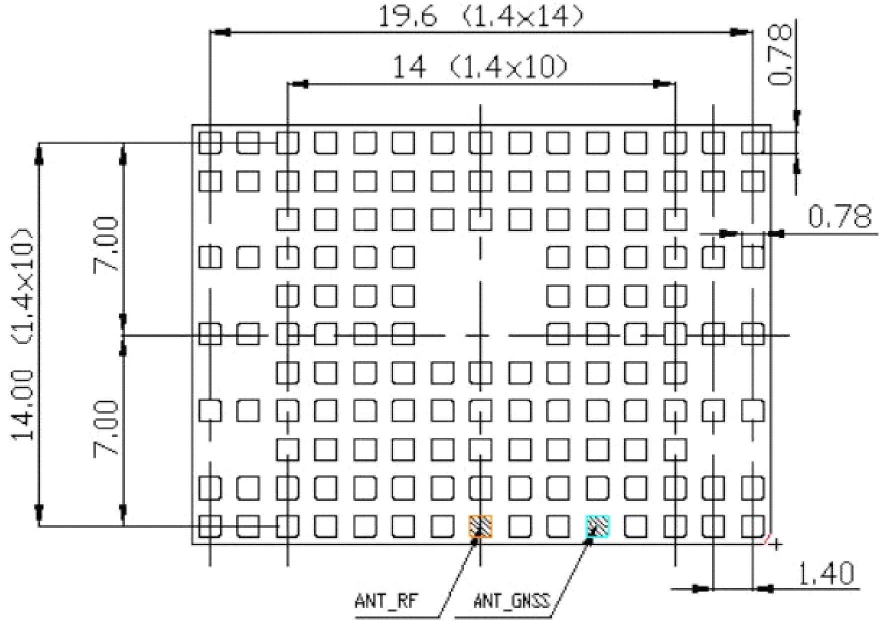


Figure 62: **Land pattern TX82-W, TX82-W-B, TX62-W-B and TX62-W-C (top view)**

The stencil design illustrated in Figure 63 and Figure 64 is recommended by Telit Cinterion as a result of extensive tests with Telit Cinterion Daisy Chain modules.

The central ground pads are primarily intended for stabilizing purposes, and may show some more voids than the application interface pads at the module's rim. This is acceptable, since they are electrically irrelevant.

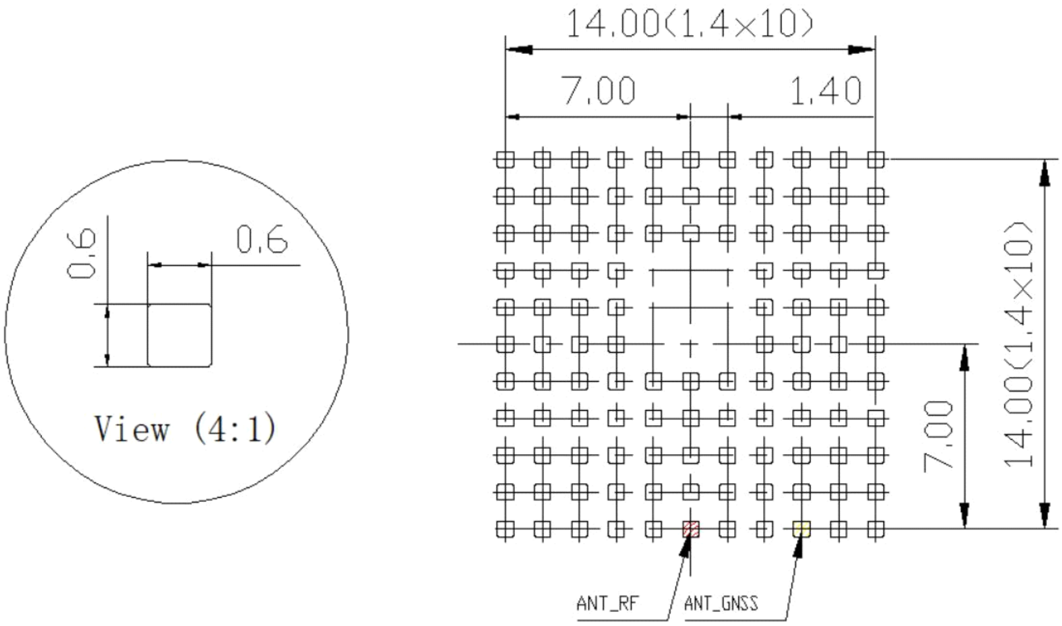


Figure 63: Recommended design for 110µm thick stencil for TX62-W (top view)

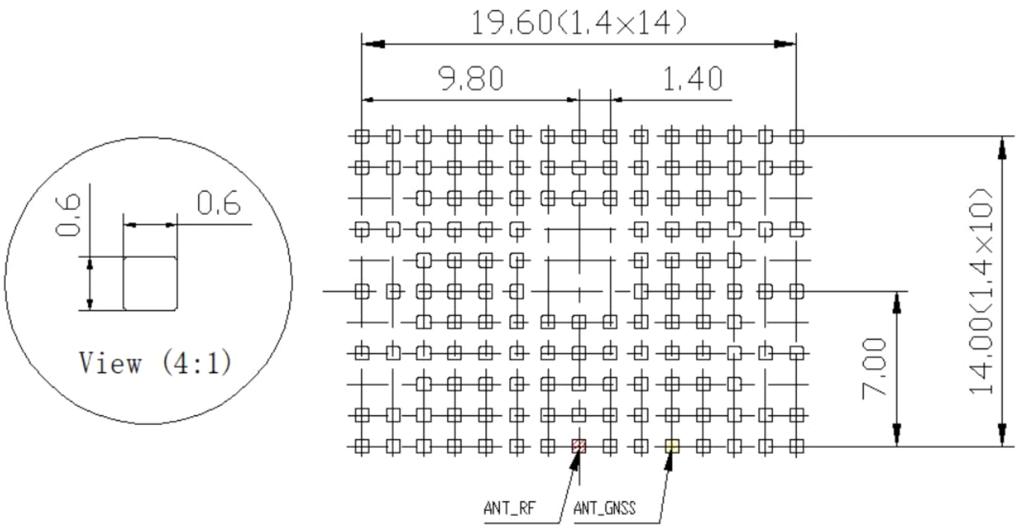


Figure 64: Recommended design for 110µm thick stencil for TX82-W, TX82-W-B, TX62-W-B and TX62-W-C (top view)

5.3.1.2 Board Level Characterization

Board level characterization issues should also be taken into account if devising an SMT process.

Characterization tests should attempt to optimize the SMT process with regard to board level reliability. This can be done by performing the following physical tests on sample boards: Peel test, bend test, tensile pull test, drop shock test and temperature cycling. Sample surface mount checks are described in [5].

It is recommended to characterize land patterns before an actual PCB production, taking individual processes, materials, equipment, stencil design, and reflow profile into account. For land and stencil pattern design recommendations see also Section 5.3.1.1. Optimizing the solder stencil pattern design and print process is necessary to ensure print uniformity, to decrease solder voids, and to increase board level reliability.

Generally, solder paste manufacturer recommendations for screen printing process parameters and reflow profile conditions should be followed. Maximum ratings are described in Section 5.3.3.

5.3.2 Moisture Sensitivity Level

Cinterion® TX62/TX82 comprises components that are susceptible to damage induced by absorbed moisture.

Telit Cinterion's Cinterion® TX62/TX82 module complies with the latest revision of the IPC/JEDEC J-STD-020 Standard for moisture sensitive surface mount devices and is classified as MSL 4.

For additional moisture sensitivity level (MSL) related information see Section 5.3.4 and Section 5.3.2.

5.3.3 Soldering Conditions and Temperature

5.3.3.1 Reflow Profile

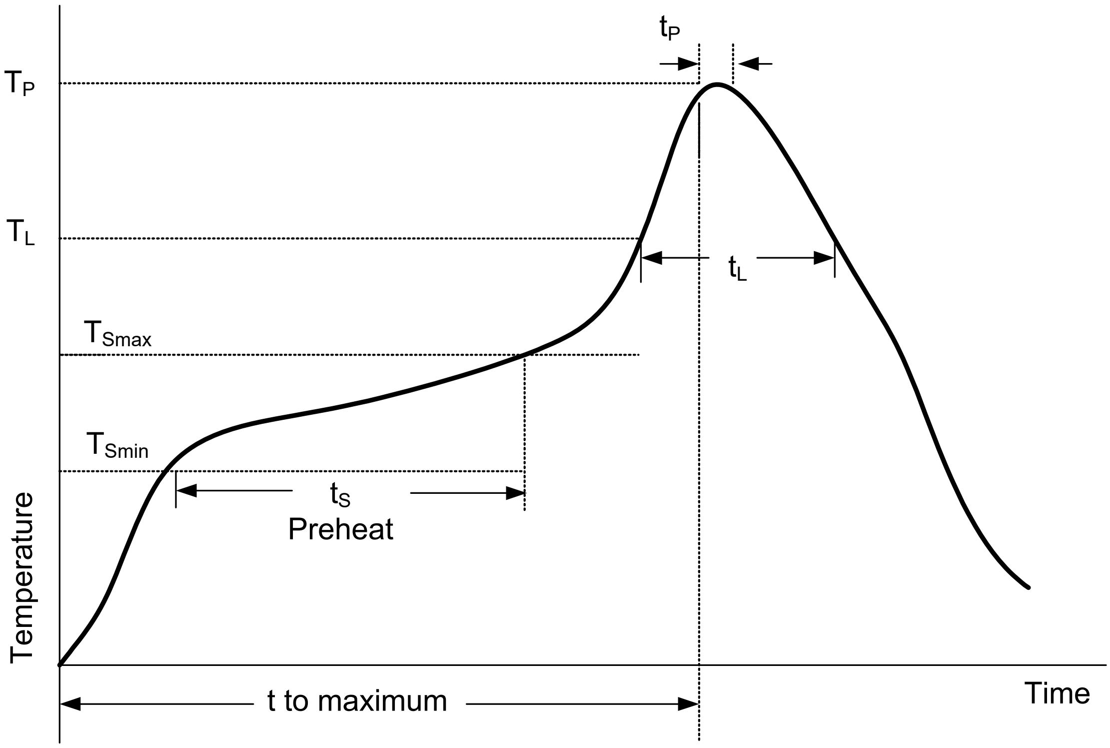


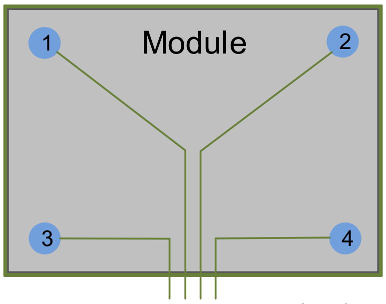
Figure 65: **Reflow Profile**

Table 34: **Reflow temperature ratings**[1]

Profile Feature	Pb-Free Assembly
Preheat & Soak Temperature Minimum (T_{Smin}) Temperature Maximum (T_{Smax}) Time (t_{Smin} to t_{Smax}) (t_S)	 150°C 180°C 60-120 seconds
Average ramp up rate (T_{Smax} to T_P)	3K/second max.[2]
Liquidous temperature (T_L) Time at liquidous (t_L)	217°C 50-90 seconds
Peak package body temperature (T_P)	245°C +0/-5°C
Time (t_P) within 5 °C of the peak package body temperature (T_P)	30 seconds max.
Limited ramp-down rate (Tp - 200°C) Average ramp-down rate from 200°C	1K-2.5K/second max.[2, 3] 3K/second max.[2]
Time 25°C to maximum temperature	8 minutes max.

1. Please note that the reflow profile features and ratings listed above are based on the joint industry standard IPC/JEDEC J-STD-020E, and are as such meant as a general guideline. For more information on reflow profiles and their optimization please refer to [5].
2. Temperatures measured on shielding at each corner. For more information see [5].
During the critical ramp-down phase, (T_P to 200°C), temperature differences between sensors (1,2,3,4) should be as low as possible

.



3. Lowest ramp-down rate achievable is also dependent on reflow oven tape and settings used. Use lowest ramp down rate from Tp- 200°C. For more information see [5].

5.3.3.2 Maximum Temperature and Duration

The following limits are recommended for the SMT board-level soldering process to attach the module:

A maximum module temperature of 245°C. This specifies the temperature as measured at the module's top side.

A maximum duration of 15 seconds at this temperature.

Note:

While the solder paste manufacturers' recommendations for best temperature and duration for solder reflow should generally be followed, the limits listed above must not be exceeded.

Cinterion® TX62/TX82 is specified for one soldering cycle only. Once Cinterion® TX62/TX82 is removed from the application, the module will very likely be destroyed and cannot be soldered onto another application.

5.3.4 Durability and Mechanical Handling

5.3.4.1 Storage Conditions

Cinterion® TX62/TX82 modules, as delivered in tape and reel carriers, must be stored in sealed, moisture barrier anti-static bags. The conditions stated below are only valid for modules in their original packed state in weather protected, non-temperature-controlled storage locations. Normal storage time under these conditions is 12 months maximum.

Table 35: Storage conditions

Type	Condition	Unit	Reference
Air temperature: Low High	-25 +40	°C	IPC/JEDEC J-STD-033A
Humidity relative: Low High	10 90 at 40°C	%	IPC/JEDEC J-STD-033A
Air pressure: Low High	70 106	kPa	IEC TR 60271-3-1: 1K4 IEC TR 60271-3-1: 1K4
Movement of surrounding air	1.0	m/s	IEC TR 60271-3-1: 1K4
Water: rain, dripping, icing and frosting	Not allowed	---	---
Radiation: Solar Heat	1120 600	W/m²	ETS 300 019-2-1: T1.2, IEC 60068-2-2 Bb ETS 300 019-2-1: T1.2, IEC 60068-2-2 Bb
Chemically active substances	Not recommended		IEC TR 60271-3-1: 1C1L
Mechanically active substances	Not recommended		IEC TR 60271-3-1: 1S1
Vibration sinusoidal: Displacement Acceleration Frequency range	 1.5 5 2-9 9-200	 mm m/s² Hz	IEC TR 60271-3-1: 1M2

Table 35: **Storage conditions**

Type	Condition	Unit	Reference
Shocks: Shock spectrum Duration Acceleration	 semi-sinu-soidal 1 50	 ms m/s²	IEC 60068-2-27 Ea

5.3.4.2 Processing Life

Cinterion® TX62/TX82 must be soldered to an application within 72 hours after opening the moisture barrier bag (MBB) it was stored in.

As specified in the IPC/JEDEC J-STD-033 Standard, the manufacturing site processing the modules should have ambient temperatures below 30°C and a relative humidity below 60%.

5.3.4.3 Baking

Baking conditions are specified on the moisture sensitivity label attached to each MBB (see Figure 71 for details):

- It is *not necessary* to bake Cinterion® TX62/TX82, if the conditions specified in Section 5.3.4.1 and Section 5.3.4.2 were not exceeded.
- It is *necessary* to bake Cinterion® TX62/TX82, if any condition specified in Section 5.3.4.1 and Section 5.3.4.2 was exceeded.

If baking is necessary, the modules must be put into trays that can be baked to at least 125°C. Devices should not be baked in tape and reel carriers at any temperature.

5.3.4.4 Electrostatic Discharge

Electrostatic discharge (ESD) may lead to irreversible damage for the module. It is therefore advisable to develop measures and methods to counter ESD and to use these to control the electrostatic environment at manufacturing sites.

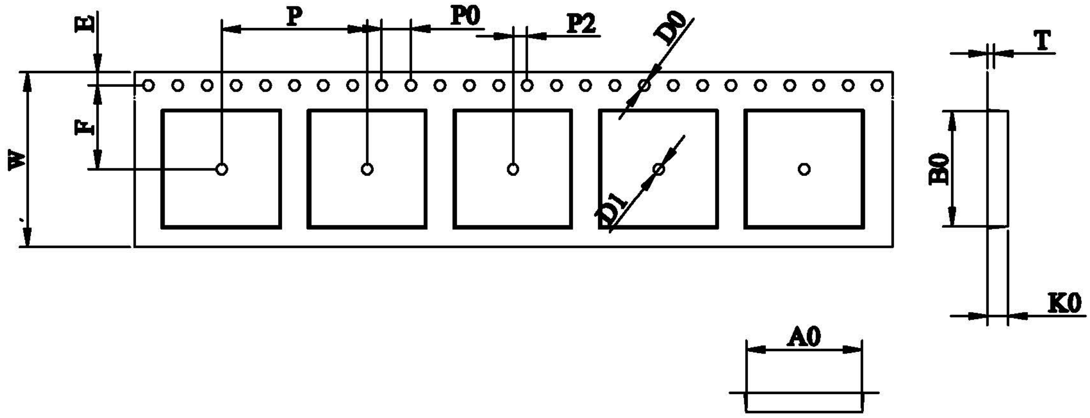
Please refer to Section 4.6 for further information on electrostatic discharge.

5.4 Packing

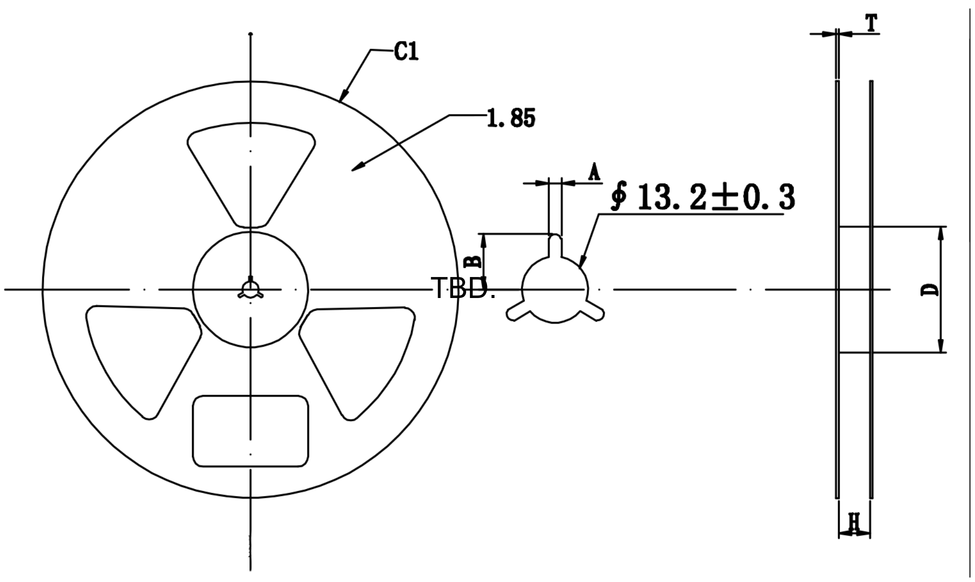
5.4.1 Tape and Reel

The single-feed tape carrier for Cinterion® TX62/TX82 is illustrated in Figure 66. The figure also shows the proper part orientation. The tape width is 24mm and the Cinterion® TX62/TX82 modules are placed on the tape with a 22mm pitch. The reels are 330mm in diameter with a core diameter of 99.50mm. Each reel contains 500 modules.

5.4.1.1 Orientation



A0: 15.80mm B0: 15.80mm D0: 1.50mm D1: 1.5mm E: 1.75mm
F: 11.50mm K0: 2.80mm P: 20.00mm P0: 4.00mm P2: 2.00mm
T: 0.30mm W: 24.00mm



A: 2.50mm B: 11.00mm C1: 330mm D: 99.50mm H: 24.50mm
T: 2.30mm

Figure 66: **Carrier tape (TX62-W only)**

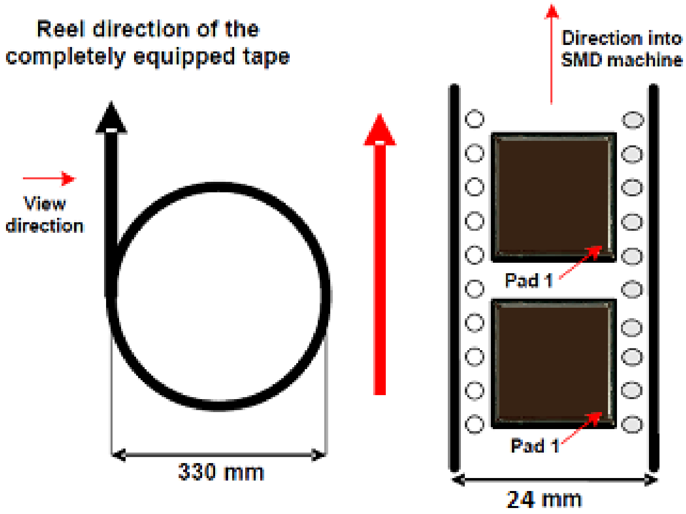


Figure 67: Reel direction (TX62-W only)

5.4.1.2 Barcode Label

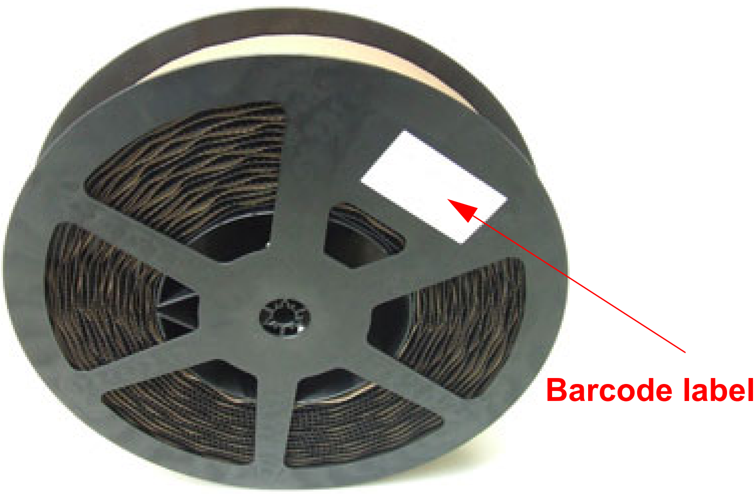


Figure 68: Barcode label on tape reel

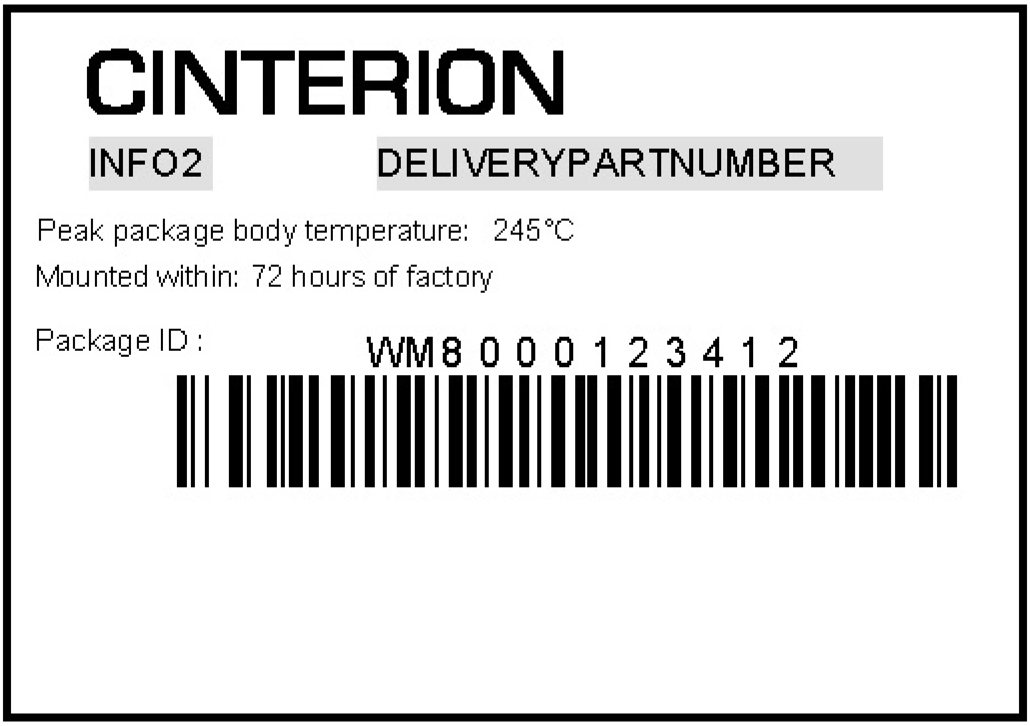


Figure 69: **Barcode label on tape reel - layout**

Variables on the label are explained in Table 36.

5.4.2 Shipping Materials

Cinterion® TX62/TX82 is distributed in tape and reel carriers. The tape and reel carriers used to distribute Cinterion® TX62/TX82 are packed as described below, including the following required shipping materials:

- Moisture barrier bag, including desiccant and humidity indicator card
- Transportation box

5.4.2.1 Moisture Barrier Bag

The tape reels are stored inside a moisture barrier bag (MBB), together with a humidity indicator card and desiccant pouches - see Figure 70. The bag is ESD protected and delimits moisture transmission. It is vacuum-sealed and should be handled carefully to avoid puncturing or tearing. The bag protects the Cinterion® TX62/TX82 modules from moisture exposure. It should not be opened until the devices are ready to be soldered onto the application.

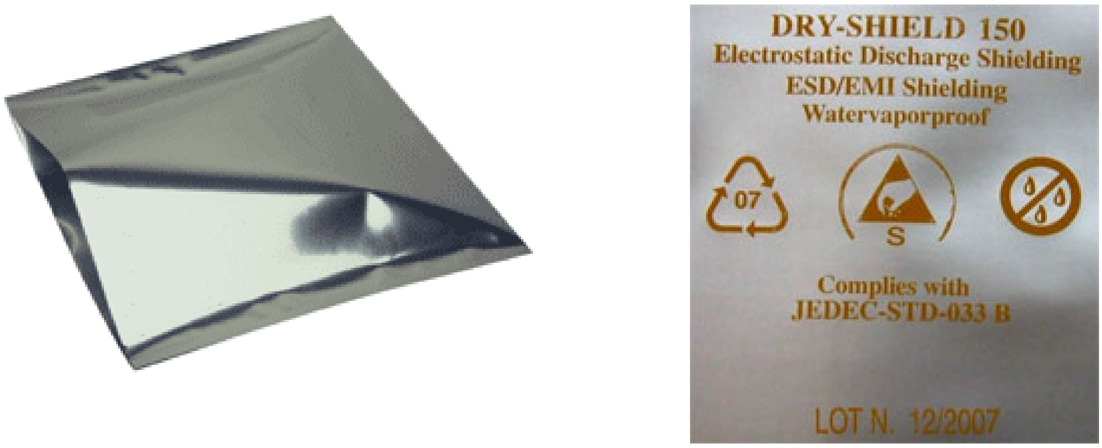


Figure 70: Moisture barrier bag (MBB) with imprint

The label shown in Figure 71 summarizes requirements regarding moisture sensitivity, including shelf life and baking requirements. It is attached to the outside of the moisture barrier bag. Variables on the label are explained in Table 36.

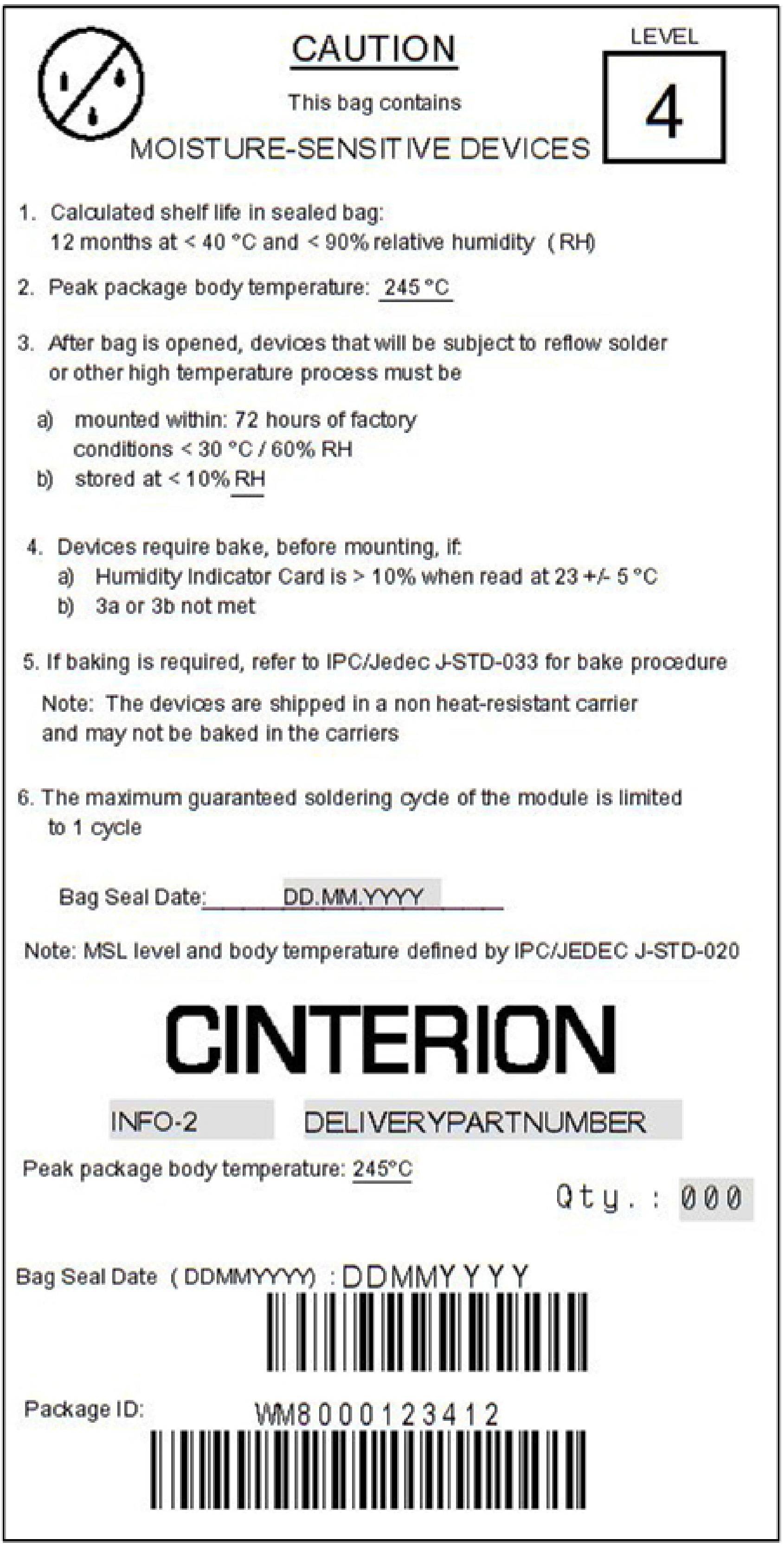


Figure 71: Moisture Sensitivity Label

MBBs contain one or more desiccant pouches to absorb moisture that may be in the bag. The humidity indicator card described below should be used to determine whether the enclosed components have absorbed an excessive amount of moisture.

The desiccant pouches should not be baked or reused once removed from the MBB.

The humidity indicator card is a moisture indicator and is included in the MBB to show the approximate relative humidity level within the bag. Sample humidity cards are shown in Figure 72. If the components have been exposed to moisture above the recommended limits, the units will have to be rebaked.

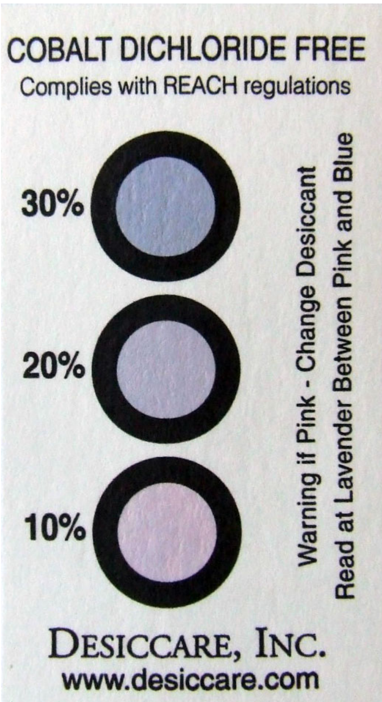


Figure 72: **Humidity Indicator Card - HIC**

A baking is required if the humidity indicator inside the bag indicates 10% RH or more.

5.4.2.2 Transportation Box

Tape and reel carriers are distributed in a box, marked with a barcode label for identification purposes. A box contains two reels with 500 modules each.

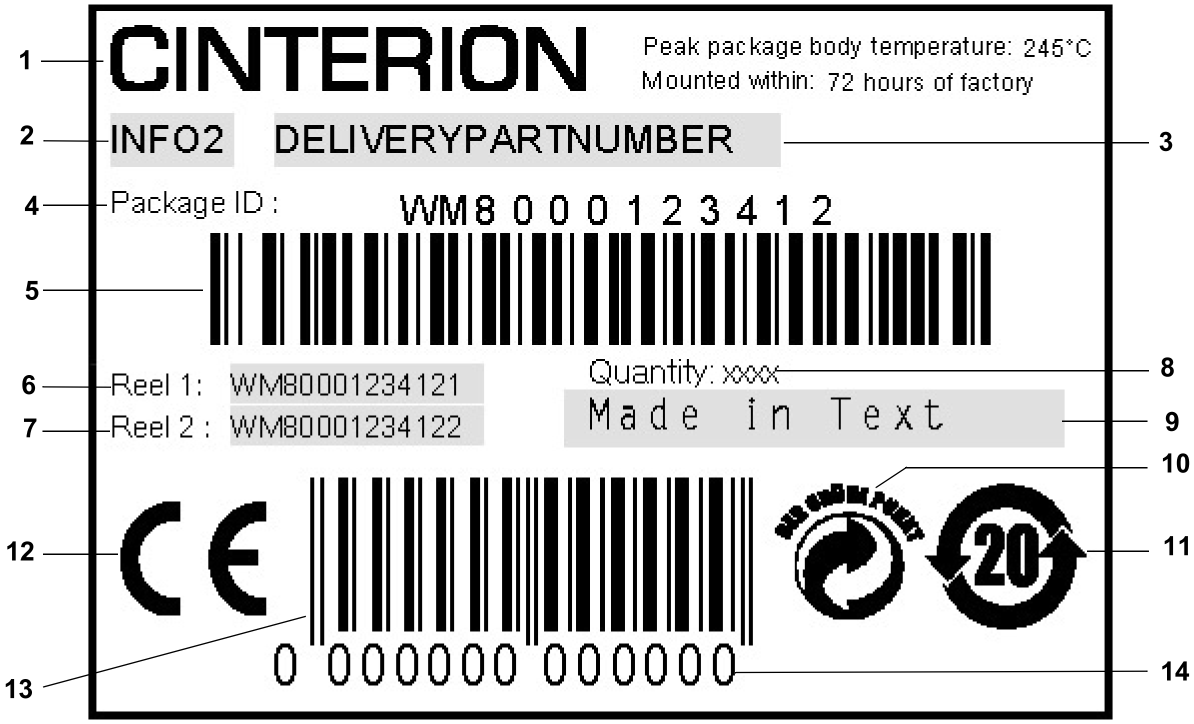


Figure 73: **Sample of VP box label**

Table 36: **VP Box label information**

No.	Information
1	Cinterion logo
2	Product name
3	Product ordering number
4	Package ID number of VP box (format may vary depending on the product)
5	Package ID barcode (Code 128)
6	Package ID Reel 1 (format may vary depending on the product)
7	Package ID Reel 2 (format may vary depending on the product)
8	Quantity of the modules inside the VP box (max. 1000 pcs)
9	Country of production
10	Der Grüne Punkt (Green Dot) symbol
11	Chinese RoHS symbol (see Table 40)
12	CE logo (CE mark on VP box label is present only for modules with CE imprinted on the shielding)
13	European Article Number (EAN-13) barcode
14	European Article Number, consists of 13 digits (EAN-13)

5.4.3 Trays

If small module quantities are required, e.g., for test and evaluation purposes, Cinterion® TX62/TX82 may be distributed in trays (for dimensions see Figure 76). The small quantity trays are an alternative to the single-feed tape carriers normally used. However, the trays are not designed for machine processing. They contain modules to be (hand) soldered onto an external application

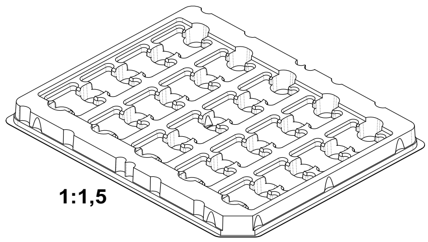


Figure 74: **Small quantity tray**

Trays are packed and shipped in the same way as tape carriers, including a moisture barrier bag with desiccant and humidity indicator card as well as a transportation box (see also Section 5.4.2).

Figure 75: **Trays with packaging materials**

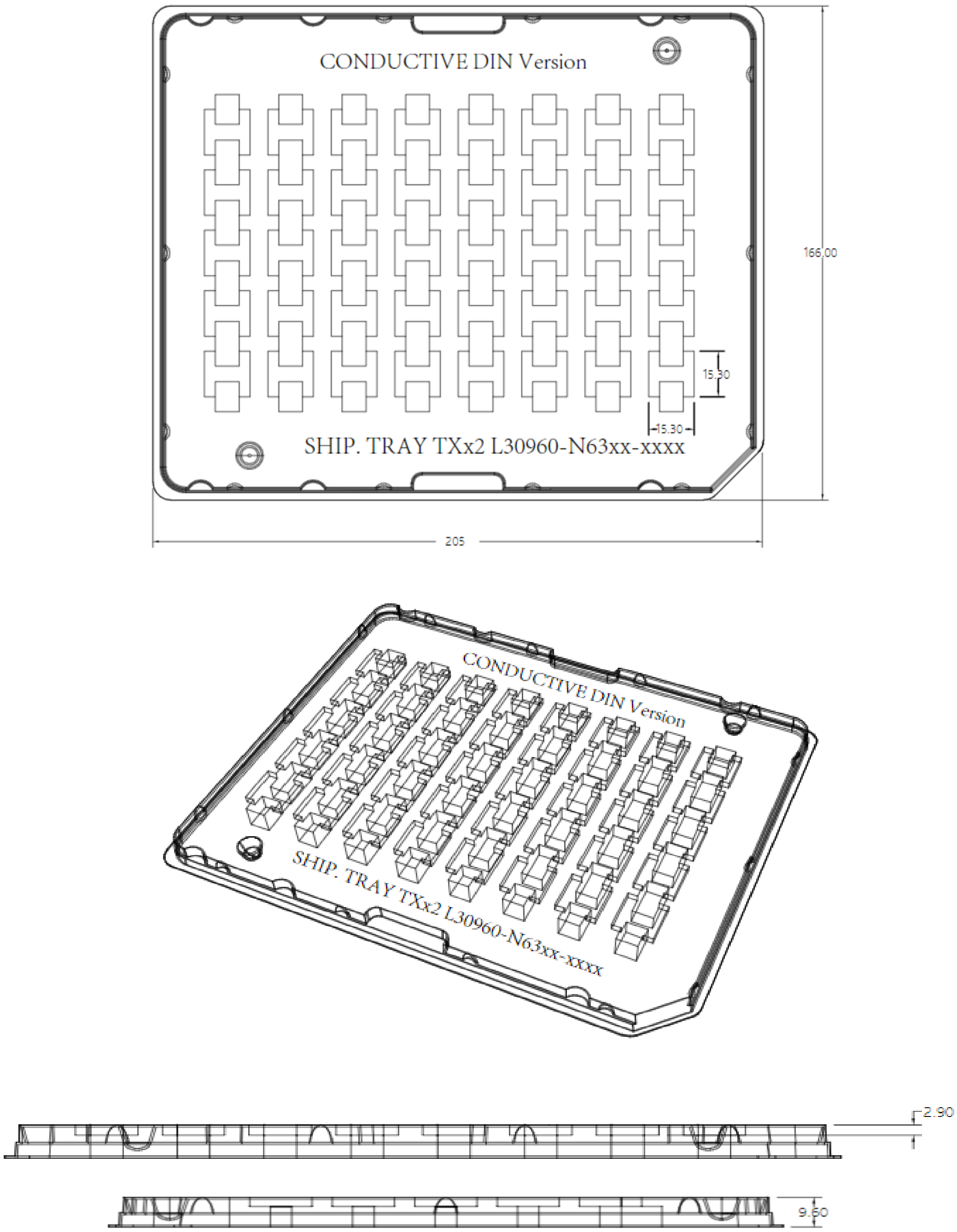


Figure 76: Tray dimensions TX62-W

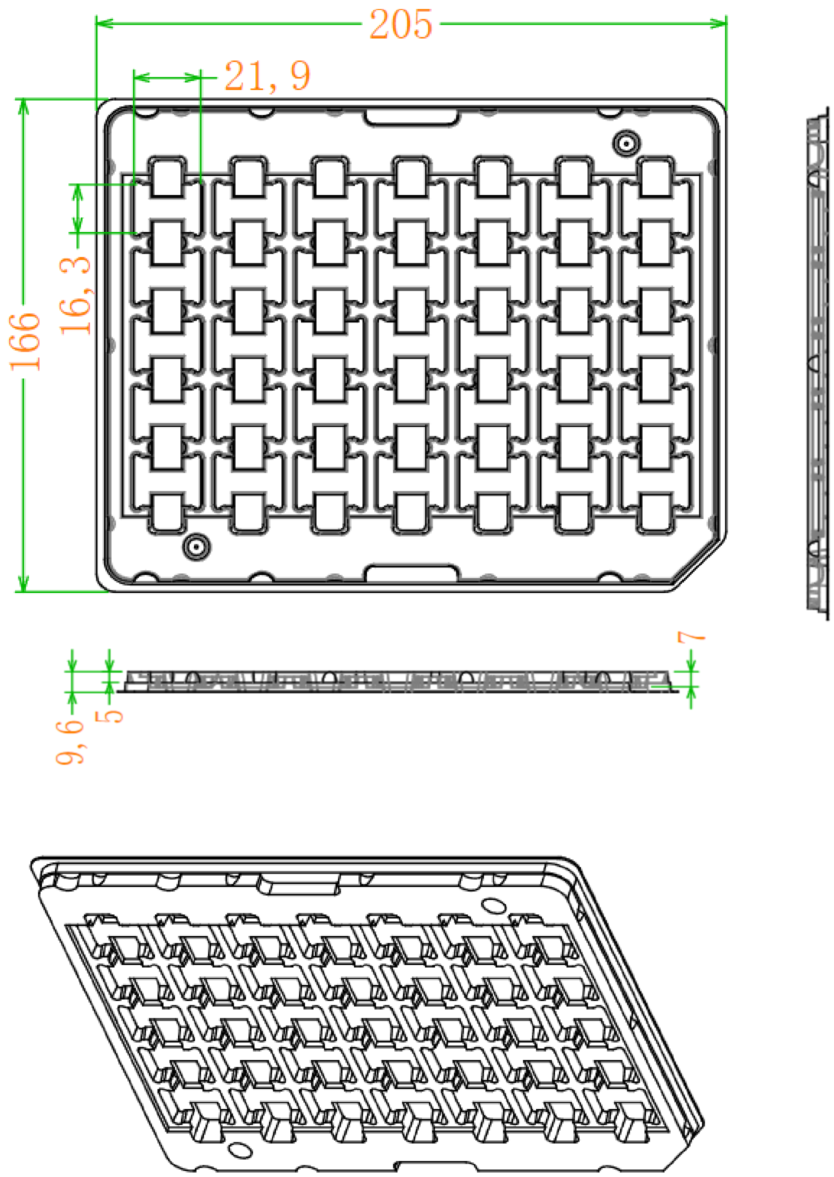


Figure 77: Tray dimensions TX82-W

6 Regulatory and Type Approval Information

6.1 Directives and Standards

Cinterion® TX62/TX82 is designed to comply with the directives and standards listed below.

It is the responsibility of the application manufacturer to ensure compliance of the final product with all provisions of the applicable directives and standards as well as with the technical specifications provided in the Hardware Interface Description.[4]

Table 37: **Directives**

2014/53/EU	Directive of the European Parliament and of the Council of 16 April 2014 on the harmonisation of the laws of the Member States relating to the making available on the market of radio equipment and repealing Directive 1999/5/EC. The product is labeled with the CE conformity mark
2002/95/EC (RoHS 1) 2011/65/EC (RoHS 2)	Directive of the European Parliament and of the Council of 27 January 2003 (and revised on 8 June 2011) on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS)
1907/2006/EC (REACH)	Regulation (EC) No 1907/2006 of the European Parliament and of the Council of 18 December 2006 concerning the Registration, Evaluation, Authorisation and Restriction of Chemicals (REACH), establishing a European Chemicals Agency, amending Directive 1999/45/EC and repealing Council Regulation (EEC) No 793/93 and Commission Regulation (EC) No 1488/94 as well as Council Directive 76/769/EEC and Commission Directives 91/155/EEC, 93/67/EEC, 93/105/EC and 2000/21/EC. Cinterion® modules comply with the REACH regulation that specifies a content of less than 0.1% per substance mentioned in the SVHC candidate list (Release 16.06.2014).

Table 38: **Standards of North American type approval**

CFR Title 47	Code of Federal Regulations, Part 22 and Part 24 (Telecommunications, PCS); US Equipment Authorization FCC

4. Manufacturers of applications which can be used in the US shall ensure that their applications have a PTCRB approval. For this purpose they can refer to the PTCRB approval of the respective module.

Table 38: **Standards of North American type approval**

OET Bulletin 65 (Edition 97-01)	Evaluating Compliance with FCC Guidelines for Human Exposure to Radiofrequency Electromagnetic Fields
UL 62368-1	Audio/video, information and communication technology equipment - Part 1: Safety requirements (for details see Section 6.1.1)
NAPRD.03 V5.35	Overview of PCS Type certification review board Mobile Equipment Type Certification and IMEI control PCS Type Certification Review board (PTCRB)
RSS132 (Issue2) RSS133 (Issue5)	Canadian Standard

Table 39: **Standards of European type approval**

3GPP TS 51.010-1	Digital cellular telecommunications system (Release 7); Mobile Station (MS) conformance specification;
GCF-CC V3.71	Global Certification Forum - Certification Criteria
ETSI EN 301 511 V12.5.1	Global System for Mobile communications (GSM); Mobile Stations (MS) equipment; Harmonized Standard covering the essential requirements of article 3.2 of Directive 2014/53/EU
ETSI EN 301 908-1 V11.1.1	IMT cellular networks; Harmonised Standard covering the essential requirements of article 3.2 of the Directive 2014/53/EU; Part 1: Introduction and common requirements
ETSI EN 301 908-2 V11.1.2	IMT cellular networks; Harmonised Standard covering the essential requirements of article 3.2 of the Directive 2014/53/EU; Part 2: CDMA Direct Spread (UTRA FDD) User Equipment (UE)
ETSI EN 301 489-52 V1.1.0	Electromagnetic Compatibility (EMC) standard for radio equipment and services; Part 52: Specific conditions for Cellular Communication Mobile and portable (UE) radio and ancillary equipment; Harmonized Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU
Draft ETSI EN 301 489-01 V2.2.0	ElectroMagnetic Compatibility (EMC) standard for radio equipment and services; Part 1: Common technical requirements; Harmonized Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU and the essential requirements of article 6 of Directive 2014/30/EU
ETSI EN 301489-19 V2.1.0	ElectroMagnetic Compatibility (EMC) standard for radio equipment and services; Part 19: Specific conditions for Receive Only Mobile Earth Stations (ROMES) operating in the 1,5 GHz band providing data communications and GNSS receivers operating in the RNSS band (ROGNSS) providing positioning, navigation, and timing data; Harmonised Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU

Table 39: **Standards of European type approval**

ETSI EN 303 413 V1.1.1	Satellite Earth Stations and Systems (SES); Global Navigation Satellite System (GNSS) receivers; Radio equipment operating in the 1 164 MHz to 1 300 MHz and 1 559 MHz to 1 610 MHz frequency bands; Harmonised Standard covering the essential requirements of article 3.2 of Directive 2014/53/EU
IEC 62368-1 (EN 62368-1, UL 62368-1)	Audio/video, information and communication technology equipment - Part 1: Safety requirements (for details see Section 6.1.1)

Table 40: **Requirements of quality**

IEC 60068	Environmental testing
DIN EN 60529	IP codes
EN 62311:2008	Assessment of electronic and electrical equipment related to human expo-sure restrictions for electromagnetic fields (0 Hz - 300 GHz)

Table 41: **Standards of the Ministry of Information Industry of the People's Republic of China**

SJ/T 11363-2006	"Requirements for Concentration Limits for Certain Hazardous Substances in Electronic Information Products" (2006-06).
SJ/T 11364-2006	"Marking for Control of Pollution Caused by Electronic Information Products" (2006-06).  According to the "Chinese Administration on the Control of Pollution caused by Electronic Information Products" (ACPEIP) the EPUP, i.e., Environmental Protection Use Period, of this product is 20 years as per the symbol shown here, unless otherwise marked. The EPUP is valid only as long as the product is operated within the operating limits described in the Telit Cinterion Hardware Interface Description. Please see Table 42 for an overview of toxic or hazardous substances or elements that might be contained in product parts in concentrations above the limits defined by SJ/T 11363-2006.

Table 42: Toxic or hazardous substances or elements with defined concentration limits

部件名称 Name of the part	有毒有害物质或元素 Hazardous substances					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
金属部件 (Metal Parts)	O	O	O	O	O	O
电路模块 (Circuit Modules)	X	O	O	O	O	O
电缆及电缆组件 (Cables and Cable Assemblies)	O	O	O	O	O	O
塑料和聚合物部件 (Plastic and Polymeric parts)	O	O	O	O	O	O
O: 表示该有毒有害物质在该部件所有均质材料中的含量均在SJ/T11363-2006 标准规定的限量要求以下。 Indicates that this toxic or hazardous substance contained in all of the homogeneous materials for this part is below the limit requirement in SJ/T11363-2006. X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出SJ/T11363-2006标准规定的限量要求。 Indicates that this toxic or hazardous substance contained in at least one of the homogeneous materials used for this part *might exceed* the limit requirement in SJ/T11363-2006.						

6.1.1 IEC 62368-1 Classification

With respect to the safety requirements for audio/video, information and communication technology equipment defined by the hazard based product safety standard for ICT and AV equipment - i.e., IEC-62368-1 (EN 62368-1, UL 62368-1) - Cinterion® modules are classified as shown below:

Standalone operation of the modules is not possible. Modules will always be incorporated in an external application (Customer Product).

Customer understands and is responsible that the product incorporating the Cinterion® module must be designed to be compliant with IEC-62368-1 (EN 62368-1, UL 62368-1) to ensure protection against hazards and injuries. When operating the Cinterion® module the external application (Customer Product) must provide safeguards not to exceed the power limits given by classification to Power Source Class 1 (15 Watts) under normal operating conditions, abnormal conditions, or in the presence of a single fault. When using a battery power supply the external application must provide safeguards not to exceed the limits defined by PS-1, as well. The external application (Customer Product) must take measures to limit the power, the voltage or the current, respectively, if required, and must provide safeguards to protect ordinary persons against pain or injury caused by the voltage/ current.

In case of a usage of the Cinterion® module not in accordance with the specifications or in single fault condition the external application (Customer Product) must be capable to withstand levels according to ES-1 / PS-1 also on all ports that are initially intended for signaling or audio, e.g., USB, RS-232, GPIOs, SPI, earphone and microphone interfaces.

In addition, the external application (Customer Product) must be designed in a way to distribute thermal energy generated by the intended operation of the Cinterion® module. In case of high temperature operation, the external application must provide safeguards to protect ordinary persons against pain or injury caused by the heat.

Table 43: **IEC 62368-1 Classification**

Source of Energy	Class	Limits
Electrical energy source	ES-1	The Cinterion® modules contain no electrical energy source - especially no battery. The electrical components and circuits have to be externally power supplied: DC either smaller 60 V Or less than 2 mA AC up to 1kHz smaller 30 V-rms or 42.4 V peak AC above 100kHz smaller 70 V rms
Power Source (potential ignition source causing fire)	PS-1	Power source provided by the external application must not exceed 15W, even under worst case and any single fault condition defined by IEC-62368-1: Section 6.2.2.3.
Hazardous Substances, Chemical reaction	--	Under regular conditions, the Cinterion® module does not contain any chemically reactive substances, and no chemical energy source, especially no battery. Module is compliant with RoHS and REACH (see above). In very rare cases however - under abnormal conditions (i.e. wrong supply voltage, burned module) or in the presence of single electrical component faults (i.e. shortcut) - health hazardous substances might be released if the worst comes to the worst.

Table 43: IEC 62368-1 Classification

Source of Energy	Class	Limits
Kinetic / mechanical energy source	MS-1	The Cinterion® modules have no sharp edges and corners, no moving parts, no loosing, exploding or imploding parts. The mass is well below 1kg.
Thermal energy source	TS-2	Under normal operating conditions, abnormal operating conditions or single fault conditions the temperature does not exceed +100°C on the metal surface (shielding)
Radiated energy source	RS-1	The Cinterion® module does not contain a radiant energy source, any lasers, lamps, LEDs, X-Ray emitting components or acoustic couplers.

6.2 SAR requirements specific to portable mobiles

Mobile phones, PDAs or other portable transmitters and receivers incorporating a module must be in accordance with the guidelines for human exposure to radio frequency energy. This requires the Specific Absorption Rate (SAR) of portable Cinterion® TX62/TX82 based applications to be evaluated and approved for compliance with national and/or international regulations.

Since the SAR value varies significantly with the individual product design manufacturers are advised to submit their product for approval if designed for portable use. For European and US markets the relevant directives are mentioned below. The manufacturer of the end device is in the responsibility to provide clear installation and operating instructions for the user, including the minimum separation distance required to maintain compliance with SAR and/or RF field strength limits, as well as any special usage conditions required to do so, such as a required accessory, the proper orientation of the device, the max antenna gain for detachable antennas, or other relevant criteria. It is the responsibility of the manufacturer of the final product to verify whether or not further standards, recommendations or directives are in force outside these areas.

Products intended for sale on US markets

ES 59005/ANSI C95.1

Considerations for evaluation of human exposure to Electromagnetic Fields (EMFs) from Mobile Telecommunication Equipment (MTE) in the frequency range 30MHz - 6GHz

Products intended for sale on European markets

EN 50360

Product standard to demonstrate the compliance of mobile phones with

the basic restrictions related to human exposure to electromagnetic fields (300MHz -3GHz)

EN 62311:2008

Assessment of electronic and electrical equipment related to human

expo-sure restrictions for electromagnetic fields (0 Hz - 300 GHz)

Note:

SAR requirements are specific only for portable devices and not for mobile devices as defined below:

- Portable device:

  A portable device is defined as a transmitting device designed to be used so that the radiating structure(s) of the device is/are within 20 centimeters of the body of the user.

- Mobile device:

  A mobile device is defined as a transmitting device designed to be used in other than fixed locations and to generally be used in such a way that a separation distance of at least 20 centimeters is normally maintained between the transmitter's radiating structure(s) and the body of the user or nearby persons. In this context, the term "fixed location" means that the device is physically secured at one location and is not able to be easily moved to another location.

6.3 Reference Equipment for Type Approval

The Telit Cinterion reference setup submitted to type approve Cinterion® TX62/TX82 (including a special approval adapter for the DSB75) is shown in the following figure[5]:

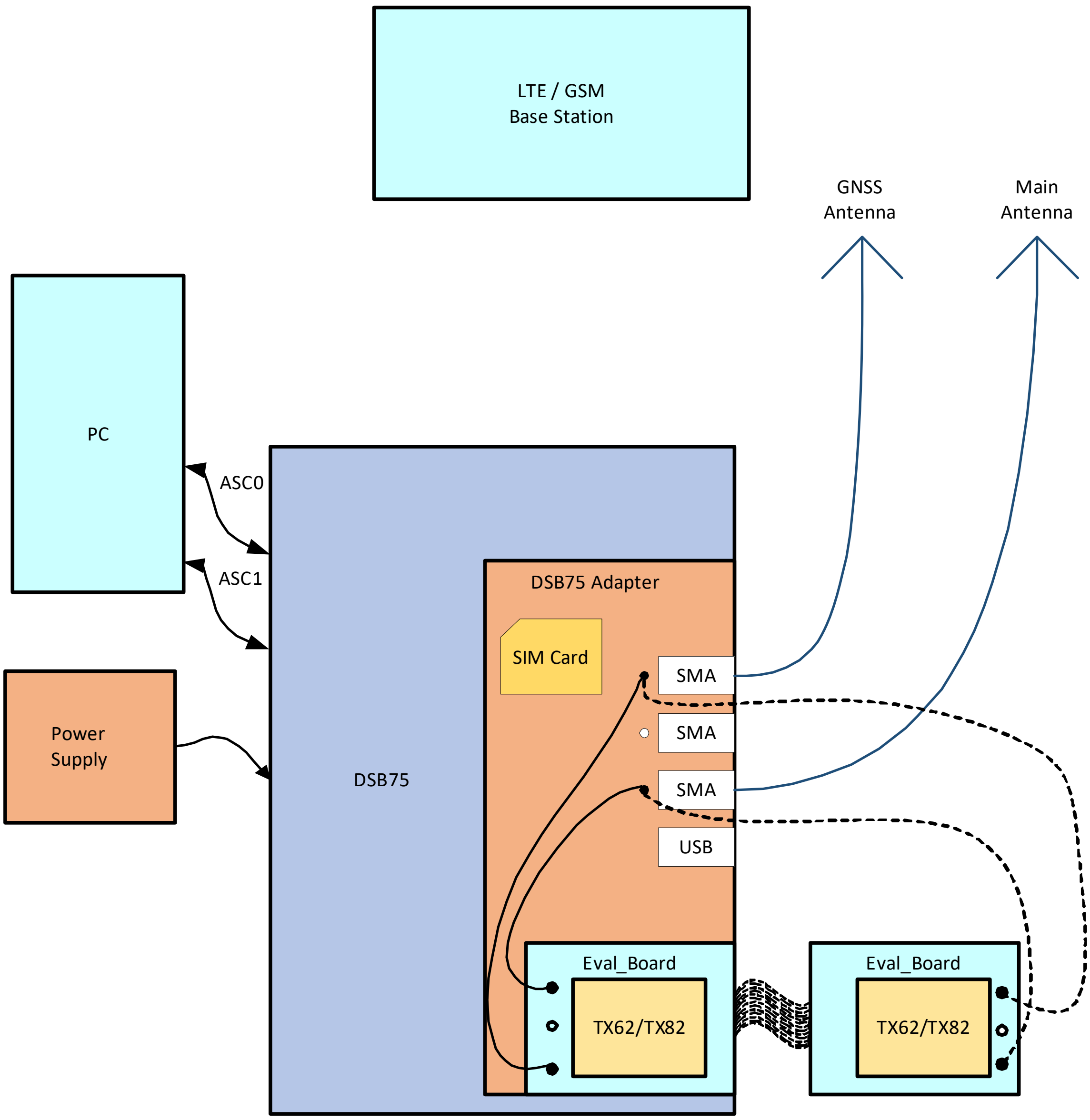


Figure 78: **Reference equipment for type approval**

5. For RF performance tests a mini-SMT/U.FL to SMA adapter with attached 6dB coaxial attenuator is chosen to connect the evaluation module directly to the LTE/GSM/GNSS test equipment instead of employing the SMA antenna connectors on the Cinterion® TX62/TX82-DSB75 adapter as shown in Figure 78. The following products are recommended:
Hirose SMA-Jack/U.FL-Plug conversion adapter HRMJ-U.FLP(40)
(for details see http://www.hirose-connectors.com/ or http://www.farnell.com/
Aeroflex Weinschel Fixed Coaxial Attenuator Model 3T/4T
(for details see http://www.aeroflex.com/ams/weinschel/pdfiles/wmod3&4T.pdf)

6.4 Compliance with FCC and ISED Rules and Regulations

The Equipment Authorization Certification for the Telit Cinterion reference application described in Section 5.3 will be registered under the following identifiers:

FCC Identifier: QIPTX82-W
ISED Certification Number: 7830A-TX82W
Granted to Telit Cinterion Deutschland GmbH

FCC Identifier: QIPTX62-W
ISED Certification Number: 7830A-TX62W
Granted to Telit Cinterion Deutschland GmbH

FCC Identifier: QIPTX62-W-B
ISED Certification Number: 7830A-TX62WB
Granted to Telit Cinterion Deutschland GmbH

FCC Identifier: QIPTX82-W-B
ISED Certification Number: 7830A-TX82WB
Granted to Telit Cinterion Deutschland GmbH

Manufacturers of mobile or fixed devices incorporating Cinterion® TX62/TX82 modules are authorized to use the FCC Grants and ISED Certificates of the Cinterion® TX62/TX82 modules for their own final products according to the conditions referenced in these documents. In this case, an FCC/ IC label of the module shall be visible from the outside, or the host device shall bear a second label stating "Contains FCC ID: QIPTX82-W" or "Contains FCC ID: QIPTX82-W-B" or "Contains FCC ID: QIPTX62-W" or "Contains FCC ID: QIPTX62-W-B", and accordingly “Contains IC: 7830A-TX82W“ or “Contains IC: 7830A-TX82WB“ or “Contains IC: 7830A-TX62W“ or “Contains IC: 7830A-TX62WB“. The integration is limited to fixed or mobile categorized host devices, where a separation distance between the antenna and any person of min. 20cm can be assured during normal operating conditions.

For mobile and fixed operation configurations the antenna gain, including cable loss, must not exceed the limits listed in the following Table 44, Table 46, and Table 47 for FCC and/or ISED.

Table 44: Antenna gain limits for FCC and ISED for TX82-W

Maximum gain in operating band	FCC limit	ISED limit	All limits	Unit
LTE Band 2	11.01	11.01	11.01	dBi
LTE Band 4	8.00	8.00	8.00	dBi
LTE Band 5	12.41	9.10	9.10	dBi
LTE Band 12	11.70	8.61	8.61	dBi
LTE Band 13	12.16	8.93	8.93	dBi
LTE Band 25	11.01	11.01	11.01	dBi
LTE Band 26	12.41	9.10	9.10	dBi

Table 44: Antenna gain limits for FCC and ISED for TX82-W

Maximum gain in operating band	FCC limit	ISED limit	All limits	Unit
LTE Band 66	8.00	8.00	8.00	dBi
LTE Band 71	11.47	8.45	8.45	dBi
LTE Band 85	11.60	8.61	8.61	dBi
GSM850	8.60	5.30	5.30	dBi
PCS1900	10.20	10.20	10.20	dBi

Table 45: Antenna gain limits for FCC and ISED for TX82-W-B - TBD.

Maximum gain in operating band	FCC limit	ISED limit	All limits	Unit
LTE Band 2				dBi
LTE Band 4				dBi
LTE Band 5				dBi
LTE Band 12				dBi
LTE Band 13				dBi
LTE Band 25				dBi
LTE Band 26				dBi
LTE Band 66				dBi
GSM850				dBi
PCS1900				dBi

Table 46: Antenna gain limits for FCC and ISED for TX62-W

Maximum gain in operating band	FCC limit	ISED limit	All limits	Unit
LTE Band 2	11.01	11.01	11.01	dBi
LTE Band 4	8.00	8.00	8.00	dBi
LTE Band 5	12.41	9.10	9.10	dBi
LTE Band 12	11.70	8.61	8.61	dBi
LTE Band 13	12.16	8.93	8.93	dBi
LTE Band 25	11.01	11.01	11.01	dBi
LTE Band 26	12.41	9.10	9.10	dBi
LTE Band 66	8.00	8.00	8.00	dBi
LTE Band 71	11.47	8.45	8.45	dBi
LTE Band 85	11.60	8.61	8.61	dBi

Table 47: Antenna gain limits for FCC and ISED for TX62-W-B

Maximum gain in operating band	FCC limit	ISED limit	All limits	Unit
LTE Band 2	8.01	8.01	8.01	dBi
LTE Band 4	5.00	5.00	5.00	dBi
LTE Band 5	9.41	6.10	6.10	dBi
LTE Band 8	9.70	---	9.70	dBi
LTE Band 12	8.70	5.61	5.61	dBi
LTE Band 13	9.16	5.93	5.93	dBi
LTE Band 25	8.01	8.01	8.01	dBi
LTE Band 26	9.41	6.10	6.10	dBi
LTE Band 66	5.00	5.00	5.00	dBi
LTE Band 71	8.47	5.45	5.45	dBi
LTE Band 85	8.60	5.61	5.61	dBi

Warning:

Manufacturers of portable applications incorporating Cinterion® TX62/TX82 modules are required to have their final product certified and apply for their own FCC Grant and ISED Certificate related to the specific portable mobile. This is mandatory to meet the SAR requirements for portable mobiles (see Section 6.2 for detail).

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Note:

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules and with ISED license-exempt RSS standard(s). These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

Reorient or relocate the receiving antenna.

Increase the separation between the equipment and receiver.

Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

Consult the dealer or an experienced radio/TV technician for help.

This Class B digital apparatus complies with Canadian ICES-003.

If Canadian approval is requested for devices incorporating Cinterion® TX62/TX82 modules the below notes will have to be provided in the English and French language in the final user documentation. Manufacturers/OEM Integrators must ensure that the final user documentation does not contain any information on how to install or remove the module from the final product.

Notes:

(ISED)

(EN) This Class B digital apparatus complies with Canadian ICES-003 and RSS-210. Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

(FR) Cet appareil numérique de classe B est conforme aux normes canadiennes ICES-003 et RSS-210. Son fonctionnement est soumis aux deux conditions suivantes: (1) cet appareil ne doit pas causer d'interférence et (2) cet appareil doit accepter toute interférence, notamment les interférences qui peuvent affecter son fonctionnement.

(EN) Radio frequency (RF) Exposure Information

The radiated output power of the Wireless Device is below the Innovation, Science and Economic Development Canada (ISED) radio frequency exposure limits. The Wireless Device should be used in such a manner such that the potential for human contact during normal operation is minimized.

This device has also been evaluated and shown compliant with the ISED RF Exposure limits under mobile exposure conditions. (antennas are greater than 20cm from a person‘s body).

(FR) Informations concernant l'exposItion aux fréquences radio (RF)

La puissance de sortie émise par l'appareil de sans fil est inférieure à la limite d'exposition aux fréquences radio d'Innovation, Sciences et Développement économique Canada (ISDE). Utilisez l'appareil de sans fil de façon à minimiser les contacts humains lors du fonctionnement normal.

Ce périphérique a également été évalué et démontré conforme aux limites d'exposition aux RF d'ISDE dans des conditions d'exposition à des appareils mobiles (les antennes se situent à moins de 20cm du corps d'une personne).

6.5 Compliance with Korean Rules and Regulations (TX62-W-B)

The TX62-W-B reference application described in Section 6.3 complies with the requirements of the Korean Certification (KC).

The certificate granted in accordance with KC has the identifier:

R-C-QIP-TX62-W-B

Identifier and the KC (certification) logo are part of the module's label.

Please note that TX62-W-B is has been certified under Clause 2, Article 58-2 of the Radio Waves Act as described in Figure 79.

위 기자재는「전파법」제58조의2 제2항에 따라 인증되었음을 증명합니다.

It is verified that foregoing equipment has been certificated under the Clause 2, Article 58-2 of Radio Waves Act.

2021년(Year) 09월(Month) 27일(Day)

국립전파연구원장 국립전파연구원장인

Director General of National Radio Research Agency

※ 인증 받은 방송통신기자재는 반드시 "적합성평가표시" 를 부착하여 유통하여야 합니다.
위반시 과태료 처분 및 인증이 취소될 수 있습니다.

Figure 79: Radio equipment certification

7 Document Information

7.1 Related Documents

[1] TX62/TX82 AT Command Set
[2] TX62/TX82 Release Note
[3] Application Note 48: SMT Module Integration
[4] Application Note 40: Thermal Solutions
[5] Universal Serial Bus Specification Revision 2.0, April 27, 2000
[6] Application Note 40: Thermal Solutions for Cinterion® TXx2-W Applications
[7] Application Note 48: SMT Module Integration
[8] Differences between Selected Cinterion® Modules, Hardware Migration Guide, v11
[9] Cinterion® IoT Suite User Guide for TXx2-W modules
[10] Cinterion® IoT Suite Online Documentation
[11] Cinterion® IoT SDK User Guide, v01

7.2 Terms and Abbreviations

Abbreviation	Description
ADC	Analog-to-digital converter
AGC	Automatic Gain Control
ANSI	American National Standards Institute
ARFCN	Absolute Radio Frequency Channel Number
ARP	Antenna Reference Point
ASC0/ASC1	Asynchronous Controller. Abbreviations used for first and second serial interface of Cinterion® TX62/TX82
B	Thermistor Constant
BER	Bit Error Rate
BTS	Base Transceiver Station
CB or CBM	Cell Broadcast Message
CE	Conformité Européene (European Conformity)
CHAP	Challenge Handshake Authentication Protocol
CPU	Central Processing Unit
CS	Coding Scheme
CSD	Circuit Switched Data
CTS	Clear to Send
DAC	Digital-to-Analog Converter
DAI	Digital Audio Interface
dBm0	Digital level, 3.14dBm0 corresponds to full scale, see ITU G.711, A-law

Abbreviation	Description
DCE	Data Communication Equipment (typically modems, e.g. Telit Cinterion module)
DCS 1800	Digital Cellular System, also referred to as PCN
DRX	Discontinuous Reception
DSB	Development Support Box
DSP	Digital Signal Processor
DSR	Data Set Ready
DRX	Discontinuous Reception
DTE	Data Terminal Equipment (typically computer, terminal, printer or, for example, GSM application)
DTR	Data Terminal Ready
DTX	Discontinuous Transmission
EFR	Enhanced Full Rate
EGSM	Enhanced GSM
EIRP	Equivalent Isotropic Radiated Power
EMC	Electromagnetic Compatibility
EMI	Electromagnetic Interference
ERP	Effective Radiated Power
ESD	Electrostatic Discharge
ETS	European Telecommunication Standard
FCC	Federal Communications Commission (U.S.)
FDMA	Frequency Division Multiple Access
FR	Full Rate
GMSK	Gaussian Minimum Shift Keying
GPRS	General Packet Radio Service
GSM	Global Standard for Mobile Communications
HiZ	High Impedance
HR	Half Rate
I/O	Input/Output
IC	Integrated Circuit
IMEI	International Mobile Equipment Identity
ISO	International Standards Organization
ITU	International Telecommunications Union
kbps	kbits per second
LED	Light Emitting Diode

Abbreviation	Description
Li-Ion/Li+	Lithium-Ion
Li battery	Rechargeable Lithium Ion or Lithium Polymer battery
LPM	Link Power Management
Mbps	Mbits per second
MMI	Man Machine Interface
MO	Mobile Originated
MS	Mobile Station (GSM module), also referred to as TE
MSISDN	Mobile Station International ISDN number
MT	Mobile Terminated
NTC	Negative Temperature Coefficient
OEM	Original Equipment Manufacturer
PA	Power Amplifier
PAP	Password Authentication Protocol
PBCCH	Packet Switched Broadcast Control Channel
PCB	Printed Circuit Board
PCL	Power Control Level or Paging Cycle Length
PCM	Pulse Code Modulation
PCN	Personal Communications Network, also referred to as DCS 1800
PDU	Protocol Data Unit
PLL	Phase Locked Loop
PPP	Point-to-point protocol
PSK	Phase Shift Keying
PSU	Power Supply Unit
PTW	Paging Time Window
R&TTE	Radio and Telecommunication Terminal Equipment
RAM	Random Access Memory
RF	Radio Frequency
RLS	Radio Link Stability
RMS	Root Mean Square (value)
RoHS	Restriction of the use of certain hazardous substances in electrical and electronic equipment.
ROM	Read-only Memory
RTC	Real Time Clock
RTS	Request to Send
Rx	Receive Direction

Abbreviation	Description
SAR	Specific Absorption Rate
SAW	Surface Acoustic Wave
SELV	Safety Extra Low Voltage
SIM	Subscriber Identification Module
SMD	Surface Mount Device
SMS	Short Message Service
SMT	Surface Mount Technology
SPI	Serial Peripheral Interface
SRAM	Static Random Access Memory
TA	Terminal adapter (e.g. GSM module)
TDMA	Time Division Multiple Access
TE	Terminal Equipment, also referred to as DTE
TLS	Transport Layer Security
Tx	Transmit Direction
UART	Universal asynchronous receiver-transmitter
URC	Unsolicited Result Code
USSD	Unstructured Supplementary Service Data
VSWR	Voltage Standing Wave Ratio

7.3 Safety Precaution Notes

The following safety precautions must be observed during all phases of the operation, usage, service or repair of any cellular terminal or mobile incorporating Cinterion® TX62/TX82. Manufacturers of the cellular terminal are advised to convey the following safety information to users and operating personnel and to incorporate these guidelines into all manuals supplied with the product. Failure to comply with these precautions violates safety standards of design, manufacture and intended use of the product. Telit Cinterion assumes no liability for customer's failure to comply with these precautions.

	When in a hospital or other health care facility, observe the restrictions on the use of mobiles. Switch the cellular terminal or mobile off, if instructed to do so by the guidelines posted in sensitive areas. Medical equipment may be sensitive to RF energy. The operation of cardiac pacemakers, other implanted medical equipment and hearing aids can be affected by interference from cellular terminals or mobiles placed close to the device. If in doubt about potential danger, contact the physician or the manufacturer of the device to verify that the equipment is properly shielded. Pacemaker patients are advised to keep their hand-held mobile away from the pacemaker, while it is on.
	Switch off the cellular terminal or mobile before boarding an aircraft. Make sure it cannot be switched on inadvertently. The operation of wireless appliances in an aircraft is forbidden to prevent interference with communications systems. Failure to observe these instructions may lead to the suspension or denial of cellular services to the offender, legal action, or both.
	Do not operate the cellular terminal or mobile in the presence of flammable gases or fumes. Switch off the cellular terminal when you are near petrol stations, fuel depots, chemical plants or where blasting operations are in progress. Operation of any electrical equipment in potentially explosive atmospheres can constitute a safety hazard.
	Your cellular terminal or mobile receives and transmits radio frequency energy while switched on. Remember that interference can occur if it is used close to TV sets, radios, computers or inadequately shielded equipment. Follow any special regulations and always switch off the cellular terminal or mobile wherever forbidden, or when you suspect that it may cause interference or danger.
	Road safety comes first! Do not use a hand-held cellular terminal or mobile when driving a vehicle, unless it is securely mounted in a holder for speakerphone operation. Before making a call with a hand-held terminal or mobile, park the vehicle. Speakerphones must be installed by qualified personnel. Faulty installation or operation can constitute a safety hazard.

	IMPORTANT! Cellular terminals or mobiles operate using radio signals and cellular networks. Because of this, connection cannot be guaranteed at all times under all conditions. Therefore, you should never rely solely upon any wireless device for essential communications, for example emergency calls. Remember, in order to make or receive calls, the cellular terminal or mobile must be switched on and in a service area with adequate cellular signal strength. Some networks do not allow for emergency calls if certain network services or phone features are in use (e.g. lock functions, fixed dialing etc.). You may need to deactivate those features before you can make an emergency call. Some networks require that a valid SIM card be properly inserted in the cellular terminal or mobile.

8 Appendix

8.1 List of Parts and Accessories

Table 48: List of parts and accessories

Description	Supplier	Ordering information
TX62-W	Telit Cinterion	Standard module Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6300-A130 Module label number[1]: S30960-S6300-A130-1 Standard module with AT&T and Verizon approval Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6300-B130 Module label number[1]: S30960-S6300-B130-1 Standard module with embedded MFF-XS eUICC Telit Cinterion IMEI: Packaging unit (ordering) number: L30900-N6307-A140 Module label number[1]: S30900-S6307-A140-1 Customer IMEI mode: Packaging unit (ordering) number: L30960-N6305-A130 Module label number[1]: S30960-S6305-A130-1

Table 48: List of parts and accessories

Description	Supplier	Ordering information
TX62-W-B	Telit Cinterion	Standard module Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6650-A130 Module label number[1]: S30960-S6650-A130-1 Standard module with AT&T and Verizon approval Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6650-B130 Module label number[1]: S30960-S6650-B130-1 Standard module (Korean variant) Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6650-K100 Module label number[1]: S30960-S6650-K100-1 Standard module with embedded MFF-XS eUICC Telit Cinterion IMEI: Packaging unit (ordering) number: L30900-N6657-A140 Module label number[1]: S30900-S6657-A140-1 Customer IMEI mode: Packaging unit (ordering) number: L30960-N6655-A130 Module label number[1]: S30960-S6655-A130-1 Standard module with Telstra approval Telit Cinterion IMEI: Packaging label number: L30960-N6650-A100
TX62-W-C	Telit Cinterion	Standard module Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6660-A200 Module label number[1]: L30960-N6660-A200-1

Table 48: List of parts and accessories

Description	Supplier	Ordering information
TX82-W	Telit Cinterion	Standard module Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6600-A130 Module label number[1]: S30960-S6600-A130-1. Standard module with AT&T and Verizon approval Telit Cinterion IMEI: Packaging unit (ordering) number: L30960-N6600-B130 Module label number[1]: S30960-S6600-B130-1. Standard module with embedded MFF-XS eUICC Telit Cinterion IMEI: Packaging unit (ordering) number: L30900-N6607-A140 Module label number[1]: S30900-S6607-A140-1 Customer IMEI mode: Packaging unit (ordering) number: L30960-N6605-A130 Module label number[1]: S30960-S6605-A130-1
TX62-W Evaluation Module	Telit Cinterion	Standard module Ordering number: L30960-N6301-A100
TX62-W-B Evaluation Module	Telit Cinterion	Standard module Ordering number: L30960-N6651-A100
TX62-W-C Evaluation Module	Telit Cinterion	Standard module Ordering number: L30960-N6661-A200
TX82-W Evaluation Module	Telit Cinterion	Standard module Ordering number: L30960-N6601-A100 Standard module with embedded MFF-XS eUICC Ordering number: L30900-N6608-A140
TX82-W Evaluation Module	Telit Cinterion	Standard module Ordering number: TBD.
DSB75 Evaluation Kit	Telit Cinterion	Ordering number: L36880-N8811-A100

Table 48: List of parts and accessories

Description	Supplier	Ordering information
DSB Mini Compact Evaluation Board	Telit Cinterion	Ordering number: L30960-N0030-A100
LGA DevKit	Telit Cinterion	LGA DevKit consists of Cinterion® LGA DevKit T Base PCB: Ordering number: L30960-N0113-A100 Cinterion® LGA DevKit Socket T: Ordering number: L30960-N0114-A100
EVAL DSB Adapter for mounting Cinterion® TX62/TX82 evaluation modules onto DSB75	Telit Cinterion	Ordering number: L30960-N0100-A100
SIM card holder incl. push button ejector and slide-in tray	Molex	Ordering numbers: 91228 91236 Sales contacts are listed in Table 49.

1. **Note:** At the discretion of Telit Cinterion, module label information can either be laser engraved on the module's shielding or be printed on a label adhered to the module's shielding.

Table 49: Molex sales contacts (subject to change)

Molex For further information please click: http://www.molex.com	Molex Deutschland GmbH Otto-Hahn-Str. 1b 69190 Walldorf Germany Phone: +49-6227-3091-0 Fax: +49-6227-3091-8100 Email: mxgermany@molex.com	American Headquarters Lisle, Illinois 60532 U.S.A. Phone: +1-800-78MOLEX Fax: +1-630-969-1352
Molex China Distributors Beijing, Room 1311, Tower B, COFCO Plaza No. 8, Jian Guo Men Nei Street, 100005 Beijing P.R. China Phone: +86-10-6526-9628 Fax: +86-10-6526-9730	Molex Singapore Pte. Ltd. 110, International Road Jurong Town, Singapore 629174 Phone: +65-6-268-6868 Fax: +65-6-265-6044	Molex Japan Co. Ltd. 1-5-4 Fukami-Higashi, Yamato-City, Kanagawa, 242-8585 Japan Phone: +81-46-265-2325 Fax: +81-46-265-2365

8.2 Module Label Information

The label engraved on the top of Cinterion® TX62/TX82 comprises the following information.

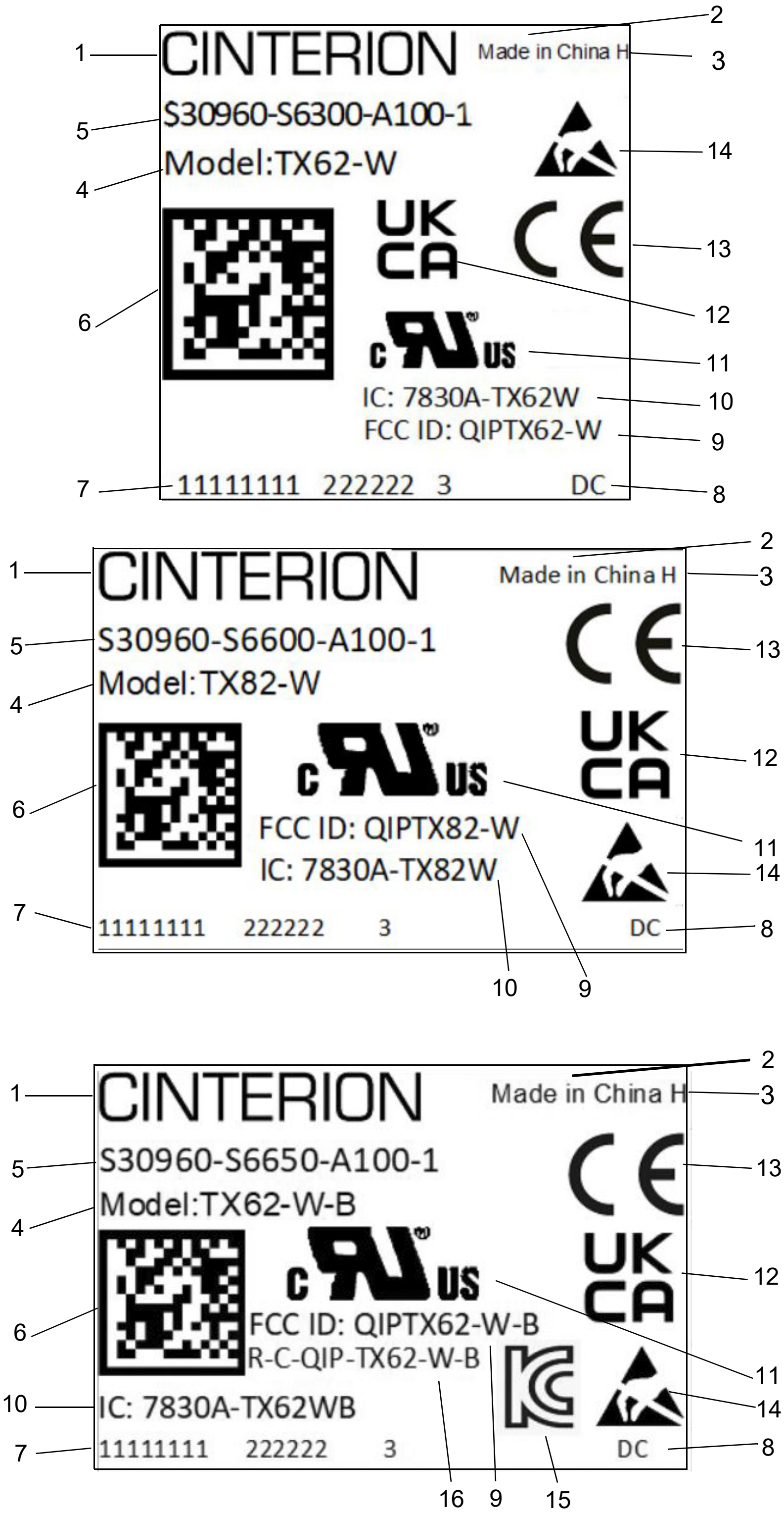


Figure 80: **Cinterion® TX62/TX82/TX62-W-B label**

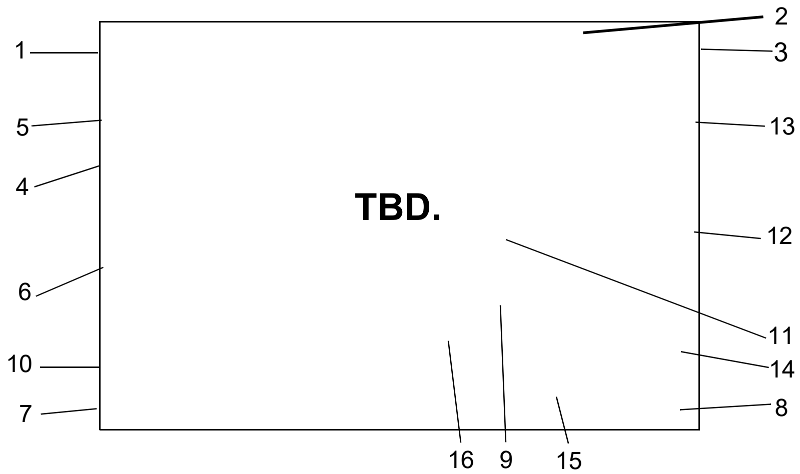


Figure 81: TX82-W-B label

Table 50: Cinterion® TX62/TX82 label information

No.	
1	Cinterion logo
2	Manufacturing country (e.g., "Made in China")
3	Factory code (e.g. "H")
4	Product name/variant (e.g., "TX62-W")
5	Product order code
6	Manufacturer 2D barcode
7	Product IMEI
8	2-digit date code of product production (for decoding see Table 51 below)
9	FCC ID
10	IC ID
11	Underwriters Laboratories (UL) mark
12	United Kingdom Conformity Assessed (UKCA) mark
13	European Conformity (CE) mark
14	Electrostatic-sensitive device symbol
15	Korean Certification (KC) mark
16	Korean Certification Identifier

Table 51: Date code table

Date Code												
Code	L	M	N	P	R	S	T	U	V	W	X	A
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	1	2	3	4	5	6	7	8	9	O	N	D
Month	Jan.	Feb.	Mar.	Apr.	May	June	July	Aug.	Sept.	Oct.	Nov.	Dec.

9 Document History

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200d

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200e

Chapter	What is new
--	Layout revised.

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200c

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200d

Chapter	What is new
6.4	Table 43 band 85 information added.

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200b

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200c

Chapter	What is new
Throughout document	Revised Figure 24, Figure 37, Figure 39, and Figure 40 to indicate High/ Low signal level.
3.1.2	Revised $V_I$min for VUSB_IN.
5.3.3.1	Revised reflow profile.

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200a

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200b

Chapter	What is new
Throughout document	Revised support for LTE Cat NB1/2 as well as selected frequency bands for TX62-W-C.
3.1	Added note for output power regarding duty cycle for Cat M1 UL signals (TX62-W-C).
3.1.12.3	Revised complete section on fast shutdown.
4.2.1.2	Revised ON startup timing description.
4.2.2.2	Revised EMERG_RST timing description.
4.2.4.1	Revised module switch off timing description.

4.4.1	Revised output power setting for Band 31 and 72 (TX62-W-C; 26dBm --> 23dBm)
8.1	Updated ordering information for TX62-W-C.

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200a

Chapter	What is new
Throughout document	Added TX82-W-B as supported product variant.
3.3.3	Revised horizontal accuracy rating.
8.1	Updated ordering information.

Preceding document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.000

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.200

Chapter	What is new
4.4.1	Updated power supply ratings for TX62-W-C.
5.2	Updated section showing mechanical dimensions.

Preceding document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026b

New document: "Cinterion® TX62/TX82 Hardware Interface Description" v01.000

Chapter	What is new
Throughout document	Added new Variant: TX62-W-C.
3.1.2, 4.2.1.1	Revised high level pulse width for ON signal (1ms --> 30ms).
3.1.7	Revised Figure 15 showing enhanced ESD protection for SIM interface.
3.1.6	Added configuration of dual mode.
3.1.12.3	Added note on FST_SHDN line becoming active only 3 seconds after module startup.
3.4	Revised Figure 34.
4.2.1.1	Added pull-down resistor in Figure 36.
4.2.1.2	New section Automatic Power On.

Chapter	What is new
4.2.4.1	Clarified description of regular switch off behavior. Added note mentioning differences in switch of timing between Cat M1 and Cat NB1/2.
5.2	Updated section showing mechanical dimensions.
5.3	Added note regarding placement of external components.
6.4	Added Table 43, Table 45, and Table 46 with FCC and ISED antenna gain limits.
6.5	New section Compliance with Korean Rules and Regulations (TX62-W-B).
8.2	Updated module label information.

Preceding document: “Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026a

New document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026b

Chapter	What is new
3.1.1	Revised G15/G16 pad assignments, and removed superfluous footnote in Table 3.
3.1.2	Revised Table 4: No internal pull up resistors for I²C lines.
3.1.4, 3.1.5	Added note below Figure 11 and Figure 13 explaining dotted lines.
3.1.7	Revised Figure 16. Added Figure 17 showing how to connect eUICC to module’s SIM interface lines.
3.1.9.1	Revised GPIO availability with embedded processing option, and adapted whole document accordingly.
3.2.1	Removed “Max.” column from Table 11 and Table 12.
4.2.2.2	Revised description of the emergency restart process.
4.2.4.1	Added note that worst fast shutdown time cannot be specified.
4.4.1	Added power supply ratings for TX62-W-B and TX82-W.
6.1	Replaced UL 60950 with UL 62368-1 in Table 37.
6.1.1	Completed Table 42 regarding hazardous substances, chemical reaction.
6.2	Added remark regarding responsibility of the end device manufacturer.

Preceding document: “Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026

New document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026a

Chapter	What is new
4.4.1	Added power supply ratings for TX62-W.

Preceding document: “Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.022a

New document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.026

Chapter	What is new
2.1	Revised version of TX62-W-B
3.1.8	Revised Figure 19
3.1	Added REACH directive to Table 36
6.1	Change Safety Standard to IEC 62368-1 in Table 38
6.1.1	New chapter regarding IEC 62368-1 Classification
8.1	Revised ordering and module label numbers inn Table 47

Preceding document: “Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.022

New document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.022a

Chapter	What is new
Through-out docu-ment	Adapted GSM voltage range (3.1V...4.6V)
2.4	Revised block diagrams Figure 3, Figure 4, and Figure 5.
3.1.4	Revised Figure 11, and changed Table 18 accordingly.
3.2.1	Revised receiver input sensitivity ratings in Table 11 and Table 12.
4.2.2.2	Revised Figure 39.

Preceding document: “Cinterion® TXx2-W Hardware Interface Description" v00.016

New document: "Cinterion® TX62-W(-B)/TX82-W Hardware Interface Description" v00.022

Chapter	What is new
Throughout document	Added details about the embedded processing option. Revised supported footprint for TX62-W-B. Replaced MIM with eUICC.
3.1.2	Added signal properties for eUICC interface lines.
3.1.4	Revised Figure 11.
5.3.1.1	Revised stencil shown in Figure 64.
8.1	Updated ordering information.

Preceding document: "Cinterion® TXx2-W Hardware Interface Description" v00.002

New document: "Cinterion® TXx2-W Hardware Interface Description" v00.016

Chapter	What is new
Throughout document	Added product TX82-W and TX62-W-B
2.1	Added GPIO to Key Feature at a Glance
3.1.1	Added Table 2 for Pad Assignment of additional Pads of TX82-W
3.1.1	Revised Pad Assignment regarding GPIO in Table 2, Table 3, Figure 7 and Figure 8
3.1.2	Revised Signal Properties regarding GPIO in Table 4
3.1.8	Added new chapter for GPIO
3.4	Revised Sample Application regarding GPIO and USB in Figure 34
4.2.1	Revised Table 18 regarding GPIO, corrected some signal states
4.8	Revised Table 31 regarding GPIO, removed USB signals
7.1	Revised changes for document version 00.002

Preceding document: "Cinterion® TXx2-W Hardware Interface Description" v00.200a

New document: "Cinterion® TXx2-W Hardware Interface Description" v00.002

Chapter	What is new
Throughout document	Removed product TX82-W, thus the document version number restarted to 00.002
2.2, 3.1.2, 4.4.1	Changed minimum $BATT+_{BB}$ from 2.5V to 2.55V in Table 4 and Table 23
2.2, 5.1	Changed hight of the module in Feature at a Glance and in Figure 54 and Figure 55

Chapter	What is new
3.1.2	Revised ON signal description in Table 4
3.1.6	Added in Figure 14 hint where to place the capacitors
2.1.6.1	Added hint where to place the capacitors
2.1.7	Revised Figure 16 added reference to chapter 2.1.6.1
2.2.1	Updated Table 11
2.2.3	Revised Figure 28, Figure 29, Figure 30, Figure 31 and Figure 32 according to the footprint of TX62-W
3.4.3	Revised chapter and Figure 51 regarding GND reference point
3.7	Added placement of capacitors in Table 31 for SIM interface signals
4.1	Revised Figure 56
4.3.1.1	Changed Stencil thickness to 110µm and Stencil pattern in Figure 63
4.4.1.1	Revised Figure 66 and Figure 67

Preceding document: "Cinterion® TXx2-W Hardware Interface Description" v00.200

New document: "Cinterion® TXx2-W Hardware Interface Description" v00.200a

Chapter	What is new
1.2	Removed extended voltage range and changed eUICC size MFF2 to MFF-XS
1.3	Revised Figure 1 regarding eUICC size
1.4	Removed in Figure 3 signal ADC2
2.1.2	Removed extended voltage range in Table 4
2.4	Removed in Figure 34 wrong PAD numbers
3.4.1	Removed extended voltage range in Table 23
4.2	Revised Figure 57

Preceding document: "Cinterion® TXx2-W Hardware Interface Description" v00.038

New document: "Cinterion® TXx2-W Hardware Interface Description" v00.200

Chapter	What is new
-	New document layout

Chapter	What is new
Throughout document	Removed LTE Bd14. Renamed LTE Bd4 (AWS --> AWS-1), LTE Bd66 (1700MHz --> AWS-3). Added support for optional eUICC interface.
1.2	Added references from key feature list to appropriate document sections.
1.2	Added support for Cinterion® IoT Module services (MODS) as key feature.
2.1.1	Revised Note 2 of Table 3
2.1.2	Revised Table 4 related to power supply
2.1.2.1	Added absolute maximum ratings for digital lines in normal operation.
2.1.7	New section eUICC Interface.
2.1.11.2	Adapted power indication circuit shown in Figure 23.
2.1.11.3	Revised fast shutdown description.
2.3.1	Added note that concurrent GNSS and GSM/LTE operations are not supported.
2.3.2	Revised description for active GNSS antenna and Figure 33
2.3.3	Revised Table 14 listing GNSS antenna interface characteristics.
2.4	Revised Figure 34
3.2.3	Revised section Signal States after Startup including Table 14.
3.4.1	Revised Table 23 related to power supply
4.2	New section Mechanical Dimensions of TX82-W, TX82-W-B, TX62-W-B and TX62-W-C
4.3.1.1	Added Land pattern and Stencil.

New document: "Cinterion® TX62-W/TX82-W Hardware Interface Description" v00.038

Chapter	What is new
--	Initial document setup.