Table 2.1 QFN48 Pin-out

Pin	Name	Description
1	Tx14	Transmitter electrode
2	PGM	Programming Pin
3	SW_IN	Wake-up from suspend and switch input
4	n/c	~
5	SDA	I^2C Data
6	SCL	I^2C Clock
7	VDDHI	Supply Voltage
8	VSS	Ground Reference
9	VREG	Internal Regulator Voltage
10	NRST	Reset (active LOW)
11	RDY	I^2C RDY
12	n/c	~
13	Rx0A	Receiver electrode
14	Rx0B	Note1
15	Rx1A	Receiver electrode
16	Rx1B	Note1
17	Rx2A	Receiver electrode
18	Rx2B	Note1
19	Rx3A	Receiver electrode
20	Rx3B	Note1
21	Rx4A	Receiver electrode
22	Rx4B	Note1
23	Rx5A	Receiver electrode
24	Rx5B	Note1
25	Rx6A	Receiver electrode
26	Rx6B	Note1
27	Rx7A	Receiver electrode
28	Rx7B	Note1
29	Rx8A	Receiver electrode
30	Rx8B	Note1
31	Rx9A	Receiver electrode
32	Rx9B	Note1
33	Tx0	Transmitter electrode
34	Tx1	Transmitter electrode
35	Tx2	Transmitter electrode
36	Tx3	Transmitter electrode
37	VSSIO	I/O Ground Reference
38	VDDIO	I/O Supply Voltage
39	Tx4	Transmitter electrode
40	Tx5	Transmitter electrode
41	Tx6	Transmitter electrode
42	Tx7	Transmitter electrode
43	Tx8	Transmitter electrode
44	Tx9	Transmitter electrode
45	Tx10	Transmitter electrode
46	Tx11	Transmitter electrode
47	Tx12	Transmitter electrode
48	Tx13	Transmitter electrode

Note1: Any of these can be configured through I^2C as the ProxSense® electrode.