JEFFREY HO TAK SHAN

EMPLOYMENT

Automation Test Engineer

BlackBerry

Jan 2015 - Apr 2015

- Performed regression, verification and smoke testing
- Authored and implemented PA test cases using Gherkin and Ruby in Aptana Studio

Software Test Engineer

Zynga

May 2014 - Aug 2014

- Acted as QA lead and signed off on properly tested iOS and Android builds
- Wrote and followed test plans to detail
- Participated and provided valuable input in the new app feature during meetings
- Logged detailed issues into JIRA and kept track of the bugs for verification testing

On-Site Support Specialist

Sun Life Financial

Jan 2013 - Apr 2013

- Imaged Win 7 and other applications on machines for weekly hardware refreshes
- Communicated efficiently with clients to troubleshoot issues using remote desktop
- Performed monthly audits and updated inventory data entry sheets

Technical System Analyst

Rogers Communication

Dec 2011 - Apr 2012

- Troubleshot issues raised by other teams during daily status call
- Represented the team in release management meetings
- Implemented a team intranet Wiki site with XAMPP web based server applications

EDUCATION

Waterloo, ON

University of Waterloo

Fall 2012 - Present

- Candidate for Bachelor of Applied Science (BASc)
- Honours in Electrical Engineering, Co-operative program
- Relevant courses: Electronics Circuits, Signals and Systems, Data Structures and Algorithms

PROJECTS

Personal Website

Self-Initiated Project

Dec 2014

• Used HTML5, CSS and Bootstrap to implement a website from scratch

Traffic Lights Sequencer

University of Waterloo, Digital Circuits

May 2013 - Aug 2013

- Implemented traffic lights simulator with FPGA chip in VHDL using Quartus design software
- Gained exposure to finite state machines and sequential logic design

Robot Car with Remote Control Capabilities

Brebeuf College School, Computer Engineering Course

Sept 2009 - June 2011

- Experience with soldering, drilling and etching board
- Programmed the remote control functionality using PIC assembly language in MPLAB
- Gained exposure to various electrical complements during the board assembly

Languages and Technologies

- C++, HTML, CSS, SQL, Java, C#, VHDL, Visual Basic, MPLAB assembly
- Quartus, Eclipse, MATLAB, System Center Configuration Manager, Microsoft Office