

# *Jeffrey J. Inferrera*

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**Education**      **University of Texas at Austin**, Austin, Texas      Jan. 2020 – Present  
M.S., Computer Science

**Cornell University**, Ithaca, New York      Aug. 2012 – Dec. 2015  
B.S., Electrical and Computer Engineering  
GPA: 3.74/4.3

**Folsom Lake College**, Folsom, California      Aug. 2009 – May 2012  
Transfer Student, Engineering  
GPA: 4.0/4.0

**Research Externship**      **Ultra High Speed Nonlinear Integrated Circuit Design**      August 2015  
**Lab of Cornell University**, Ithaca, New York  
Advisor: Professor Ehsan Afshari  
Shadowed research team; attended group meetings.

**Professional Experience**      **HDR Engineering Inc.**, Folsom, California      May 2016 – July 2019  
Title: Electrical Engineer  
Electrical and I&C designer for infrastructure projects including water, wastewater, transportation, and government projects. Designed electrical and P&ID drawings and specifications; modeled electrical distribution systems for power flow and arc flash analysis; drafted technical memorandums; presented design work to clients.

**Cornell University**, Ithaca, New York      Jan. 2014 – May 2014  
Title: Teacher's Assistant for Microelectronics  
Lectured for small groups; graded papers; held office hours.

**Intel Corporation**, Folsom, California      June 2012 – July 2012  
Title: Intern  
Implemented 2D video game coding in C#; assembled, programmed, and tested a quadrotor with an intern team.

**Skills**      **Programming Languages:** C/C++, Java, Matlab, VisualBasic.

**Certificates & Awards**      **Professional Engineer (PE)**, California, License No. E-22582.  
**Eagle Scout**, Folsom Troop 94, Boy Scouts of America.  
**California Chess Champion**, junior varsity, competed in 31 chess tournaments.