


LAN9215 Reference Schematic

Schematic Revision 0.5

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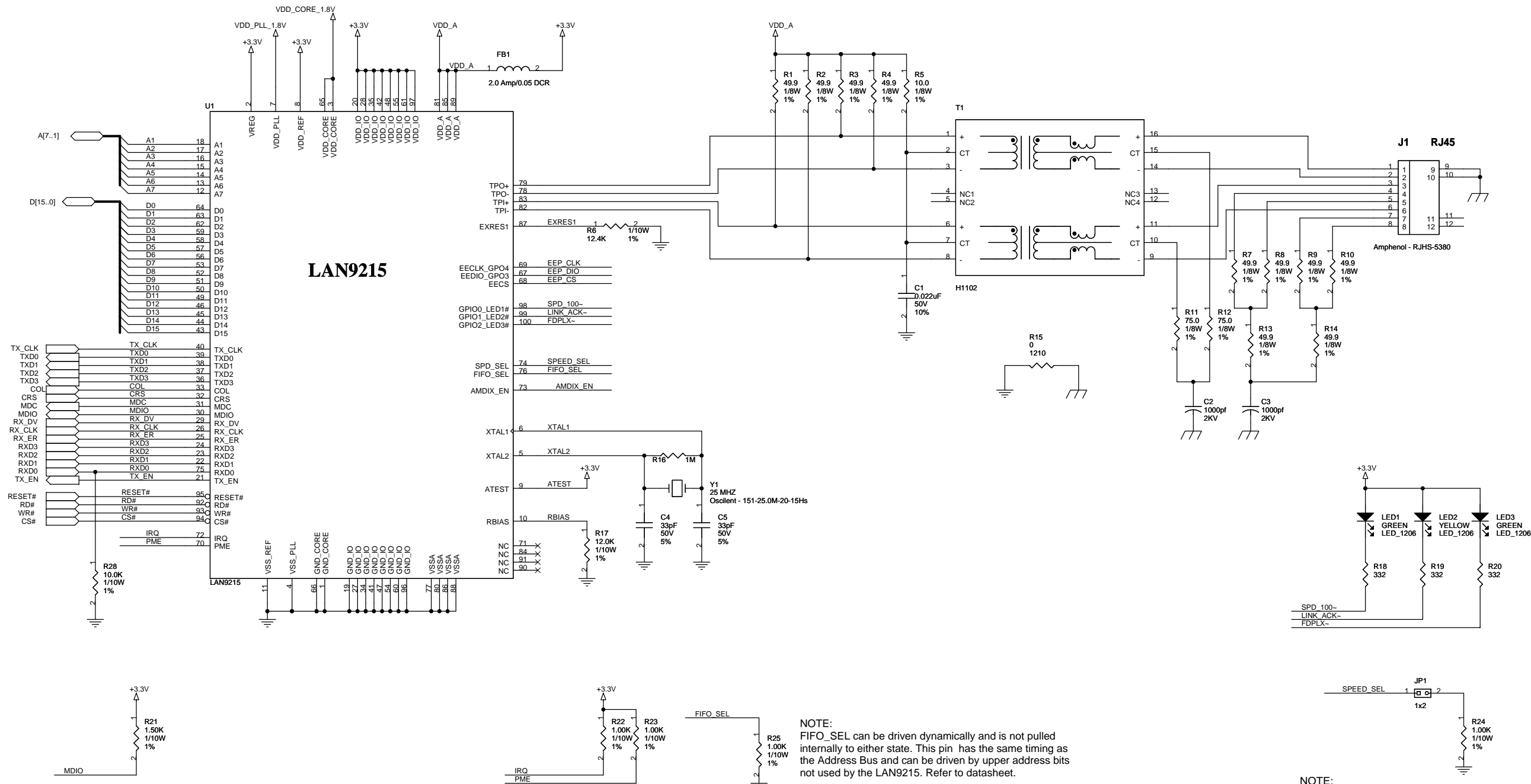
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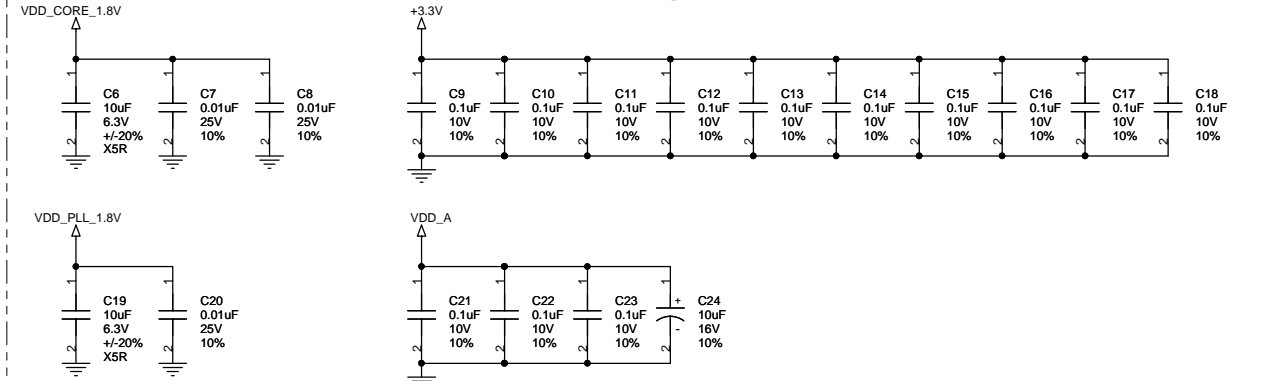
Title

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Size	Engineer	Assembly No.	Rev
C	N/A	N/A	0.5
Date: Tuesday, July 10, 2007		Sheet 1 of 2	

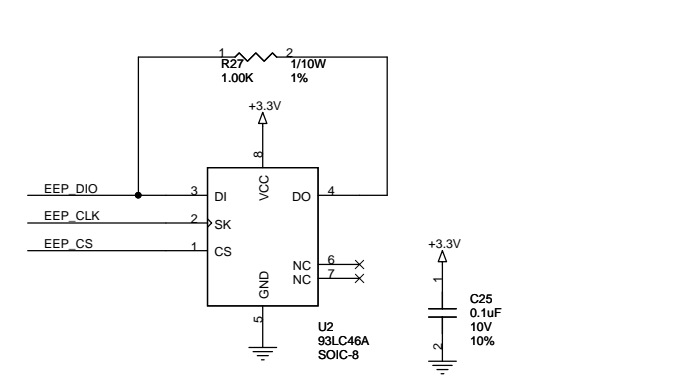


LAN9215 Decoupling Capacitors



NOTE:
C6 and C19 must be low ESR capacitors. Ceramic is recommended.

MAC EEPROM (Optional)



NOTE:
IN = 10Mbps, Half-Duplex, Auto Neg. Disabled
OUT = 100Mbps, Half-Duplex, Auto Neg. Enabled

NOTE:
IN = Auto-MDIX Functionality Enabled
OUT = Auto-MDIX Functionality Disabled