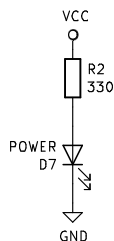
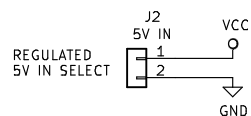
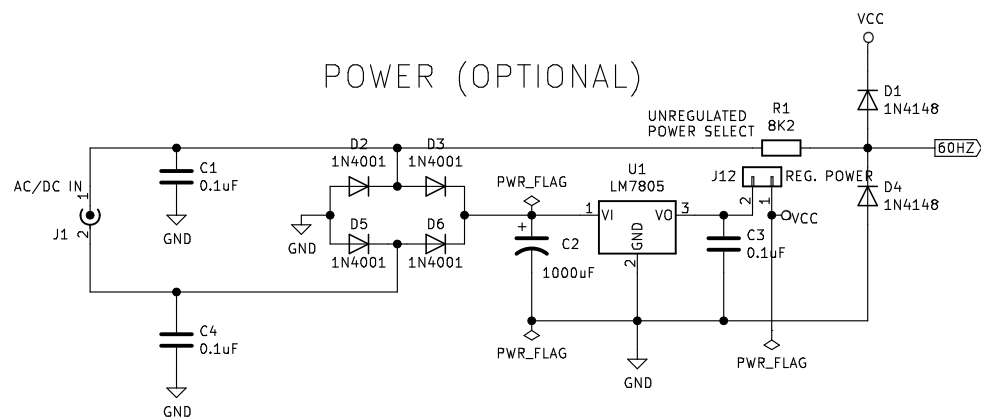
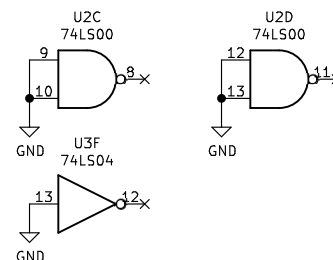


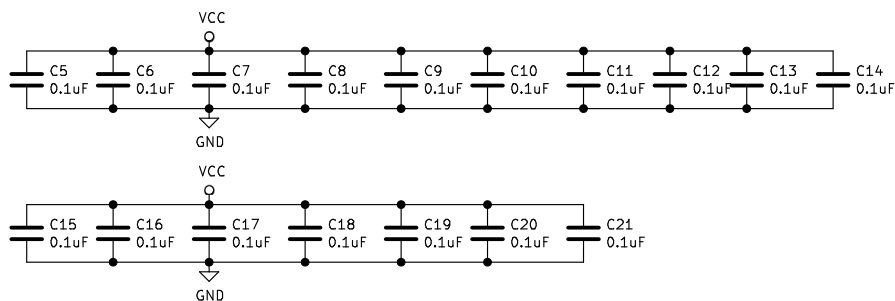
UNREGULATED
AC OR DC IN
7-15V



UNUSED GATES



BYPASS CAPS (ONE PER IC)



POWER

Jeff Tranter

Sheet: /Sheet 1/

File: sheet1.sch

Title: 6800 Single Board Computer

Size: USLetter

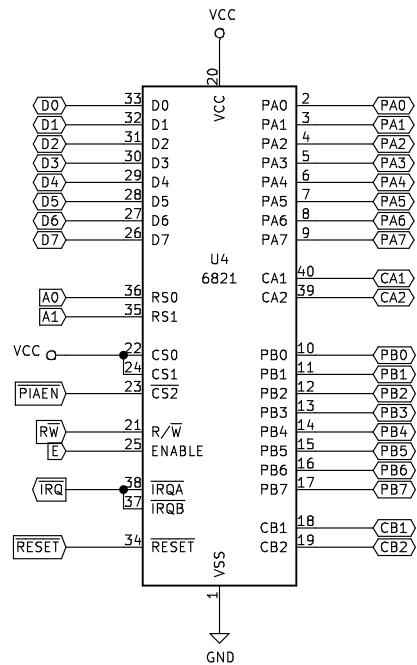
Date: 2022-02-09

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

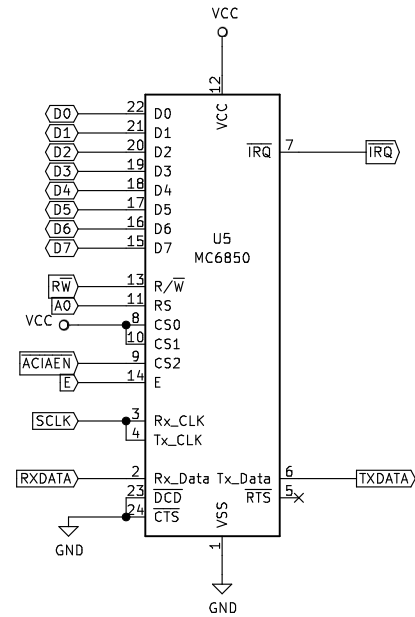
Rev: 0.1

Id: 2/6

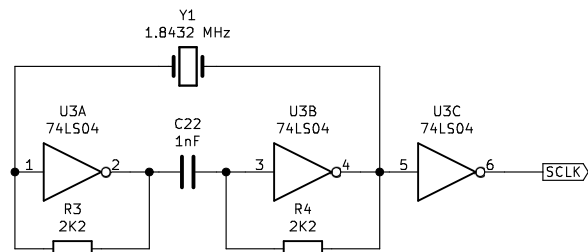
PARALLEL INTERFACE



SERIAL INTERFACE



SERIAL CLOCK (115,200 BPS X 16)



PARALLEL, SERIAL

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Sheet: /Sheet 3/

File: sheet3.sch

Title: 6800 Single Board Computer

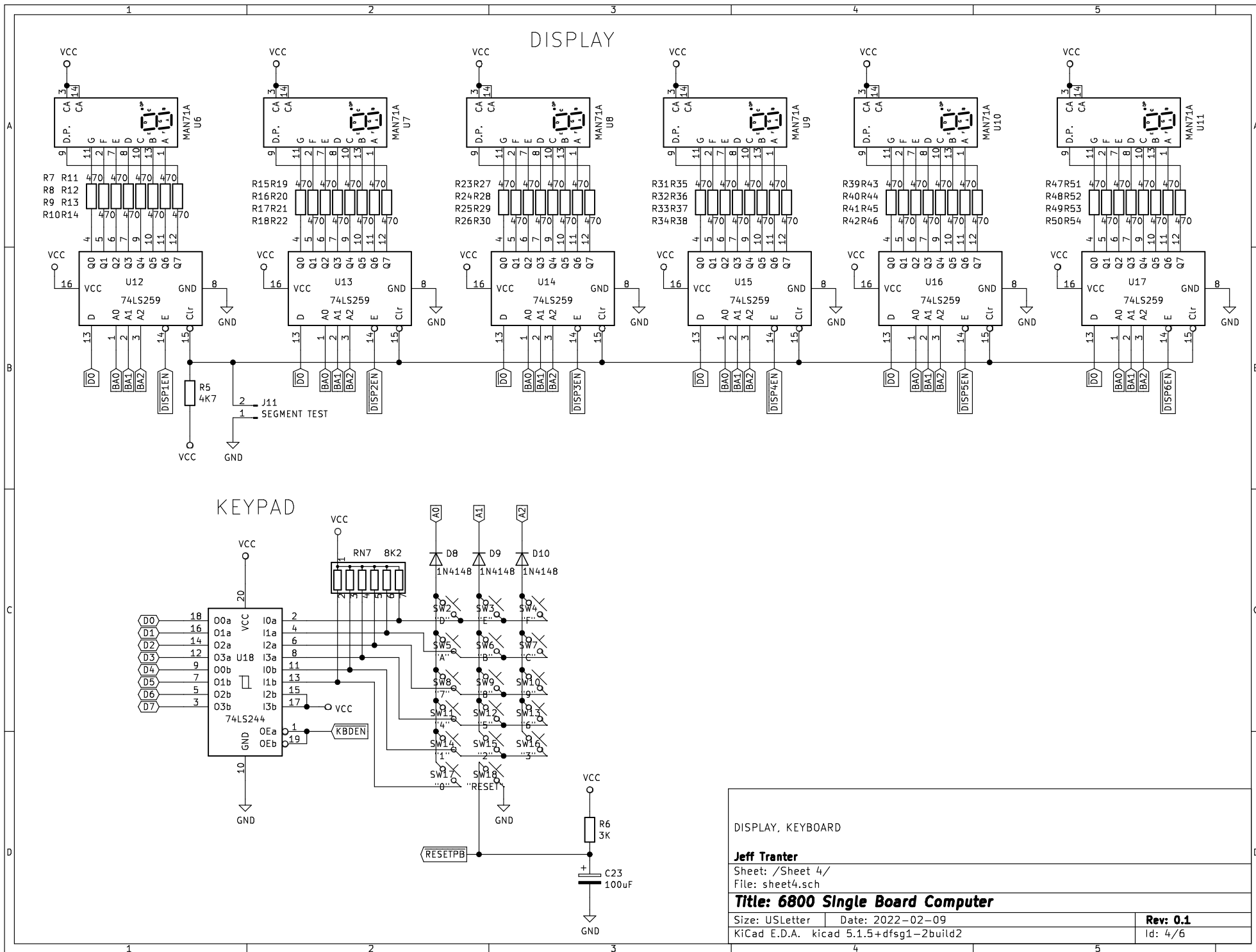
Size: USLetter

Date: 2022-02-09

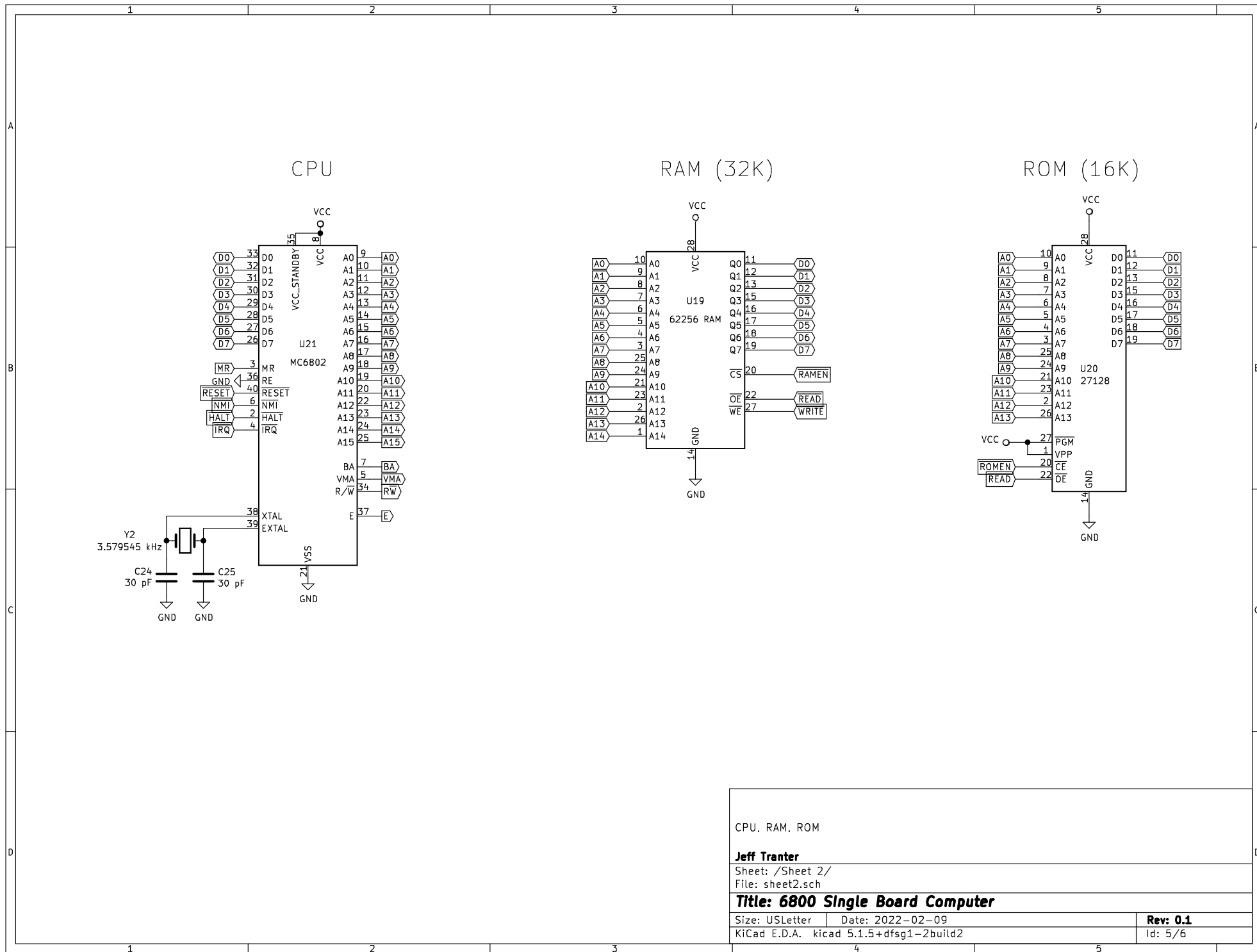
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 0.1

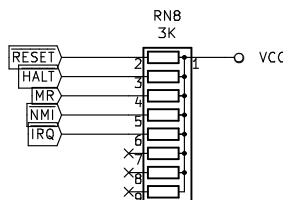
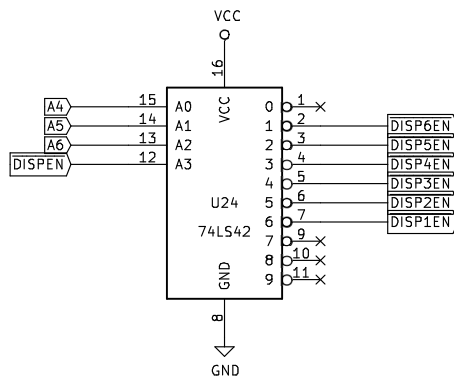
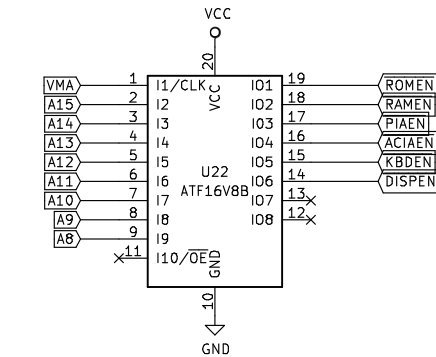
Id: 3/6



DISPLAY, KEYBOARD		
Jeff Tranter		
Sheet: /Sheet 4/		
File: sheet4.sch		
Title: 6800 Single Board Computer		
Size: USLetter	Date: 2022-02-09	Rev: 0.1
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 4/6

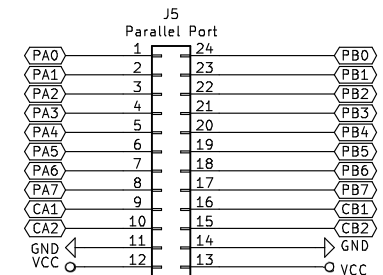
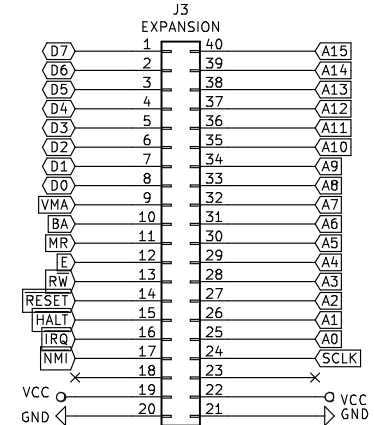
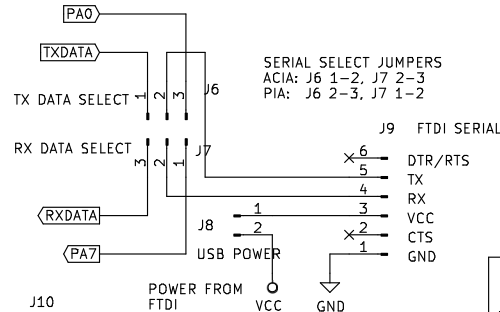
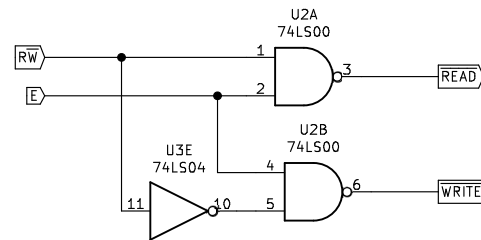
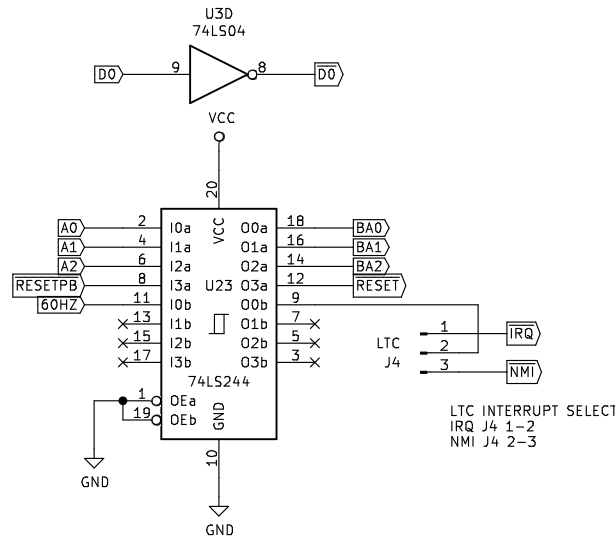
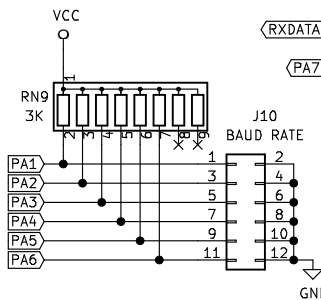


ADDRESS DECODING



BAUD RATE JUMPERS

PA1	PA2	PA3	PA4	PA5	PA6	RATE
H	H	H	H	H	H	110
L	L	L	L	H	H	300
L	L	L	L	L	H	600
L	L	L	L	L	L	1200
L	H	L	L	L	H	2400
L	H	L	L	L	L	4800
L	L	L	L	L	L	9600



DECODING. CONNECTORS

Jeff Tranter

Sheet: /Sheet 5/

File: sheet5.sch

Title: 6800 Single Board Computer

Size: USLetter Date: 2022-02-09

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 0.1

Id: 6/6