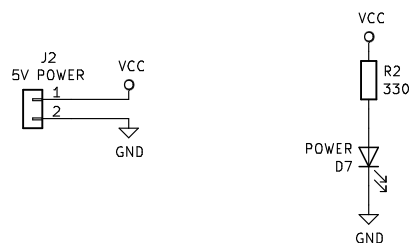
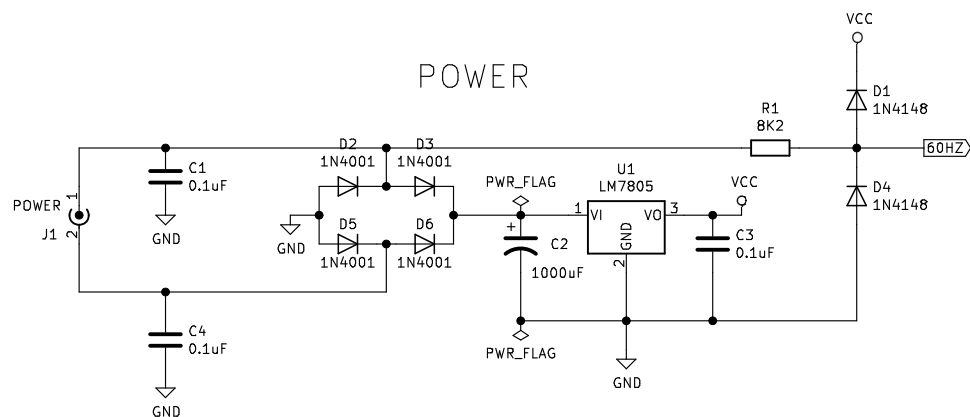
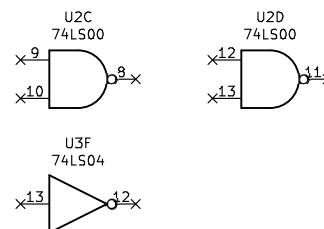


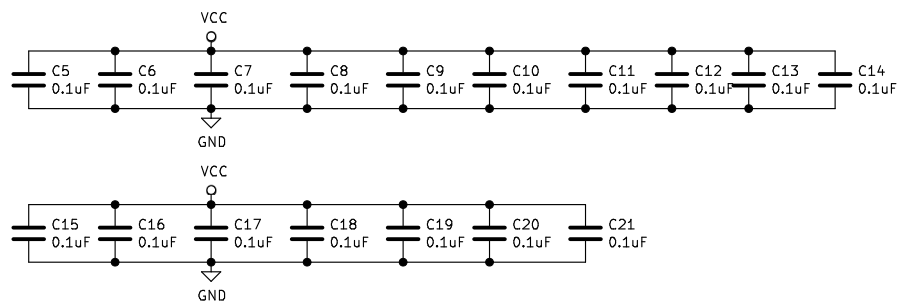
UNREGULATED
AC OR DC IN
7-15V



UNUSED GATES



BYPASS CAPS (ONE PER IC)



POWER

Jeff Tranter

Sheet: /Sheet 1/

File: sheet1.sch

Title: 6800 Single Board Computer

Size: USLetter

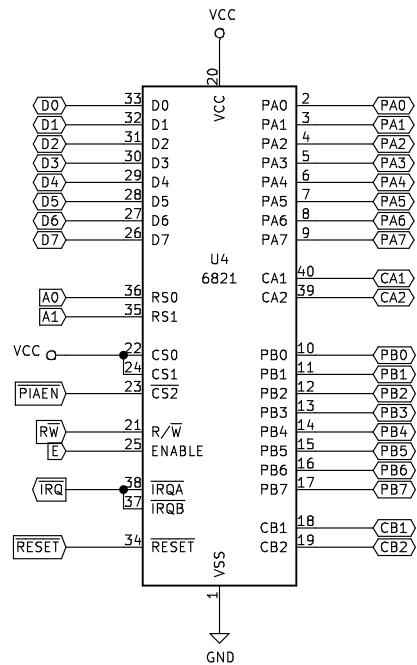
Date: 2022-01-22

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

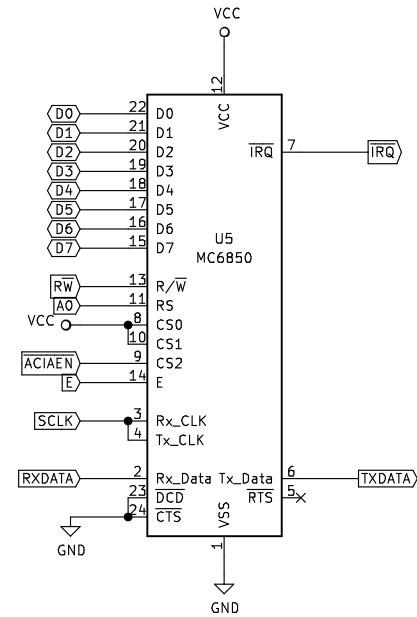
Rev: 0.1

Id: 2/6

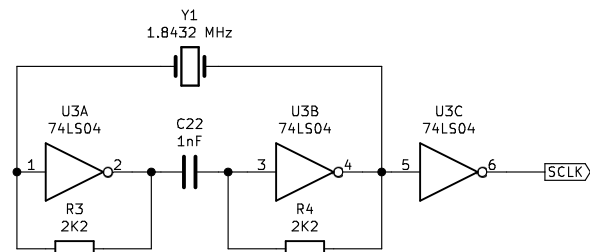
PARALLEL INTERFACE



SERIAL INTERFACE



SERIAL CLOCK (115,200 BPS X 16)



PARALLEL, SERIAL

Jeff Tranter

Sheet: /Sheet 3/

File: sheet3.sch

Title: 6800 Single Board Computer

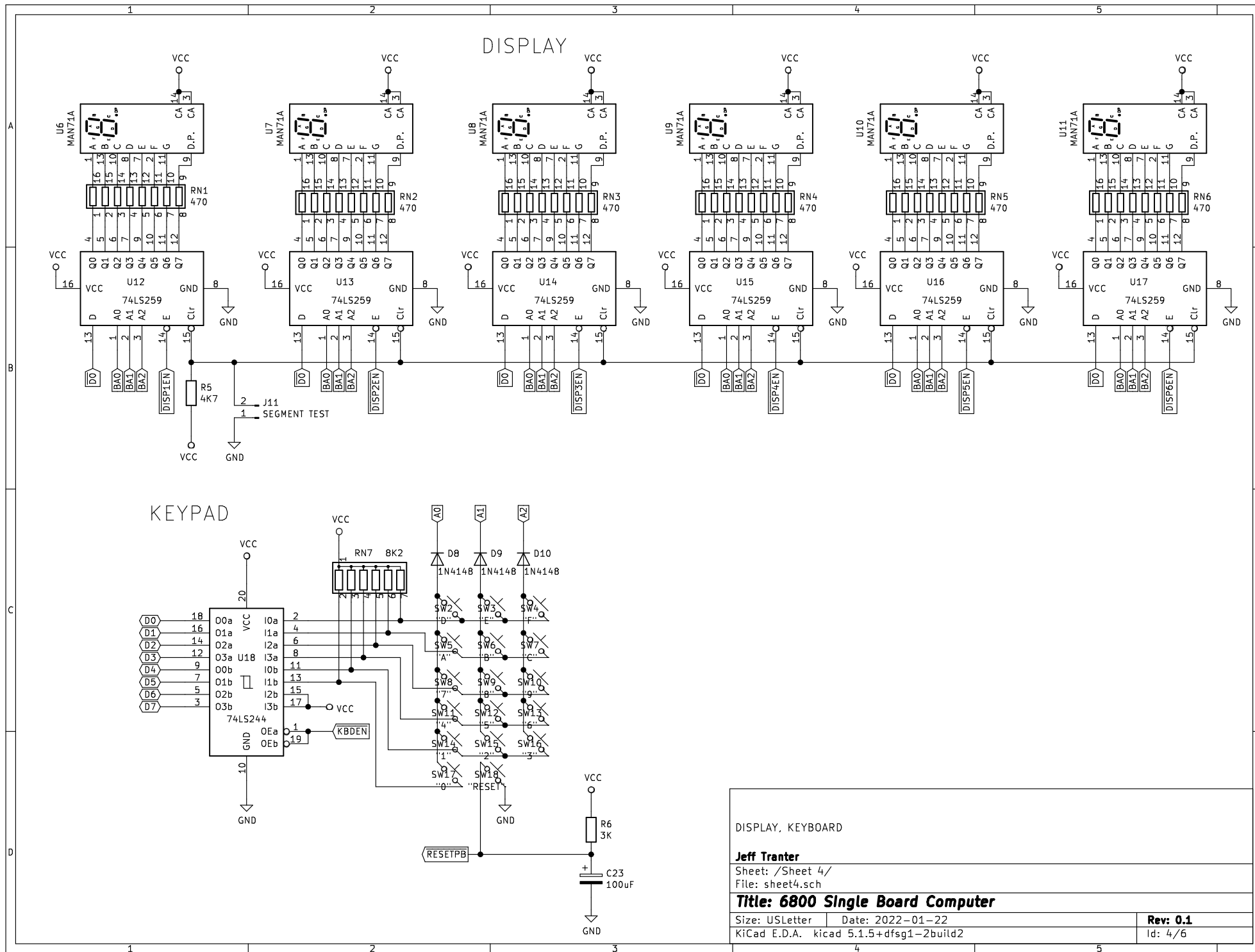
Size: USLetter

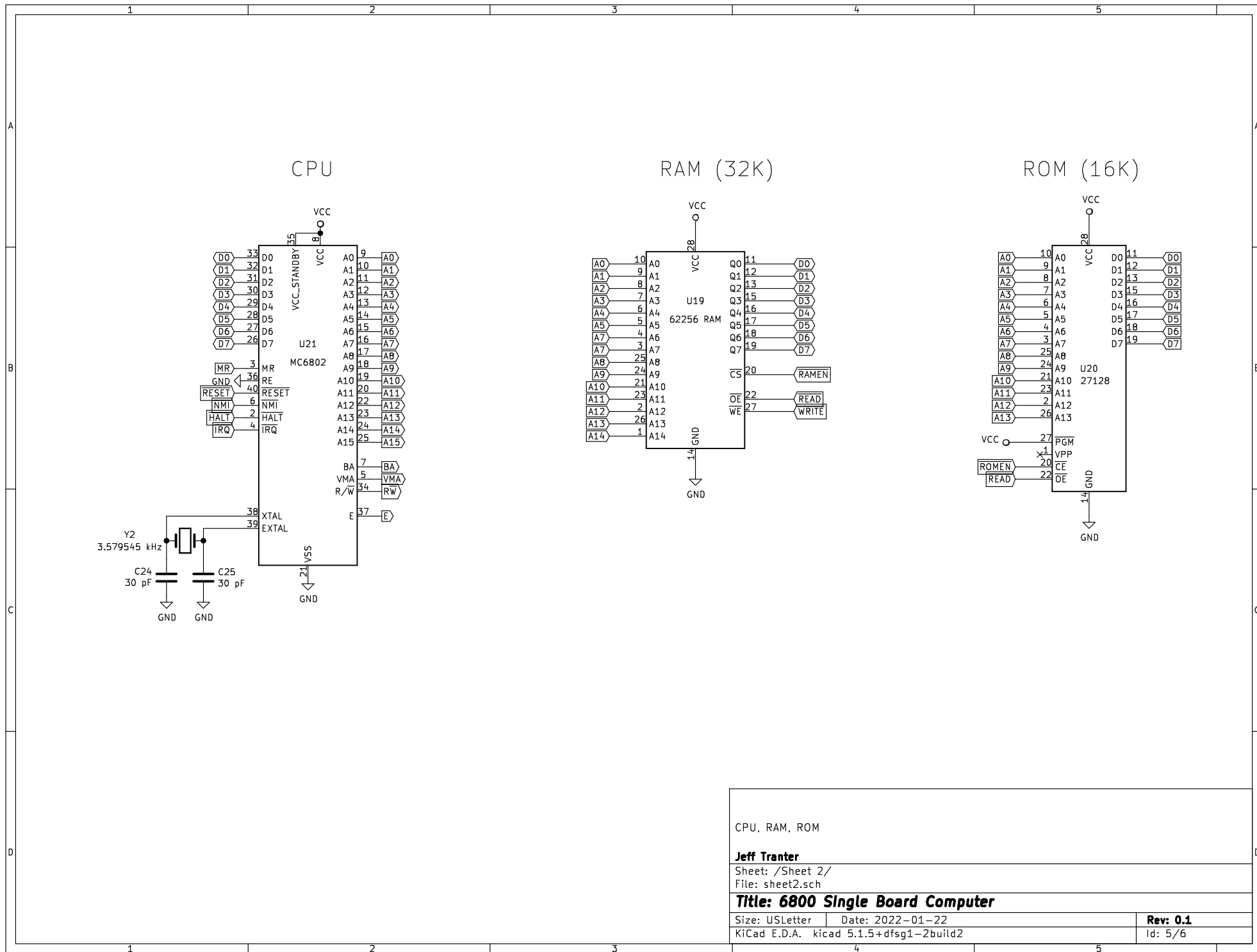
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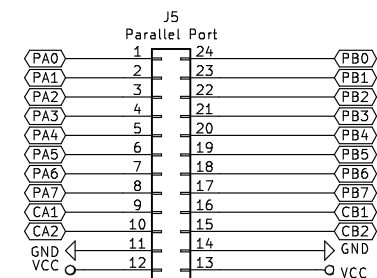
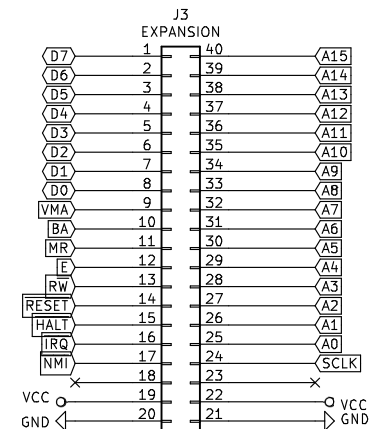
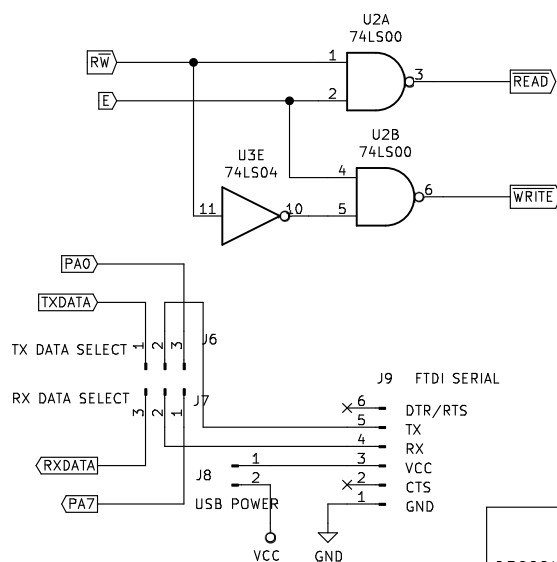
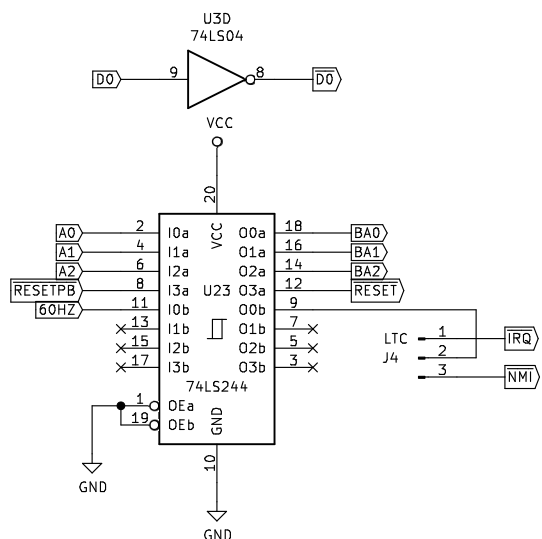
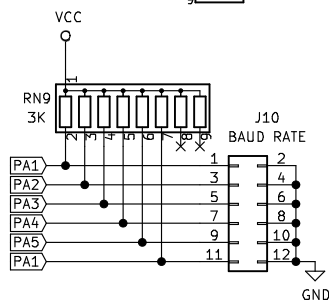
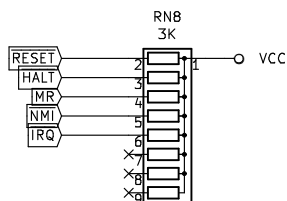
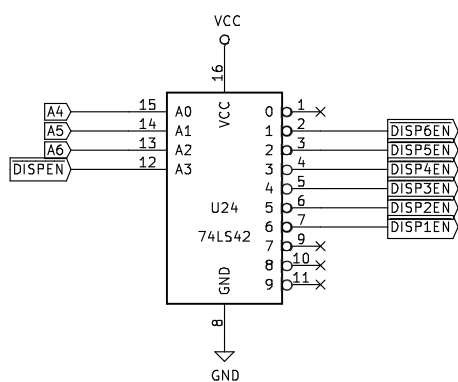
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 0.1

Id: 3/6







Id: 6/6