Sheet: Sheet 1

POWER

File: sheet1.sch

Sheet: Sheet 2

CPU, RAM, ROM

File: sheet2.sch

Sheet: Sheet 3

PARALLEL, SERIAL

File: sheet3.sch

Sheet: Sheet 4

DISPLAY, KEYBOARD

File: sheet4.sch

Sheet: Sheet 5

DECODING, CONNECTORS

File: sheet5.sch

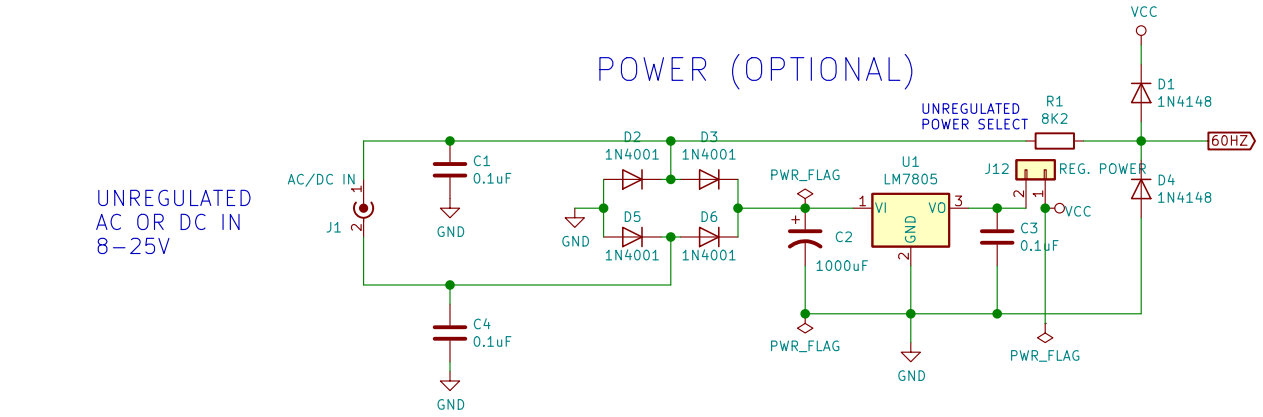
MAIN PAGE

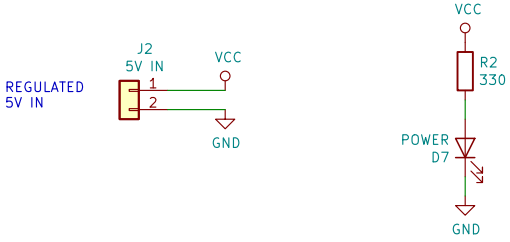
Jeff Tranter

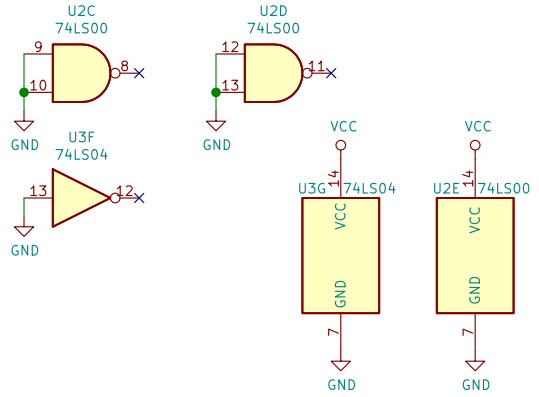
Sheet: /
File: 6800sbc.sch

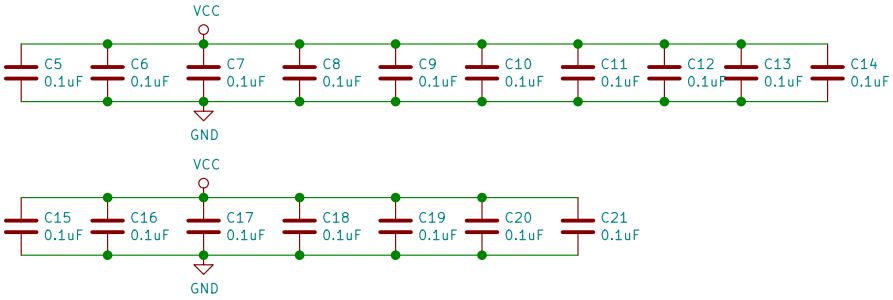
Title: 6800 SIngle Board Computer

Size: USLetter	Date: 2022-03-02	**Rev: 0.1**
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 1/6









POWER

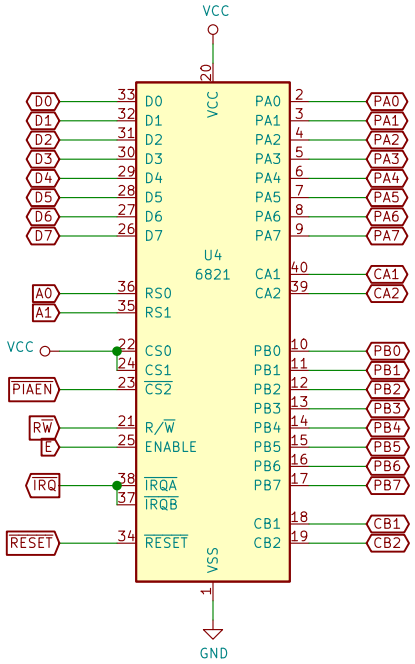
Jeff Tranter

Sheet: /Sheet 1/
File: sheet1.sch

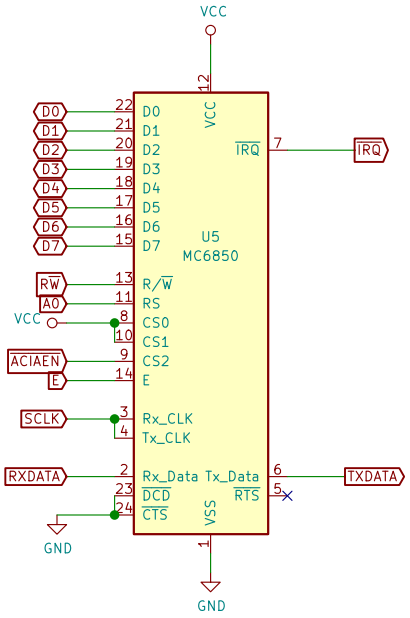
Title: 6800 SIngle Board Computer

Size: USLetter	Date: 2022-03-02	**Rev: 0.1**
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 2/6

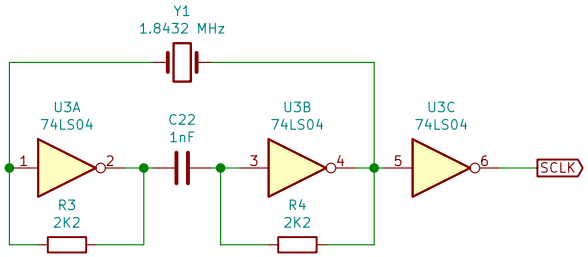
PARALLEL INTERFACE



SERIAL INTERFACE



SERIAL CLOCK (115,200 BPS X 16)



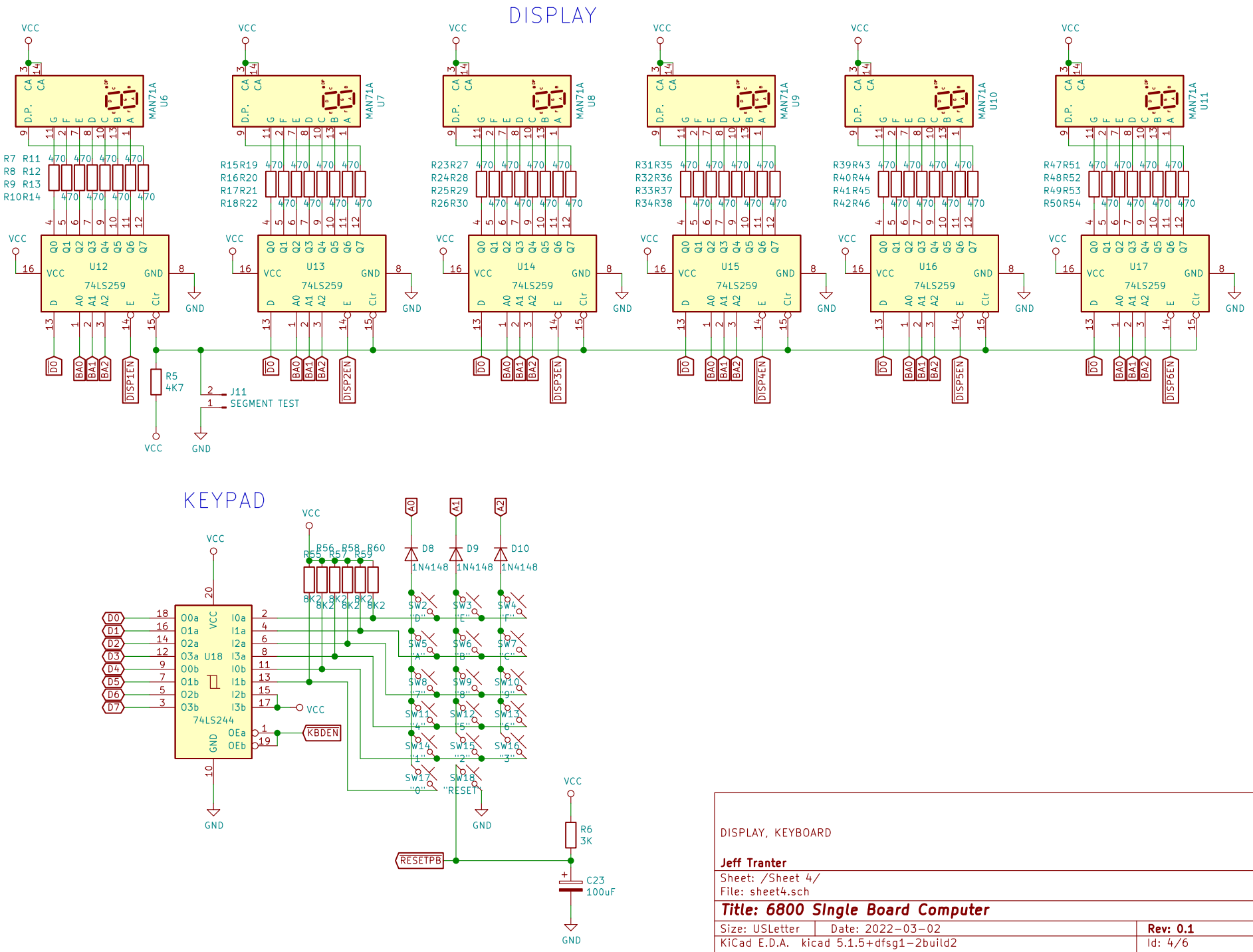
PARALLEL, SERIAL

Jeff Tranter

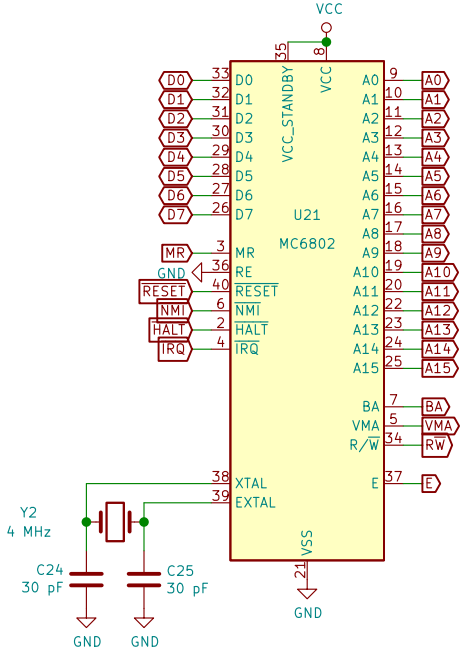
Sheet: /Sheet 3/
File: sheet3.sch

Title: 6800 SIngle Board Computer

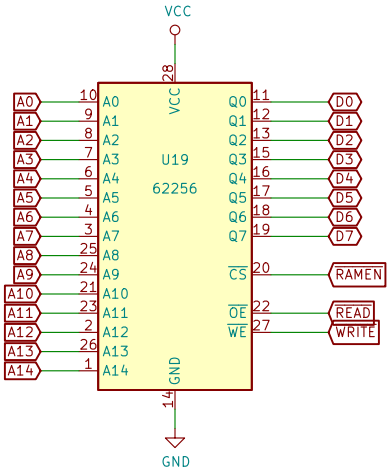
Size: USLetter	Date: 2022-03-02	**Rev: 0.1**
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 3/6


DISPLAY
KEYPAD
MAN71A
74LS259
74LS244
SEGMENT TEST
DISP1EN
DISP2EN
DISP3EN
DISP4EN
DISP5EN
DISP6EN
KBDEN
RESETPB
1N4148
DISPLAY, KEYBOARD
Jeff Tranter
Sheet: /Sheet 4/
File: sheet4.sch
Title: 6800 SIngle Board Computer
Size: USLetter
Date: 2022-03-02
Rev: 0.1
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2
Id: 4/6

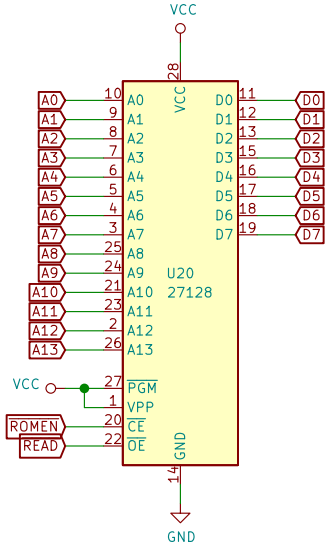
CPU



RAM (32K)



ROM (16K)



CPU, RAM, ROM

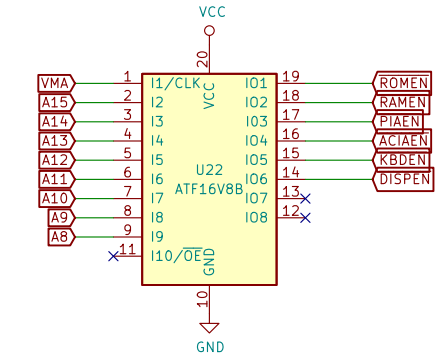
Jeff Tranter

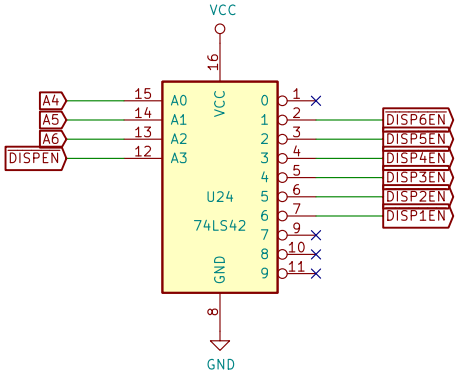
Sheet: /Sheet 2/
File: sheet2.sch

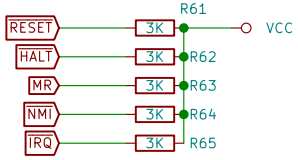
Title: 6800 SIngle Board Computer

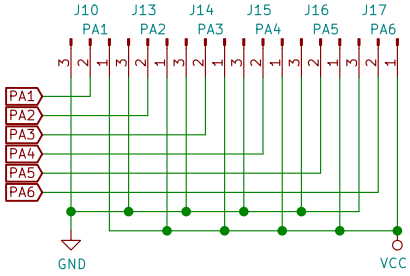
Size: USLetter	Date: 2022-03-02	**Rev: 1.0**
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 5/6

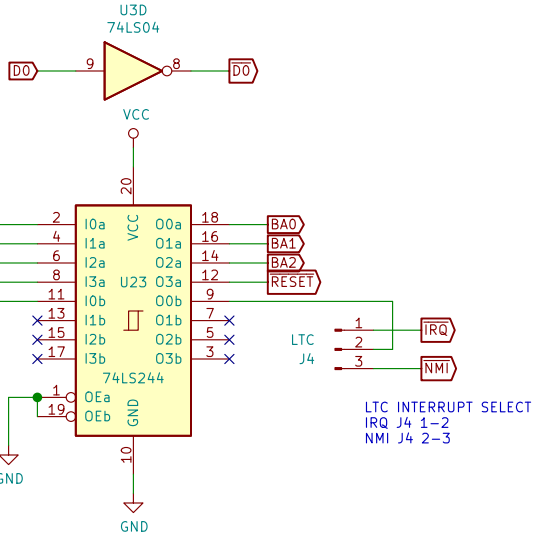
ADDRESS DECODING

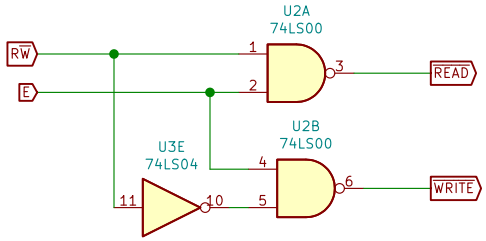


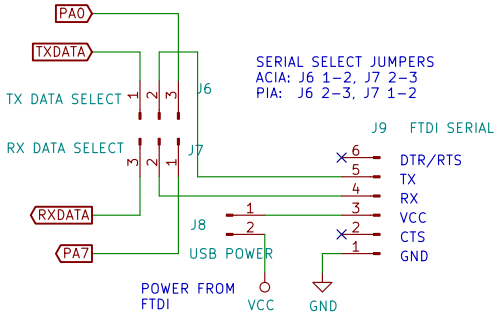


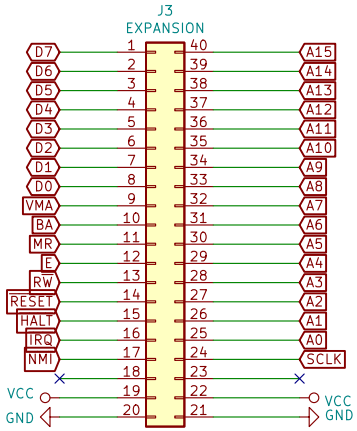


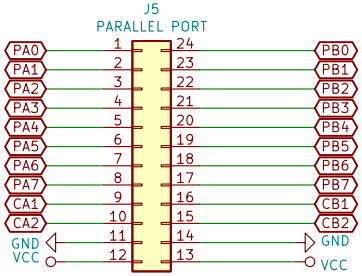












DECODING, CONNECTORS

Jeff Tranter

Sheet: /Sheet 5/
File: sheet5.sch

Title: 6800 SIngle Board Computer

Size: USLetter	Date: 2022-03-02	**Rev: 0.1**
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 6/6