Function DC 5V input	SoC Pin Name	Bank	Ball	Net Name VDD_5V	J1 1 2	Net Name VDD_5V	Ball	Bank	SoC Pin Name	Function DC 5V input
DC 5V input Ethernet Data 0 Positive				VDD_5V GND RGMIIØ TRPØ	3 4 5 6 7 8	VDD_5V GND GND				DC 5V input Ground Ground
Ethernet Data 0 Negative Ethernet Data 2 Positive				RGMII0_TRN0 GND RGMII0_TRP2	9 10	RGMII0_TRP1 RGMII0_TRN1 GND				Ethernet Data 1 Positive Ethernet Data 1 Negative Ground
Ethernet Data 2 Negative				RGMII0_TRN2 GND	15 16 17 18	RGMII0_TRP3 RGMII0_TRN3				Ethernet Data 3 Positive Ethernet Data 3 Negative
USB Data minus				USB_PHY_DP USB_PHY_DM GND	21 22 23 24	GND MDIO_CK MDIO_DATA	C10 C11		PS_MI052_501 PS_MI053_501	Ground MDIO Clk MDIO Data
USB OTG ID USB VBUS FMC	IO_0_35	Bank 35	G14	USB_PHY_ID USB_PHY_VBUS IO_B35_0	27 28	RGMII0_LED_ACT RGMII0_LED_LINK VBUS_SW_EN				Ethernet Status LED Ethernet Link LED USB Active high VBUS control output
FMC FMC	IO_L7P_T1_AD2P_35 IO_L7N_T1_AD2N_35 IO_L21P_T3_DQS_AD14P_35	Bank 35	M20	IO_B35_LP7 IO_B35_LN7 IO_B35_LP21	31 32 33 34	GND IO_B35_LP18 IO_B35_LN18	G19 G20		IO_L18P_T2_AD13P_35 IO_L18N_T2_AD13N_35	FMC FMC
FMC	IO_L21N_T3_DQS_AD14N_35 IO_L17P_T2_AD5P_35	Bank 35 Bank 35	N16 J20	IO_B35_LN21 IO_B35_LP17	37 38 39 40	IO_B35_LP9 IO_B35_LN9	L19 L20	Bank 35 Bank 35	IO_L9P_T1_DQS_AD3P_35 IO_L9N_T1_DQS_AD3N_35	FMC FMC
FMC FMC	IO_L17N_T2_AD5N_35 IO_L1P_T0_AD0P_35	Bank 35	C20	IO_B35_LN17 GND IO_B35_LP1	45 46	IO_B35_LP11 IO_B35_LN11 GND	L16 L17	Bank 35	IO_L11P_T1_SRCC_35 IO_L11N_T1_SRCC_35	FMC FMC
FMC FMC	IO_L1N_T0_AD0N_35 IO_L15P_T2_DQS_AD12P_35 IO_L15N_T2_DQS_AD12N_35	Bank 35	F19	IO_B35_LN1 IO_B35_LP15 IO_B35_LN15		IO_B35_LP4 IO_B35_LN4 IO_B35_LP8	D19 D20 M17	Bank 35 Bank 35	IO_L4P_T0_35 IO_L4N_T0_35 IO_L8P_T1_AD10P_35	FMC FMC
FMC FMC	IO_L12P_T1_MRCC_35 IO_L12N_T1_MRCC_35		K17 K18	GND IO_B35_LP12 IO_B35_LN12	55 56	IO_B35_LN8 IO_B35_LP16 IO_B35_LN16	M18 G17 G18	Bank 35	IO_L8N_T1_AD10N_35 IO_L16P_T2_35 IO_L16N_T2_35	FMC FMC
Power-supply pins for the output drivers of Bank 35	IO L5P T0 AD9P 35			VDDIO_35_PL GND IO_B35_LP5	59 <mark>60</mark> 61 62	VDDIO_35_PL GND IO B35_LP10			IO L10P_T1 AD11P_35	Power-supply pins for the output drivers of Bank 35 FMC
FMC FMC	IO_L5N_T0_AD9N_35 IO_L3P_T0_DQS_AD1P_35	Bank 35 Bank 35	E19 E17	IO_B35_LN5 IO_B35_LP3	65 66 67 68	IO_B35_LN10 IO_B35_LP14	J19 J18	Bank 35 Bank 35	IO_L10N_T1_AD11N_35 IO_L14P_T2_AD4P_SRCC_35	FMC FMC
FMC FMC	IO_L3N_T0_DQS_AD1N_35 IO_L2P_T0_AD8P_35 IO_L2N_T0_AD8N_35	Bank 35	B19	IO_B35_LN3 IO_B35_LP2 IO_B35_LN2	71 72 73 74	IO_B35_LN14 IO_B35_LP6 IO_B35_LN6	H18 F16 F17	Bank 35	IO_L14N_T2_AD4N_SRCC_35 IO_L6P_T0_35 IO_L6N_T0_VREF_35	FMC FMC
FMC FMC	IO_L13P_T2_MRCC_35 IO_L13N_T2_MRCC_35			GND IO_B35_LP13 IO_B35_LN13	77 78	GND IO_B35_LP24 IO_B35_LN24	K16		IO_L24P_T3_AD15P_35 IO_L24N_T3_AD15N_35	FMC FMC
FMC	IO_L19P_T3_35	Bank 35	H15	GND IO_B35_LP19	81 82 83 84	IO_B35_LP20 IO_B35_LN20	K14 J14	Bank 35 Bank 35	IO_L20P_T3_AD6P_35 IO_L20N_T3_AD6N_35	FMC FMC
FMC FMC	IO_L19N_T3_VREF_35 IO_L23P_T3_35 IO_L23N_T3_35	Bank 35	M14	IO_B35_LN19 IO_B35_LP23 IO_B35_LN23	89 90	IO_B35_LP22 IO_B35_LN22 IO_B35_25	L14 L15 J15	Bank 35	IO_L22P_T3_AD7P_35 IO_L22N_T3_AD7N_35 IO_25_35	FMC FMC
XADC analog positive supply voltage XADC	VP_6		Ј9 К9	GND XADC_VCC XADC_INP0	93 94 95 96	GND JTAG_TCK JTAG_TMS	F9 J6	Bank 0 Bank 0	TMS_0	JTAG Test Clock JTAG Test Mode Select
XADC XADC XADC	VN_0 DXP_0 DXN 0		L10 M9 M10	XADC_INNØ XADC_TEMP_P XADC_TEMP_N	99 100	JTAG_TDI JTAG_TDO GND	G6 F6	Bank 0 Bank 0		JTAG Test Data Input JTAG Test Data Output
XADC Ground Ground PS MI00	PS MIO0 500		E6	XADC_TENP_N XADC_GND GND PS_MI00	103 <mark>104</mark> 105 <mark>106</mark>	SDIO0_CLK SDIO0_CMD SDIO0_D1	C17	Bank 501	PS_MIO40_501 PS_MIO41_501 PS_MIO42_501	SDIO Clock SDIO Command SDIO Data 0
JTAG Mode Select Pin Boot Mode Select Pin				S_JTAG_H SD_BOOT_CONF_H	109 <mark>110</mark> 111 112	SDIO0_D2 SDIO0_D3	A9 F13	Bank 501 Bank 501	PS_MI043_501 PS_MI044_501	SDIO Data 1 SDIO Data 2
CAN Transmit CAN Receive eMMC Data 0	PS_MI08_500 PS_MI09_500 PS_MI010_500	Bank 500	B5		113 114 115 116 117 118	SDIO0_D4 SDIO0_CD SDIO0_WP	D16	Bank 501	PS_MIO45_501 PS_MIO46_501 PS_MIO47_501	SDIO Data 3 SDIO Card Detection SDIO Write Protected
eMMC Clock eMMC Data 1	PS_MI011_500 PS_MI012_500 PS_MI013_500	Bank 500	D9	PS_MIO11 PS_MIO12 PS_MIO13	121 122	GND UART1_TX UART1_RX	_		PS_MIO48_501 PS_MIO49_501	UART Transmit UART Receive
eMMC Data 2 eMMC Data 3	PS_MI014_500 PS_MI015_500	Bank 500	C5	PS_MI014 PS_MI015	125 126 127 128	PS_501_MIO_50 PS_501_MIO_51 FPGA_INITh	B13 B9	Bank 501 Bank 501	PS_MI050_501 PS_MI051_501	I2C0_SCL I2C0_SDA
System Power Good Active Low, PS Bank 500 Reset Output				Sys_PG PS_500_RESET_OUTn	131 132 133 134	FPGA_WARMRESETn FPGA_INIT_DONE	R10 L6 R11	Bank 0 Bank 0		Active Low, indicates initialization of configuration memory. Active Low, asynchronous reset to configuration logic. Active High, DONE indicates successful completion of configuration.
Active Low, PS Warm Reset Input Active Low, eMMC_Reset Power-supply pins for the output drivers of Bank 500				PS_WARMRESET_INn eMMC_RSTn VDDIO_500_PS	137 138	VBAT_KEY_BACK_1.8V VDDIO_501_PS GND	F11	Bank 0	VCCBATT_0	PL decryption key memory backup Power-supply pins for the output drivers of Bank 501
Function DC 5V input DC 5V input	SoC Pin Name	Bank	Ball	Net Name VDD_5V VDD 5V	J2 1 2 3 4	Net Name VDD_5V VDD_5V	Ball	Bank	SoC Pin Name	Function DC 5V input DC 5V input
LCD Display Enable				 GND	5 6	GND				
U.CD Data Enable		Bank 34	T19	IO_B34_25	7 8	IO_B34_0	R19	Bank 34		LCD Backlight Enable
LCD Data Enable LCD Peix Clock LCD Data line 14	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34	Bank 34 Bank 34 Bank 34	N18 P19 W18	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LP22	7 8 9 10 11 12 13 14	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LP21	P15 P16 V17	Bank 34 Bank 34 Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal
LCD Peix Clock	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34	Bank 34 Bank 34 Bank 34 Bank 34 Bank 34	N18 P19 W18 W19 N17	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LP22 IO_B34_LN22 IO_B34_LP23 IO_B34_LN23	7 8 9 10 11 12 13 14 15 16 17 18 19 20	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LP21 IO_B34_LN21 IO_B34_LP20 IO_B34_LN20	P15 P16	Bank 34 Bank 34 Bank 34 Bank 34 Bank 34	IO_L24P_T3_34 IO_L24N_T3_34	Touchscreen Reset Touchscreen Interrupt
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23P_T3_34	Bank 34	N18 P19 W18 W19 N17	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LP22 IO_B34_LN22 IO_B34_LN22 IO_B34_LP23	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LP21 IO_B34_LN21 IO_B34_LP20	P15 P16 V17 V18 T17	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23P_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T3_34 IO_L19N_T3_VREF_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17	IO_B34_25 IO_B34_LP13 IO_B34_LP12 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 GND IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LP21 IO_B34_LN21 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LP18 IO_B34_LN18 IO_B34_LP12 IO_B34_LN12	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 2 LCD Data line 3
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23P_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T3_34 IO_L19P_T1_DQS_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LP22 IO_B34_LN22 IO_B34_LN23 GND IO_B34_LP19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN21 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL	P15 P16 V17 V18 T17 R18 V16 W16 U18	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 1	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23N_T3_34 IO_L13P_T3_34 IO_L19P_T3_34 IO_L19P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5N_T0_34 IO_L11P_T1_SRCC_34 IO_L11N_T1_SRCC_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN1 IO_B34_LN1 IO_B34_LN1	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN21 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LP18 IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LP15 IO_B34_LN15	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19 N20 P20 T20 U20	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L12N_T1_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 0 LCD Data line 1	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23N_T3_34 IO_L13P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5N_T0_34 IO_L5N_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 U15 P14 R14	IO_B34_25 IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 GND IO_B34_LP19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LP1 IO_B34_LN5	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN21 IO_B34_LN20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LP15	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19 N20 P20 T20	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23P_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T3_34 IO_L19P_T1_DQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L11N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 U15 P14 R14 V12 W13	IO_B34_25 IO_B34_LP13 IO_B34_LP12 IO_B34_LP22 IO_B34_LP23 IO_B34_LP23 IO_B34_LP19 IO_B34_LP19 IO_B34_LP9 IO_B34_LP9 IO_B34_LP9 IO_B34_LP5 IO_B34_LP5 IO_B34_LP1 IO_B34_LP6 IO_B34_LP4 IO_B34_LP4 IO_B34_LP4	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52	IO_B34_0 IO_B34_LP24 IO_B34_LP21 IO_B34_LP21 IO_B34_LP20 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LP18 IO_B34_LP12 IO_B34_LP12 IO_B34_LP12 IO_B34_LP12 IO_B34_LP14 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LP15 IO_B34_LN15 IO_B34_LP10 IO_B34_LP10 IO_B34_LP10 IO_B34_LP10 IO_B34_LP10 IO_B34_LP10 IO_B34_LP16 IO_B34_LN16 GND	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19 N20 P20 U20 V15 W15 V20 W20	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L20N_T3_34 IO_L18N_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14P_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15N_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L10P_T1_34 IO_L10N_T1_34 IO_L16N_T2_34 IO_L16N_T2_34 IO_L16N_T2_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L6P_T0_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34 IO_L7P_T1_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 Y16 Y17 U13	IO_B34_L95 IO_B34_LP13 IO_B34_LP22 IO_B34_LN22 IO_B34_LN23 IO_B34_LP29 IO_B34_LP19 IO_B34_LP19 IO_B34_LP9 IO_B34_LP9 IO_B34_LP5 IO_B34_LP5 IO_B34_LP5 IO_B34_LP1 IO_B34_LP1 IO_B34_LP1 IO_B34_LN6 IO_B34_LP1 IO_B34_LP4 IO_B34_LP4 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7 IO_B34_LP7	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58	IO_B34_0 IO_B34_LP24 IO_B34_LP21 IO_B34_LP21 IO_B34_LP20 IO_B34_LP20 IO_B34_LP20 IO_B34_LP18 IO_B34_LP18 IO_B34_LP12 IO_B34_LP12 IO_B34_LP12 IO_B34_LP14 IO_B34_LP14 IO_B34_LP14 IO_B34_LP15 IO_B34_LP15 IO_B34_LP15 IO_B34_LP16 IO_B34_LP16 IO_B34_LP16 IO_B34_LP16 IO_B34_LP16 IO_B34_LP17 IO_B34_LP17 IO_B34_LP17 IO_B34_LP17 IO_B34_LP17 IO_B34_LP17 IO_B34_LP17	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19 N20 P20 U20 V15 W15 V20 W20 Y18 Y19 W14	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14P_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L16N_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34 IO_L8P_T1_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L19P_T3_34 IO_L19P_T1_OQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L7P_T1_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L7P_T1_34 IO_L7P_T1_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 Y16 Y17 U13	IO_B34_L95 IO_B34_LN23 IO_B34_LN22 IO_B34_LN23 IO_B34_LP23 IO_B34_LP19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN3	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN20 IO_B34_LN20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LN15 IO_B34_LN15 IO_B34_LN16 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN8 IO_B34_LN8 IO_B34_LN8 IO_B34_LN2	P15 P16 V17 V18 T17 R18 V16 W16 U18 U19 N20 P20 U20 V15 W15 V20 W20 Y18 Y19	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14P_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L10P_T1_34 IO_L10P_T1_34 IO_L10P_T2_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 7 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L1P_T1_34 IO_L7P_T1_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 Y16 Y17 U13 V13 T11	IO_B34_L95 IO_B34_LN23 IO_B34_LN22 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN2 GND IO_B34_LN4 GND IO_B34_LN5 IO_B34_LN6 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN3 IO_B34_LN3 IO_B34_LN3	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN20 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LN15 IO_B34_LN15 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN8 IO_B34_LN8 IO_B34_LN8	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 Y18 Y19 W14 Y14 T12	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15N_T2_DQS_34 IO_L16N_T1_34 IO_L16N_T1_34 IO_L16N_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L18N_T1_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L18N_T1_34 IO_L8N_T1_34 IO_L2P_T0_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 9 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L1P_T1_34 IO_L7P_T1_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 Y16 Y17 U13 V13 T11	IO_B34_L95 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN29 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN1 IO_B34_LN4 GND IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN1	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN20 IO_B34_LP20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VDDIO_34_PL GND IO_B34_LN15 IO_B34_LN15 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN18 IO_B34_LN2 IO_B34_LN8 IO_B34_LN8 IO_B34_LN2 GND NC	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 Y18 Y19 W14 Y14 T12	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15N_T2_DQS_34 IO_L16N_T1_34 IO_L16N_T1_34 IO_L16N_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L18N_T1_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L18N_T1_34 IO_L8N_T1_34 IO_L2P_T0_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L23P_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L11P_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L1P_T1_34 IO_L7P_T1_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 Y16 Y17 U13 V13 T11	IO_B34_LP1 IO_B34_LN22 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN29 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN5 VDDIO_B34_LN5 VDDIO_B34_LN11 IO_B34_LN6 IO_B34_LN11 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN3 IO_B34_LN3 IO_B34_LN3 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN3 IO_B34_LN3 IO_B34_LN3 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN1 IO_B34_LN3 IO_B34_IN3	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80	IO_B34_0 IO_B34_LP24 IO_B34_LN24 IO_B34_LN21 IO_B34_LN20 IO_B34_LN20 IO_B34_LN20 GND IO_B34_LN18 IO_B34_LN12 IO_B34_LN12 IO_B34_LN14 VODIO_34_PL GND IO_B34_LN15 IO_B34_LN15 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN16 IO_B34_LN17 IO_B34_LN17 IO_B34_LN17 IO_B34_LN18 IO_B34_LN18 IO_B34_LN18 IO_B34_LN10 IO_B34_LN2 IO_B34_LN	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 Y18 Y19 W14 Y14 T12	Bank 34	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20N_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15N_T2_DQS_34 IO_L16N_T1_34 IO_L16N_T1_34 IO_L16N_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L8N_T1_34 IO_L18N_T1_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L18N_T1_34 IO_L8N_T1_34 IO_L2P_T0_34	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 6 LCD Data line 6 LCD Data line 2 LCD Data line 3 FMC FMC Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM
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LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 9 LCD Data line 5 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L22N_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V12 W13 V13 V11 T10 V10 T9 U10 U9 U8 U7 V8	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN22 IO_B34_LN23 GND IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN5 VDDIO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN7 IO_B34_LN1	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 98 99 192 93 94 95 96 97 98 99 190 101 102	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN20 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 IO B34 LN14 IO B34 LN15 IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN2 IO B34 LN10	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 W14 Y14 T12 U12 V17	Bank 34 Bank 3	IO_L24P_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L12P_T1_MRCC_34 IO_L12P_T1_MRCC_34 IO_L14P_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16P_T2_34 IO_L16P_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L18P_T3_34 IO_L2P_T0_34 IO_L18N_T1_3 IO_L18N_T2_13 IO_L18N_T2_13 IO_L18N_T2_13 IO_L18N_T2_SRCC_13 IO_L14N_T2_SRCC_13 IO_L14N_T2_SRCC_13	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 9 LCD Data line 6 LCD Data line 0 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L1N_T1_3CC_34 IO_L1N_T1_3CC_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN23 GND IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN2 IO_B34_LN3 IO_B34_LN4 GND IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN1 GND VDDIO_13_PL IO_B34_LN1 GND NC	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 VDDIO 34 PL GND IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN17 IO B34 LN16 IO B34 LN17 IO B34 LN17 IO B34 LN17 IO B34 LN2 IO	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 V18 V19 W14 Y14 T12 U12 V17 V19 W17 V19 W19 V19 V19 V19 V19 V19 V19 V19 V19 V19 V	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L18N_T1_31 IO_L2P_T0_34 IO_L18N_T1_31 IO_L18P_T2_I3 IO_L13P_T2_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L19P_T3_I3	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD horizontal Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 16 LCD Data line 10 LCD Data line 8 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 6 LCD Data line 6 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN29 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN4 IO_B34_LN7 IO_B34_LN1	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN17 IO B34 LN17 IO B34 LN17 IO B34 LN2 IO B34 LN16 IO B13 LN16 IO B13 LN16 IO B13 LN18 IO B13 LN18 IO B13 LN18 IO B13 LN14 IO B13 LN19	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 V19 V19 V19 V19 V19 V10 V19 V10	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21N_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12N_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15N_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16N_T1_34 IO_L16N_T1_34 IO_L16N_T1_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17N_T2_34 IO_L17N_T2_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L18N_T1_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L2N_T0_34 IO_L18N_T1_33 IO_L20N_T3_13 IO_L20N_T3_13 IO_L18P_T2_13 IO_L18N_T2_13 IO_L18N_T2_13 IO_L18N_T2_T2_MRCC_13 IO_L13N_T2_MRCC_13 IO_L13N_T2_MRCC_13	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD borizontal Synchronization signal LCD Data line 12 LCD Data line 6 LCD Data line 6 LCD Data line 7 LCD Data line 2 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 16 LCD Data line 10 LCD Data line 8 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 6 LCD Data line 6 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN29 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN5 VDDIO_B34_LN6 IO_B34_LN6 IO_B34_LN6 IO_B34_LN7 IO_B34_LN9 IO_B34_LN9 IO_B34_LN9 IO_B34_LN1 IO_B	7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 IO B34 LN14 IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN17 IO B34 LN2 IO B34 LN8 IO B34 LN8 IO B34 LN2 GND NC NC NC NC NC NC NC NC IO B13 LN16 IO B13 LN18 GND IO B13 LN18 GND IO B13 LN19	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 W14 Y14 T12 U12 U12 V19	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L18N_T1_31 IO_L2P_T0_34 IO_L18N_T1_31 IO_L18P_T2_I3 IO_L13P_T2_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L19P_T3_I3	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 7 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 9 LCD Data line 6 LCD Data line 6 LCD Data line 6 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN23 IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN5 VDDIO_B34_LN5 VDDIO_B34_LN6 IO_B34_LN7 IO_B34_LN1 GND NC NC NC NC NC NC OC ONC OC	7 8 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 GND IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 VODIO 34 PL GND IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN2 GND NC NC NC NC NC NC NC IO B13 LN16 IO B13 LN10 I	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 W14 Y14 T12 U12 U12 V19	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L18N_T1_31 IO_L2P_T0_34 IO_L18N_T1_31 IO_L18P_T2_I3 IO_L13P_T2_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L19P_T3_I3	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 7 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 10 LCD Data line 11 LCD Data line 8 LCD Data line 9 LCD Data line 9 LCD Data line 6 LCD Data line 6 LCD Data line 6 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN23 IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 IO_B34_LN4 IO_B34_LN4 IO_B34_LN5 VDDIO_B34_LN6 IO_B34_LN7 IO_B34_LN1 GND IO_B34_LN1	7 8 8 9 10 111 12 12 12 12 12 12 12 12 12 12 12 12	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN18 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 IO B34 LN15 IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN16 GND IO B34 LN17 IO B34 LN2 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B35 LN16 IO B13 LN16 IO B13 LN16 IO B13 LN16 IO B13 LN18 IO B13 LN18 IO B13 LN19 IO B13	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 W14 Y14 T12 U12 U12 V19	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L18N_T1_31 IO_L2P_T0_34 IO_L18N_T1_31 IO_L18P_T2_I3 IO_L13P_T2_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L19P_T3_I3	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 7 LCD Data line 3 FMC
LCD Peix Clock LCD Data line 14 LCD Data line 15 LCD Data line 16 LCD Data line 10 LCD Data line 8 LCD Data line 8 LCD Data line 9 LCD Data line 4 LCD Data line 6 LCD Data line 6 LCD Data line 1 Power-supply pins for the output drivers of Bank 34 FMC FMC FMC FMC FMC FMC FMC FMC FMC FM	IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L22P_T3_34 IO_L23N_T3_34 IO_L23N_T3_34 IO_L19P_T3_34 IO_L19P_T1_QS_34 IO_L9P_T1_DQS_34 IO_L9N_T1_DQS_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L5P_T0_34 IO_L1N_T1_SRCC_34 IO_L6P_T0_34 IO_L6P_T0_34 IO_L4P_T0_34 IO_L4P_T0_34 IO_L4N_T0_34 IO_L4N_T0_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L3P_T0_DQS_PUDC_B_34 IO_L1P_T0_34 IO_L1P_T0_34	Bank 34	N18 P19 W18 W19 N17 P18 R16 R17 T16 U17 T14 T15 U14 V15 P14 R14 V12 W13 V13 V11 T10 V10 U9 U8 U7 V7 V8 W8 V6	IO_B34_LP13 IO_B34_LN13 IO_B34_LN22 IO_B34_LN23 IO_B34_LN23 IO_B34_LN19 IO_B34_LN19 IO_B34_LN9 IO_B34_LN9 IO_B34_LN5 VDDIO_34_PL GND IO_B34_LN11 IO_B34_LN11 IO_B34_LN11 IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN4 GND IO_B34_LN7 IO_B34_LN4 GND IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN7 IO_B34_LN1 IO_B34_LN1 GND IO_B34_LN1 IO_B34_L	7 8 8 9 10 111 12 13 14 15 16 16 17 18 19 19 19 19 19 19 19 19 19 19 19 19 19	IO B34 0 IO B34 LP24 IO B34 LN24 IO B34 LN21 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN20 IO B34 LN18 IO B34 LN18 IO B34 LN12 IO B34 LN12 IO B34 LN12 IO B34 LN14 IO B34 LN15 IO B34 LN15 IO B34 LN15 IO B34 LN16 IO B34 LN16 IO B34 LN16 IO B34 LN10 IO B34 LN16 GND IO B34 LN16 GND IO B34 LN17 IO B34 LN2 GND IO B34 LN2 GND NC IO B33 LN16 IO B13 LN20 IO B13 LN16 IO B13 LN19 I	P15 P16 V17 V18 T17 R18 V16 W16 U19 N20 P20 U20 V15 W15 V20 W20 V15 W15 V20 W14 Y14 T12 U12 U12 V18 V19 W14 Y14 T12 U12 U12 V19	Bank 34 Bank 31 Bank 33	IO_L24P_T3_34 IO_L24N_T3_34 IO_L24N_T3_34 IO_L21P_T3_DQS_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L20P_T3_34 IO_L18P_T2_34 IO_L18N_T2_34 IO_L12P_T1_MRCC_34 IO_L12N_T1_MRCC_34 IO_L14N_T2_SRCC_34 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T1_34 IO_L16P_T1_34 IO_L16P_T2_34 IO_L16N_T2_34 IO_L17P_T2_34 IO_L17P_T2_34 IO_L17P_T3_34 IO_L2N_T0_34 IO_L2P_T0_34 IO_L18N_T1_31 IO_L2P_T0_34 IO_L18N_T1_31 IO_L18P_T2_I3 IO_L13P_T2_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L13P_T3_MRCC_I3 IO_L19P_T3_I3	Touchscreen Reset Touchscreen Interrupt LCD vertical Synchronization signal LCD Data line 12 LCD Data line 13 LCD Data line 6 LCD Data line 7 LCD Data line 7 LCD Data line 3 FMC