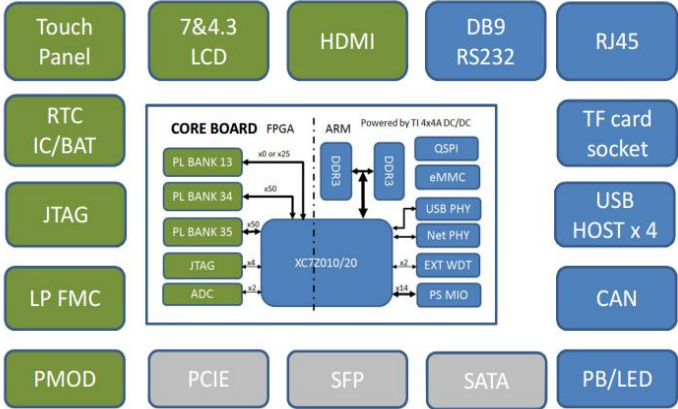


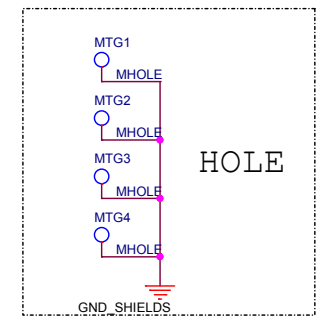
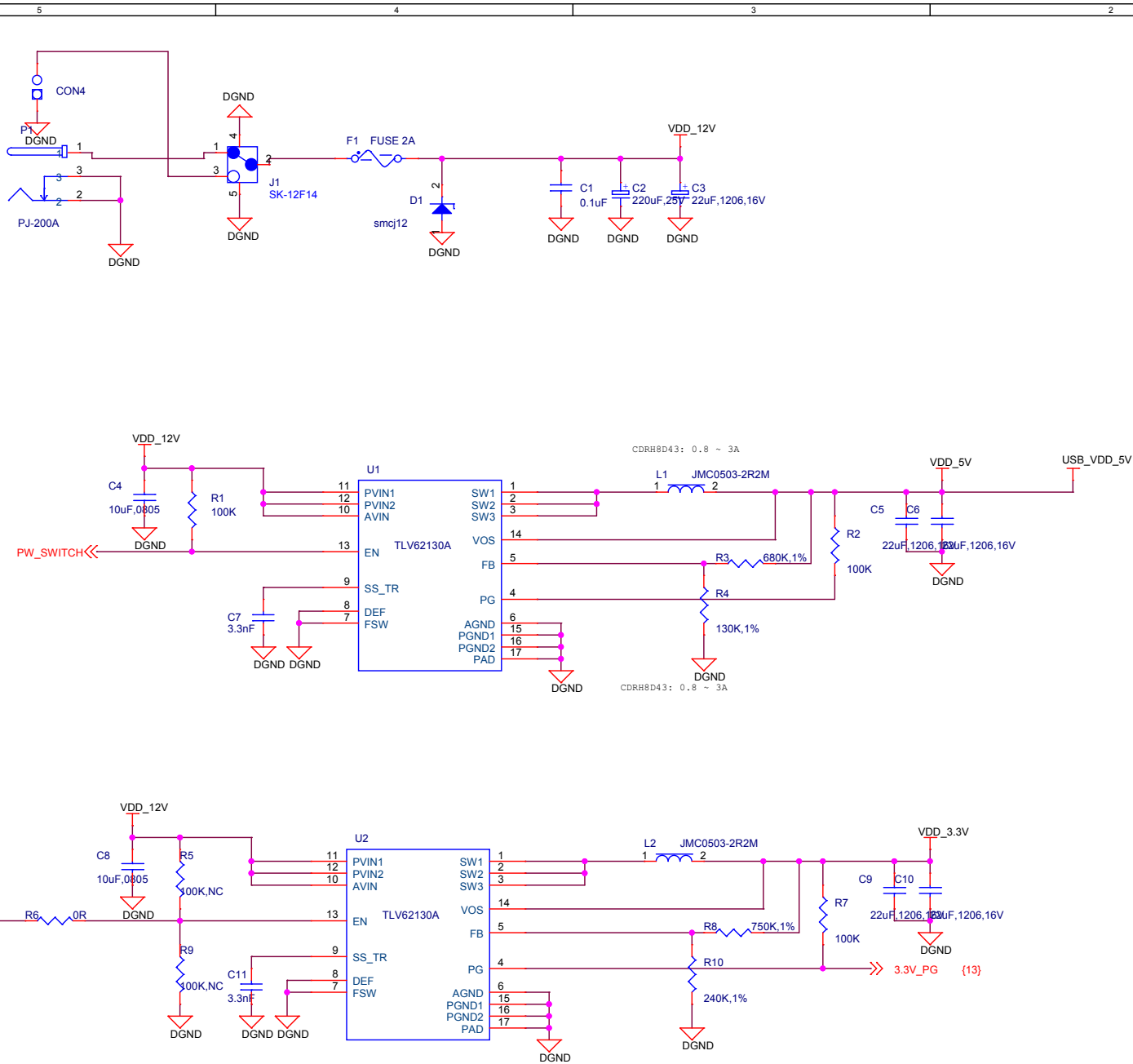
REV	Description	DATE	BY
Ver 1	Initial Release.	2015-03-25	M
Ver 1.1	Modify Jtag, SN74AUP2G08 Package; Seperate PENINQ and TPINT; ADD R185 10K;U29; Update value	2015-05-11	M

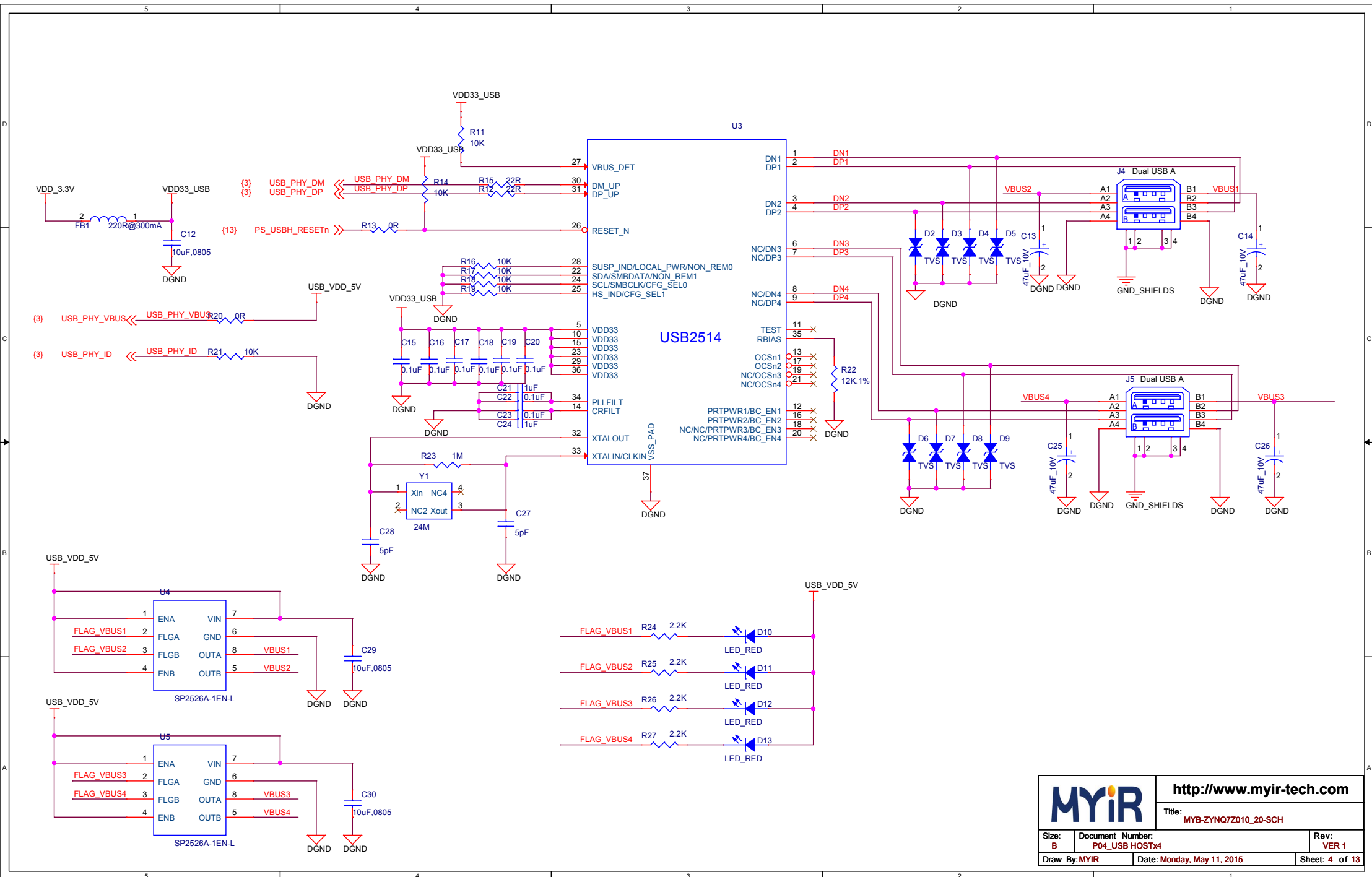
PAGE NO.	SCHEMATIC PAGE
1	TITLE PAGE
2	POWER
3	Exp Conn
4	USB HOSTx4
5	DEBUG_RS232_JTAG
6	PHY_NET1
7	HDMI
8	LCD
9	PMOD & CAN & LVDS PAIRS
10	PCIE
11	SFP & SATA
12	LP FMC
13	MISC

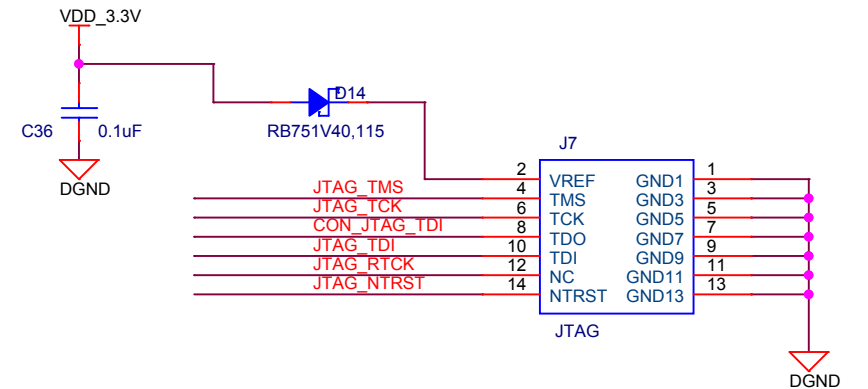
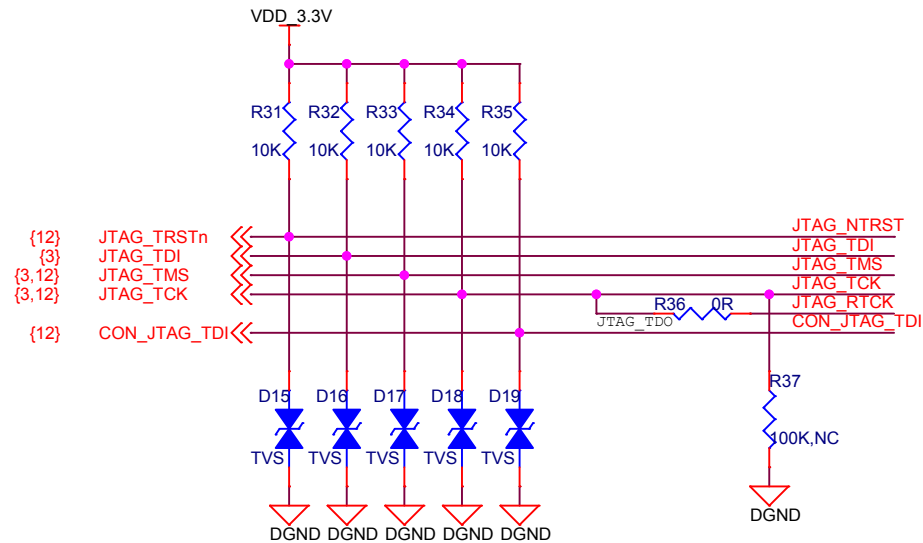
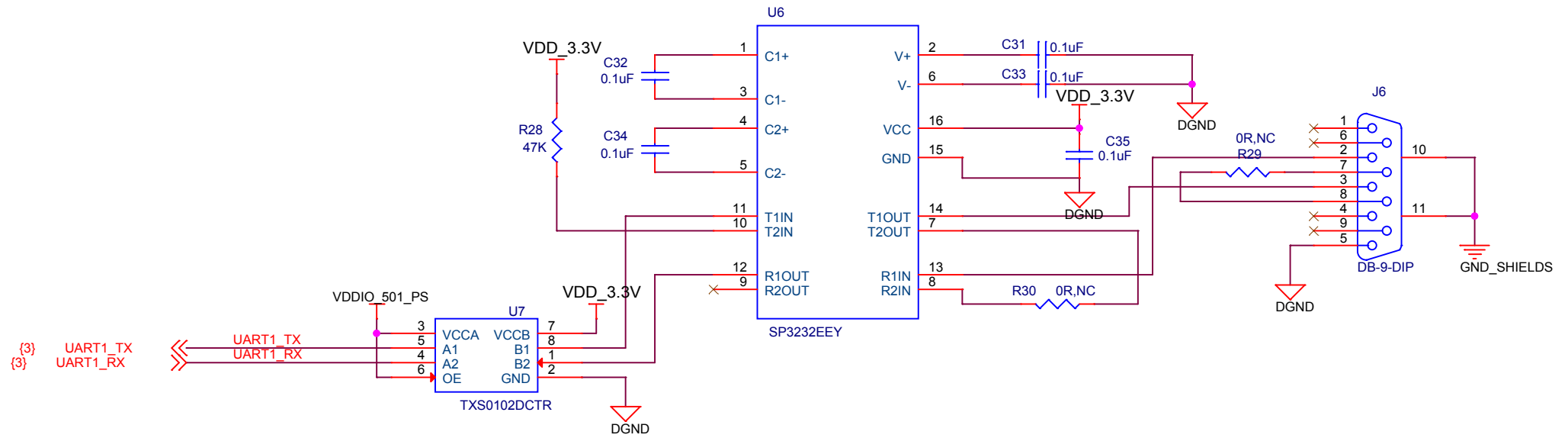
Zynq 7000 DevKit

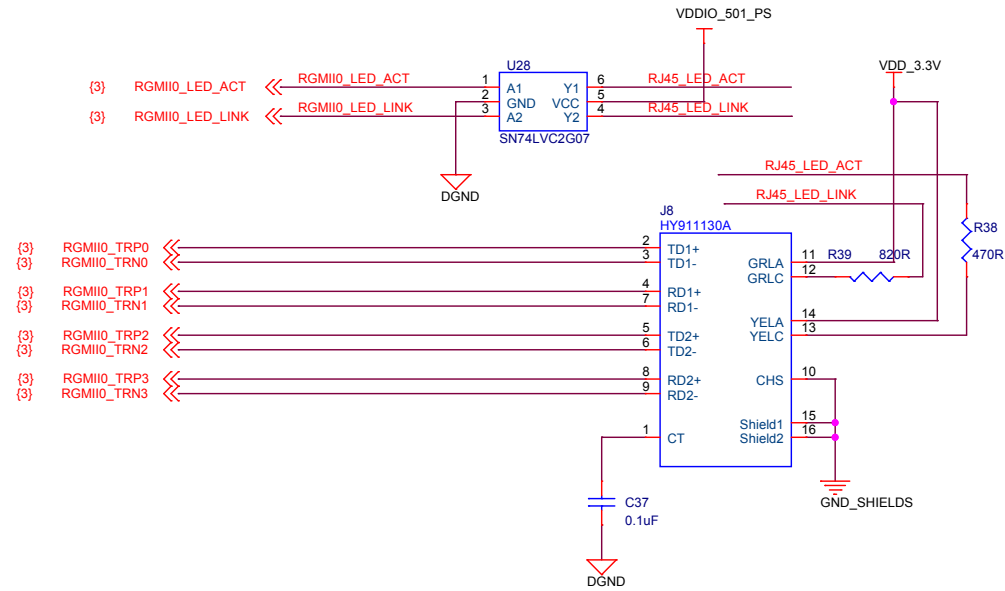
- : FPGA RESOURCE
- : ARM RESOURCE
- : ONLY FOR 7015

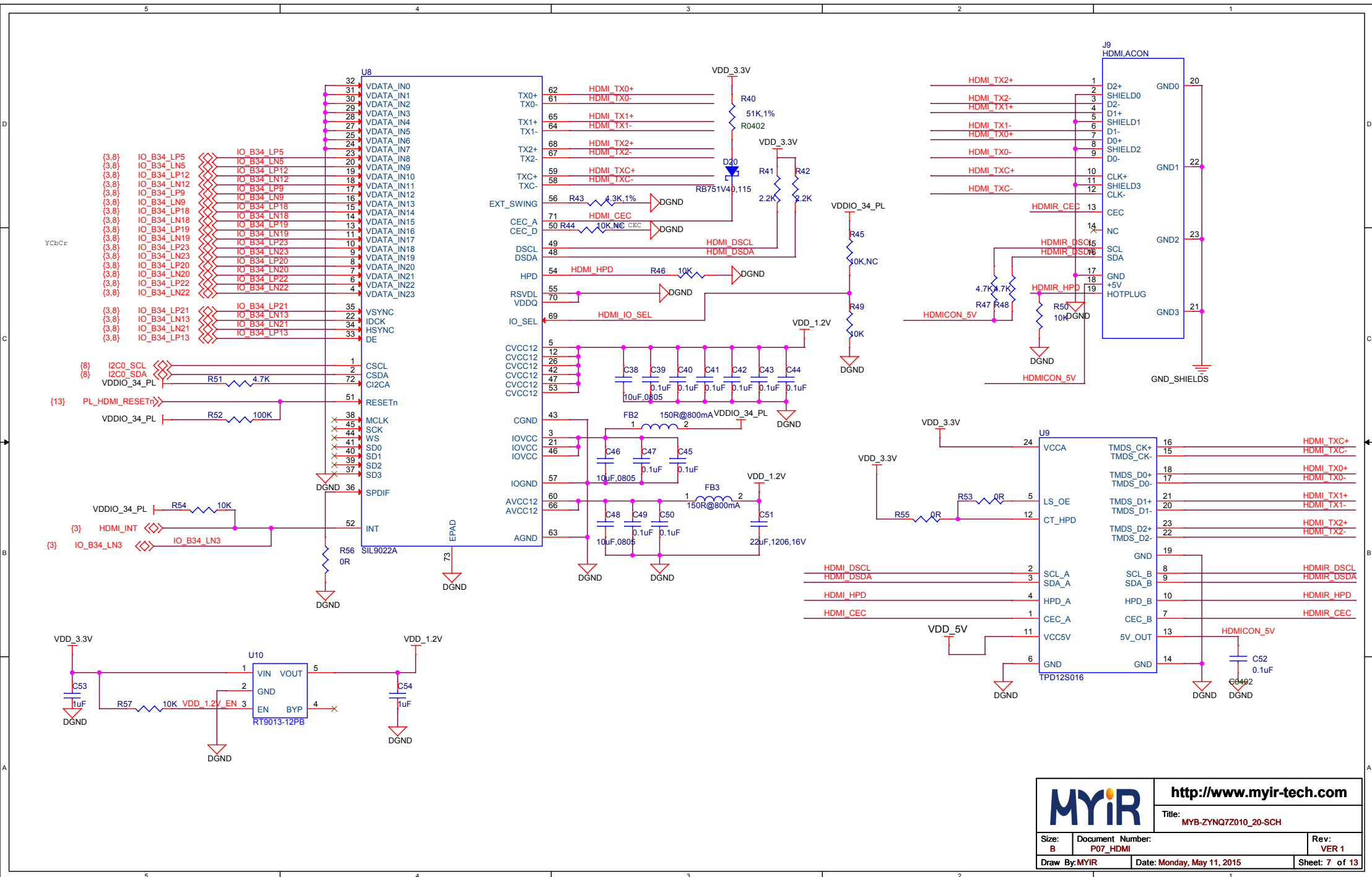






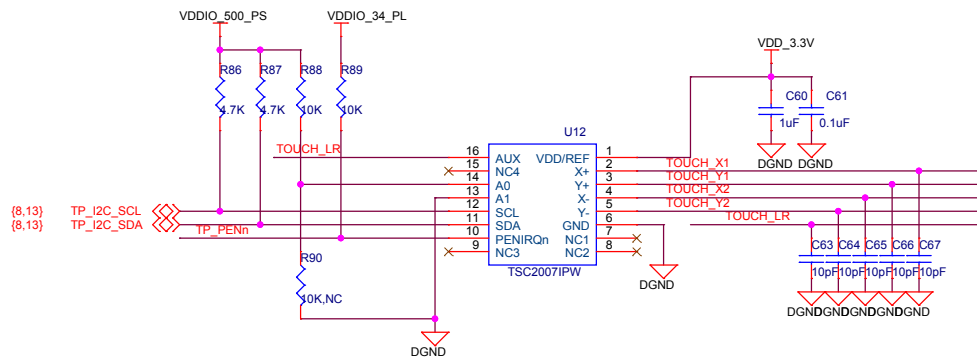
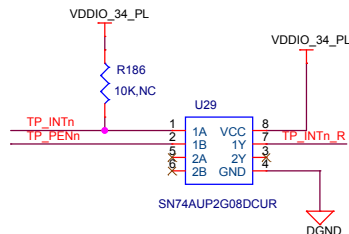
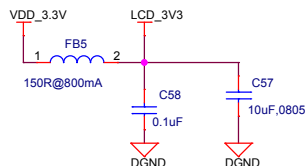
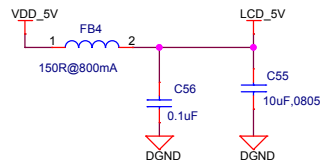






(3,7) IO_B34_LP5 IO_B34_LP5 R58 22R DVI_DATA0
 (3,7) IO_B34_LN5 IO_B34_LN5 R59 22R DVI_DATA1
 (3,7) IO_B34_LP12 IO_B34_LP12 R60 22R DVI_DATA2
 (3,7) IO_B34_LN12 IO_B34_LN12 R61 22R DVI_DATA3
 (3,7) IO_B34_LP9 IO_B34_LP9 R62 22R DVI_DATA4
 (3,7) IO_B34_LN9 IO_B34_LN9 R63 22R DVI_DATA5
 (3,7) IO_B34_LP18 IO_B34_LP18 R64 22R DVI_DATA6
 (3,7) IO_B34_LN18 IO_B34_LN18 R65 22R DVI_DATA7
 (3,7) IO_B34_LP19 IO_B34_LP19 R66 22R DVI_DATA8
 (3,7) IO_B34_LN19 IO_B34_LN19 R67 22R DVI_DATA9
 (3,7) IO_B34_LP23 IO_B34_LP23 R68 22R DVI_DATA10
 (3,7) IO_B34_LN23 IO_B34_LN23 R69 22R DVI_DATA11
 (3,7) IO_B34_LP20 IO_B34_LP20 R70 22R DVI_DATA12
 (3,7) IO_B34_LN20 IO_B34_LN20 R71 22R DVI_DATA13
 (3,7) IO_B34_LP22 IO_B34_LP22 R72 22R DVI_DATA14
 (3,7) IO_B34_LN22 IO_B34_LN22 R73 22R DVI_DATA15

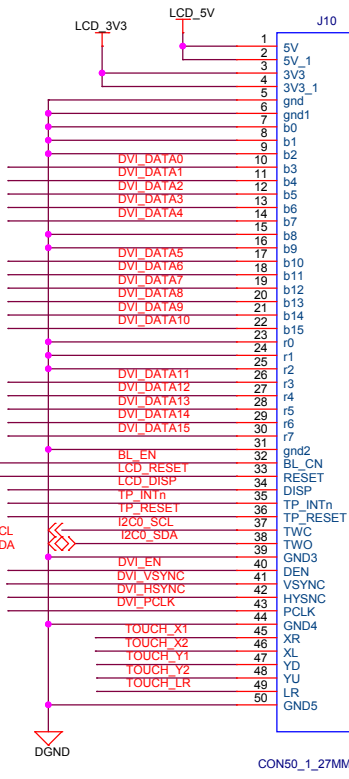
(3,7) IO_B34_LP21 IO_B34_LP21 R74 22R DVI_VSYNC
 (3,7) IO_B34_LN21 IO_B34_LN21 R75 22R DVI_HSYNC
 (3,7) IO_B34_LP13 IO_B34_LP13 R76 22R DVI_EN
 (3,7) IO_B34_LN13 IO_B34_LN13 R77 22R DVI_PCLK
 (3) IO_B34_25 IO_B34_25 R79 0R LCD_DISP
 (3) IO_B34_LP24 IO_B34_LP24 R80 0R TP_RESET
 (3) IO_B34_LN24 IO_B34_LN24 R81 0R TP_INTn_R
 (3) IO_B34_0 IO_B34_0 R82 0R BL_EN
 (3) IO_B34_0 IO_B34_0 R83 10K,NC



BLUE

GREEN

RED



BLUE

GREEN

RED

