

DSP

Year Question Solve: 14, 15, 16, 18



2014

1.a) Define digital signal processing (DSP). [1]

- Digital Signal Processing (DSP) : Digital Signal Processing (DSP) is the process of analyzing and modifying a signal to optimize or improve its efficiency or performance. It involves applying various mathematical and computational algorithms to analog and digital signals to produce a signal that's of higher quality than the original signal.

1.b) Write the advantages and limitation of DSP. [3.75]

- Advantages of DSP:

Digital signal processing processes several advantages over analog signal processing.

They are -

- Greater accuracy: The tolerance of the circuit components used to design the

analog filters affects the accuracy, whereas the DSP provides superior control of accuracy.

- Cheaper: In many applications, digital realization is comparatively cheaper than its analog counterpart.
- Ease of Data Storage: Digital signals can be easily stored on magnetic media without loss of fidelity and can be processed off-line in a remote laboratory.
- Implementation of sophisticated algorithm: The DSP allows to implement sophisticated algorithms when compared to its analog counterpart.
- Flexibility in configuration: A DSP system can be easily reconfigured by changing the program. Reconfiguration of an analog system involves redesign of system hardware.

• Applicability of VLF signals: The very low frequency signals such as those occurring in seismic application can be easily processed using a digital signal processor when compared to an analog processing system where inductors and capacitors needed would be physically very large in size.

• Time sharing: DSP allows the sharing of a given processor among a number of signals by time sharing thus reducing the cost of processing a signal.

Limitations of DSP:

• System Complexity: System complexity increases in the digital processing of an analog signal because of devices such as A/D and D/A converters and their associated filters.

• Bandwidth limited by sampling rate: Band limited signals can be sampled without information loss if the sampling rate is

more than twice the bandwidth. Therefore signals having extremely wide bandwidths require fast sampling rate A/D converters and fast digital signal processors. But there is practical limitations in the speed of operation of A/D converters and digital signal processors.

- Power Consumption: A variety of analog processing algorithms can be implemented using passive circuit elements like inductors, capacitors and resistors that do not need much power, whereas a DSP chip containing over 4 lakh transistors dissipates more power (1 watt).

1(c) Explain unit ramp sequence and exponential sequence of discrete-time signal. [4]

Unit ramp sequence: The unit ramp sequence is defined as -

$$r(n) = \begin{cases} n & \text{for } n \geq 0 \\ 0 & \text{for } n < 0 \end{cases}$$

The graphical representation of $r(n)$ is shown in Fig-1.

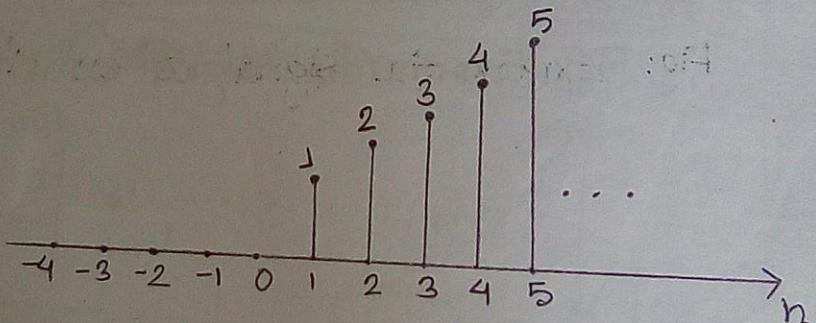


Fig-1: Unit ramp Sequence .

Exponential sequence: The exponential signal is a sequence of the form -

$$x(n) = a^n \text{ for all } n$$

When the value of $a > 1$, the sequence

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grows exponentially and when the value is $a > 1$, the sequence decreases exponentially. Note also that when $a < 0$, the discrete time exponential signal takes alternating signs.

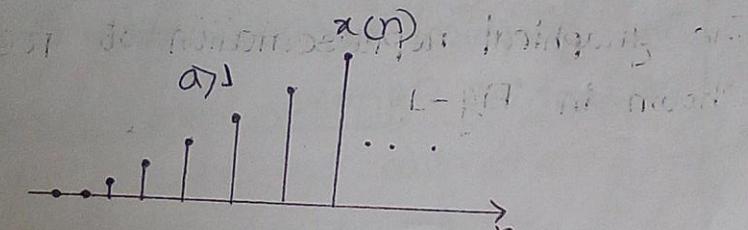


Fig: Exponential signal when $a > 1$.

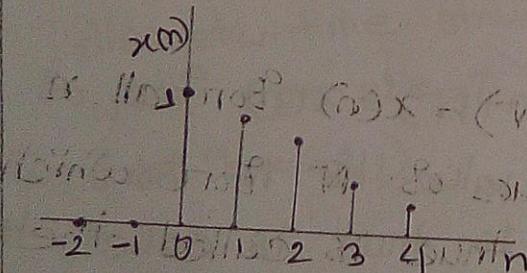
2.a) Distinguish between discrete-time signals and discrete-valued signals. [7.5]

Discrete-time signals

Discrete-time signals are defined only at certain specific values of time.

These time instants need not be equidistant.

Graphical representation:

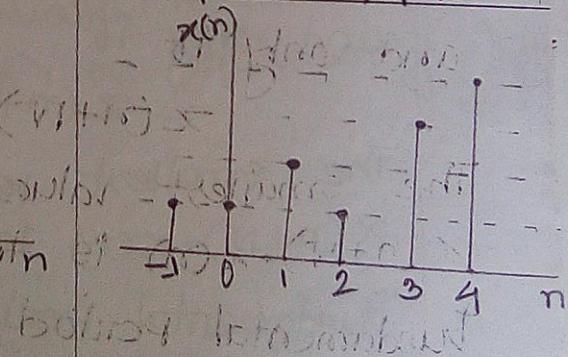


Discrete-valued signals

If a signal takes on values from a finite set of possible values it is said to be a discrete-valued signal.

These values are equidistant.

Graphical representation:



2-b) Discuss the properties by which the discrete-time sinusoids can be characterized. [3]

The properties by which the discrete-time sinusoids can be characterized given below

1. A discrete-time sinusoid is periodic only if its frequency ω is a rational number. By definition, a discrete-time signal $x(n)$ is periodic with period N ($N > 0$) if and only if

$$x(n+N) = x(n) \text{ for all } n.$$

The smallest value of N for which $x(n+N) = x(n)$ is true is called the fundamental period.

2. Discrete-time sinusoids whose frequencies are separated by an integer multiple of 2π are identical.

To prove this assertion, let us consider the sinusoid $\cos(\omega_0 n + \theta)$. It easily follows that

$$\cos[(\omega_0 + 2\pi)n + \theta] = \cos(\omega_0 n + 2\pi n + \theta) = \cos(\omega_0 n + \theta)$$

As a result, all sinusoidal sequences

$$x_k(n) = A \cos(\omega_k n + \theta) \quad \text{where } k = 0, 1, 2, \dots, 4$$

where, $\omega_k = \omega_0 + 2k\pi$, $-\pi \leq \omega_0 \leq \pi$
are identical.

3. The highest rate of oscillation in a discrete-time sinusoid is attained when $\omega = \pi$ (or $\omega = -\pi$) or equivalently, $\beta = 1$.

$$(\text{or } \beta = -1/2)$$

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2. (a) Discuss the sampling process of analog signals [3]

— There are many ways to sample an analog signal, we limit our discussion to periodic or uniform sampling, which is the type of sampling used most often in practice. This is described by the relation

$$x(n) = x_a(nT), \quad n \in \mathbb{Z}$$

Periodic Sampling establishes a relationship between the time variables t and n of continuous-time and discrete-time signals, respectively. Indeed these variables are linearly related through the sampling period T or, equivalently, through the sampling rate

$$F_s = 1/T \text{ as}$$

$$t = nT = \frac{n}{F_s}$$

As a consequence of $t = nT = \frac{n}{F_s}$,

there exists a relationship between the frequency variable F (or ω) b/w analog signals and the frequency variable f_s (or w) b/w discrete-time signals. To establish this relationship, consider an analog sinusoidal signal of the form

$$x_a(t) = A \cos(2\pi F t + \theta)$$

which, when sampled periodically at a rate $F_s = 1/T$ samples per second, yield

$$\begin{aligned} x_a(nT) &\equiv x(n) = A \cos(2\pi F n T + \theta) \\ &= A \cos\left(\frac{2\pi n F}{F_s} + \theta\right) \end{aligned}$$

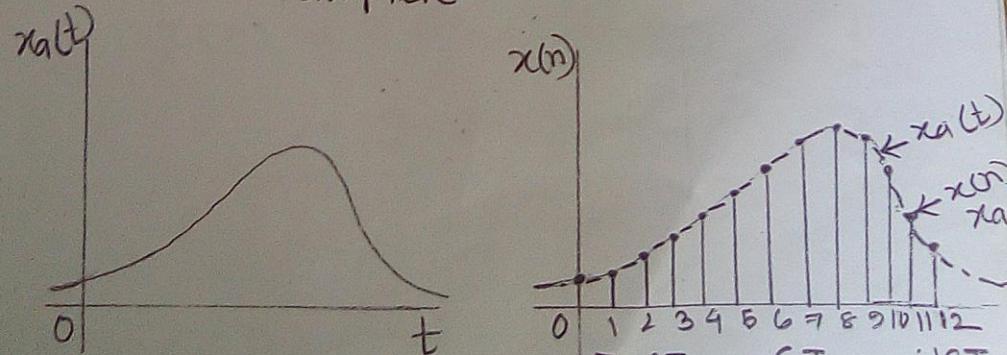
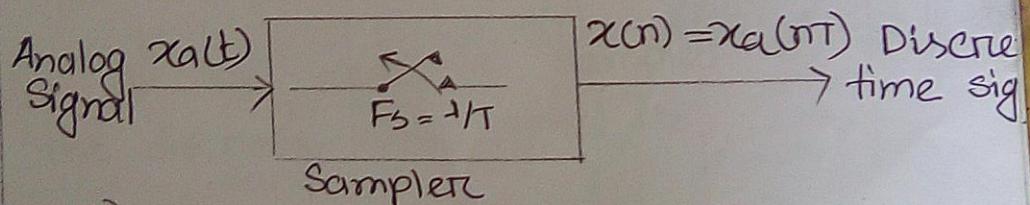


Fig: Periodic sampling
an analog signal

there exists a relationship between the frequency variable F (rad/s) and signals and the frequency variable ω (rad discrete-time signals. To establish this relationship, consider an analog sinusoidal signal of the form

$$x_a(t) = A \cos(2\pi F t + \theta)$$

which, when sampled periodically at a rate $F_s = 1/T$ samples per second, yield

$$\begin{aligned} x_a(nT) &\equiv x(n) = A \cos(2\pi F_n T + \theta) \\ &= A \cos\left(\frac{2\pi n F}{F_s} + \theta\right) \end{aligned}$$

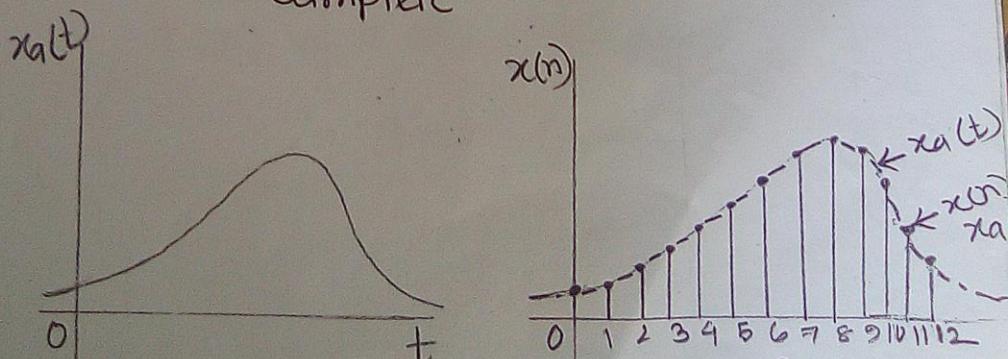
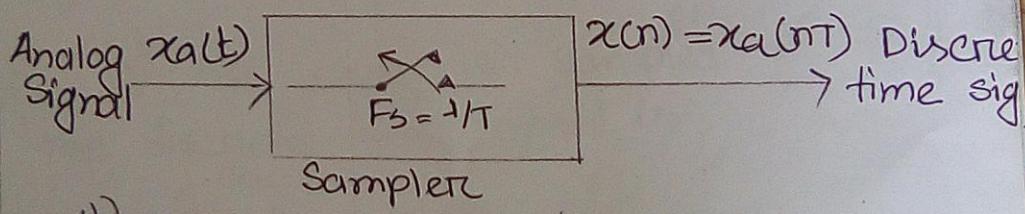


Fig: Periodic sampling of an analog signal

2.d) Given the two analog sinusoidal signals
 $x_1(t) = \cos 2\pi(10)t$ and $x_2(t) = \cos 2\pi(50)t$
are sampled at a rate of $F_s = 40$ Hz.

is there occurrence of any frequency aliasing? []

- Yes there is occur of frequency aliasing. The frequency $F_2 = 50$ Hz is an alias of the frequency $F_1 = 10$ Hz at the sampling rate of 40 samples per second.

$$\text{aliasing} (10) = 50$$

$$40 \text{ samples}$$

3. a) Consider the analog signal $x_a(t) = 3 \cos 10t$

i) Determine the minimum sampling rate required to avoid aliasing.

ii) Suppose that the signal is sampled at the rate $F_s = 200\text{Hz}$. What is the discrete-time signal obtained after sampling? [1]

— i) The frequency of the analog signal is $F = 50\text{Hz}$. Hence the minimum sampling rate required to avoid aliasing is $F_s = 100\text{Hz}$.

ii) If the signal is sampled at $F_s = 200\text{Hz}$, the discrete-time signal is,

$$x(n) = 3 \cos \frac{100\pi}{200} n$$

$$= 3 \cos \frac{\pi}{2} n$$

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3-b) Suppose two discrete signals are represented by the following two finite duration sequences.

$$x_1 = \{ 3, -1, -2, \underset{\uparrow}{5}, 0, 4, -1 \}$$

$$x_2 = \{ \underset{\uparrow}{3}, -1, -2, 5, 0, 4, -1 \}$$

Are these two signals different or not?
Explain. [1.5]

- Hence,

$$x_1 = \{ 3, -1, -2, \underset{\uparrow}{5}, 0, 4, -1 \}$$

$$x_2 = \{ \underset{\uparrow}{3}, -1, -2, 5, 0, 4, -1 \}$$

These two signals are different. Because sequence with the time origin ($n=0$) indicated by the symbol \uparrow . The x_1 and x_2 signals have different time origin ($n=0$) indicated by the symbol \uparrow .

The graphical representation of x_1 and x_2 are,

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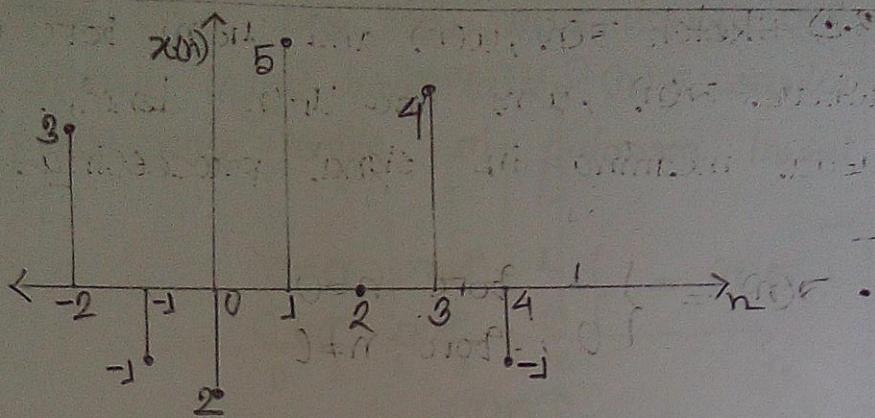


Fig-1: Graphical representation of x_1 .

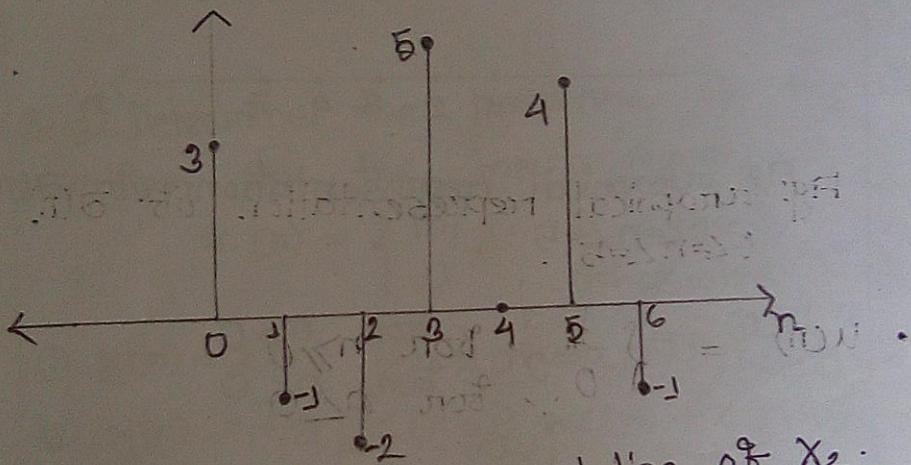


Fig-2: Graphical representation of x_2 .

From Fig-1 and Fig-2 we can clearly say that the x_1 and x_2 signals are different.

3.9) Sketch $\delta(n)$, $u(n)$ and $u_r(n)$ for $0 \leq n \leq 5$
 where $\delta(n)$, $u(n)$ and $u_r(n)$ denote their
 usual meaning in signal processing. [1.5]

$$\cdot \delta(n) = \begin{cases} 1 & \text{for } n=0 \\ 0 & \text{for } n \neq 0 \end{cases}$$

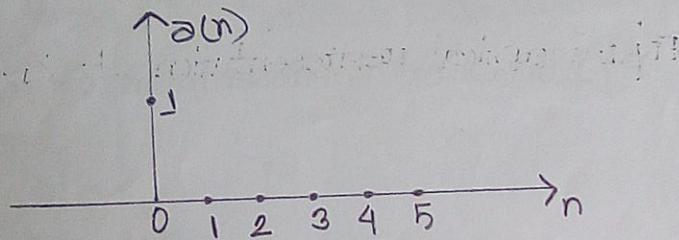


Fig: Graphical representation of $\delta(n)$ for $0 \leq n \leq 5$.

$$\cdot u(n) = \begin{cases} 1 & \text{for } n \geq 0 \\ 0 & \text{for } n < 0 \end{cases}$$

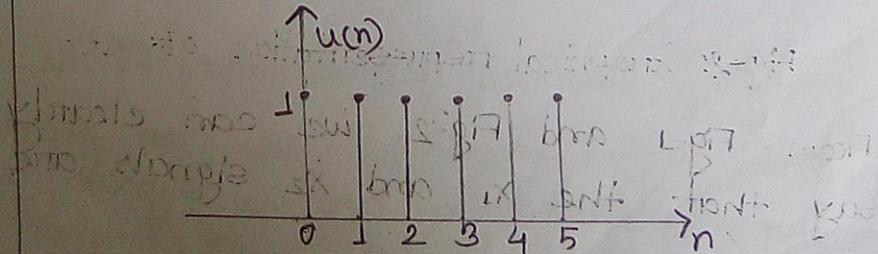


Fig: Graphical representation of $u(n)$ for $0 \leq n \leq 5$.

$$u_r(n) = \begin{cases} n, & \text{for } n \geq 0 \\ 0, & \text{for } n < 0 \end{cases}$$

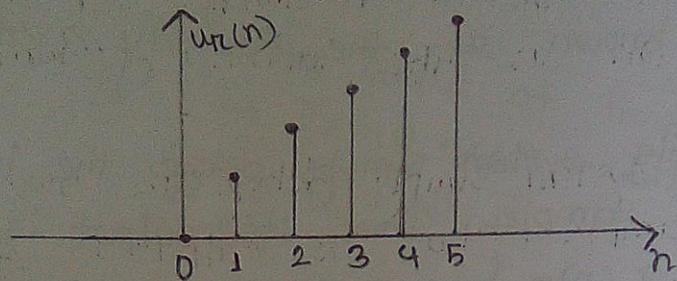


Fig: Graphical representation of $u_r(n)$ for $0 \leq n \leq 5$.

3.d) Determine the response of the following systems to the input signal:

$$x(n) = \begin{cases} 1, & -3 \leq n \leq 3 \\ 0, & \text{otherwise} \end{cases}$$

(i) $y(n) = x(n)$ (ii) $y(n) = x(n-1)$ (iii) $y(n) = x(n)$
 (iv) $y(n) = 1/3 [x(n+1) + x(n) + x(n-1)]$ [4]

- First, we determine explicitly the sample values of the input signal.

$$x(n) = \{-\dots, 0, 3, 2, 1, 0, -1, 2, 3, 0, -\dots\}$$

Next we determine the output of each system using its input-output relationship.

(I) In this case the output is exactly the same as the input signal. Such a system is known as the identity system.

(II) This system simply delays the input by one sample. Thus its output is given by

$$x(n) = \{ \dots, 0, 3, 2, \underset{\uparrow}{1}, 0, -2, 3, 0, \dots \}$$

(III) In this case the system "advances" the input one sample into the future. For example, the values of the output at time $n=0$ is $y(0)=x(1)$. The response of this system to the given input is

$$x(n) = \{ \dots, 0, 3, 2, \underset{\uparrow}{1}, 0, \underset{\uparrow}{-1}, 2, 3, 0, \dots \}$$

(IV) The output of this system at any time is the mean value of the present, the immediate past and the immediate future samples.

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For example, the output at time $n=0$ is

$$\begin{aligned}y(0) &= \frac{1}{3} [x(-1) + x(0) + x(1)] \\&= \frac{1}{3} [-1 + 0 + 1] \\&= \frac{2}{3}\end{aligned}$$

Repeating this computation for every value n , we obtain the output signal

$$y(n) = \left\{ \dots, 0, 1, \frac{5}{3}, 2, 1, \frac{2}{3}, -1, 2, \frac{5}{3} \right. \quad \uparrow$$

ID-1637820107

2016/2017 Academic Year

Q.1) Give a brief introduction about the different parts of a digital signal processing. [2.75]

- Block diagram of a digital signal processing system:

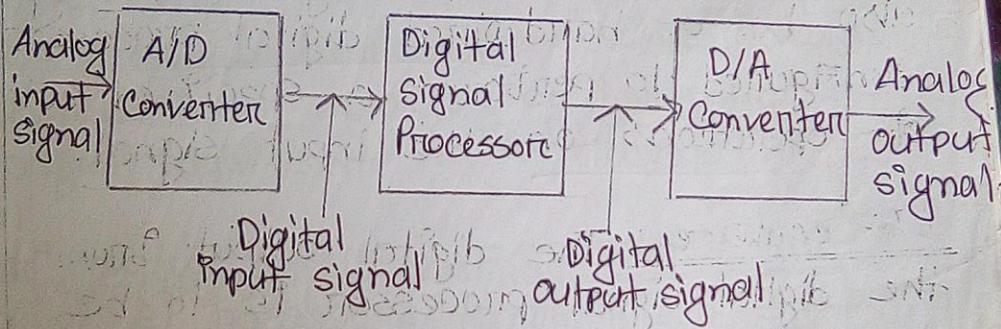


Fig: Digital signal processing system.

A/D Converter: To perform the processing digitally, there is a need for an interface between the analog signal and the digital signal processor. This interface is called an analog-to-digital (A/D) converter. The output

of the A/D converter is a digital signal that is appropriate as an input of the digital processor.

Digital Signal Processor: The digital signal processor may be a large programmable digital computer or a small microprocessor programmed to perform the desired operations on the input signal. It may also be a hardwired digital processor configured to perform a specified set of operations on the input signal.

D/A Converter: The digital output from the digital signal processor is to be given to the user in analog form, such as in speech communications, we must provide another interface from the digital domain to the analog domain such an interface is called a digital to analog (D/A) converter.

1. Q) Consider the following input-output equation of signal :

$$(I) y(n) = x(n) + \frac{1}{x(n-1)}, \quad (II) y(n) = nx(n)$$

Determine whether the system is linear or non-linear. [3]

① Law of Additivity

$$x_1(n) \rightarrow \text{sys.} \rightarrow y_1(n) = x_1(n) + \frac{1}{x_1(n-1)}$$

$$x_2(n) \rightarrow \text{sys.} \rightarrow y_2(n) = x_2(n) + \frac{1}{x_2(n-1)}$$

$$y_1(n) + y_2(n) = x_1(n) + x_2(n) + \frac{1}{x_1(n-1)} + \frac{1}{x_2(n-1)}$$

$$x_1(n) + x_2(n) \rightarrow \text{sys.} \rightarrow y'(n) = x_1(n) + \frac{1}{x_1(n-1)} + \frac{1}{x_2(n-1)} \dots - (II)$$

Eqn ① and eqn ② are not equal and the superposition principle is not satisfied.

So, the system is non-linear.

2.9) What is
with an ex
antialiasin

⑪ Law of Additivity:

$$x_1(n) \rightarrow \text{sys.} \rightarrow y_1(n) = n \cdot x_1(n)$$

$$x_2(n) \rightarrow \text{sys.} \rightarrow y_2(n) = n \cdot x_2(n)$$

$$y_1(n) + y_2(n) = n [x_1(n) + x_2(n)] \quad \text{--- } ⑪$$

$$x_1(n) + x_2(n) \rightarrow \text{sys.} \rightarrow y'(n) = n [x_1(n) + x_2(n)]$$

Eqn ⑪ and eqn ⑩ are equal and the superposition principle is satisfied.

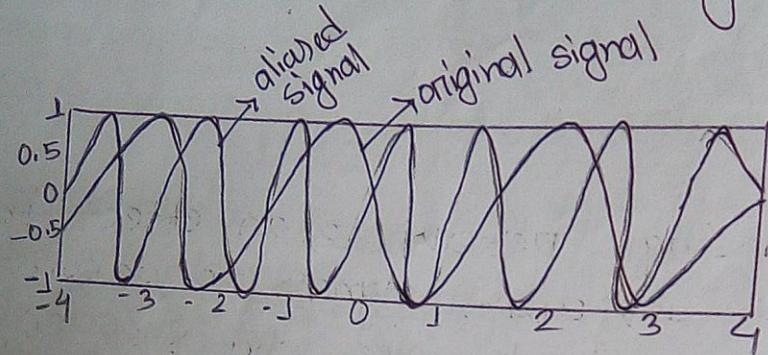
Hence the system is linear.

2.9) What is aliasing effect? Discuss it with an example. Why do you need antialiasing filter in signal processing?

- Aliasing refers to an effect that causes different signals to become indistinguishable (or aliases of one another) when sampled. Aliasing occurs when a system is measured at an insufficient sampling rate.

It also refers to the distortion or artifacts those results when the signal reconstructed from samples is different from the original continuous signal. Aliasing can occur in signals sampled in time, for instance digital audio is referred to as temporal aliasing. Aliasing can also occur in spatially sampled signals for instance digital images. Aliasing in spatially

Signals is called spatial aliasing.



To avoid aliasing we can choose very high sampling frequency. But sampling at very high frequencies introduces numerical errors.

Therefore, to avoid aliasing errors caused by the undesired high frequency signals, analog lowpass filter, called anti-aliasing filter is used prior to sampler to filter high frequency components before the signal is sampled.

Q.6) Define up sampling and down sampling with example. Why do you need to change the sampling rate of a signal? [3]

2.C) Prove that the highest rate of oscillation in a discrete sinusoidal is attained when $\omega = \pi$ or $\omega = -\pi$. [2.75]

To illustrate this property, let us investigate the characteristics of the sinusoidal signal sequence, $x(n) = \cos \omega n$. When the frequency varies from 0 to π . To simplify the argument, we take values of $\omega_0 = 0, \pi/8, \pi/4, \pi/2, \pi$ corresponding to $n = 0, 1/16, 1/8, 1/4, 1/2$, which result in periodic sequences showing periods $N = \infty, 16, 8, 4, 2$. We note that the period of the sinusoid decreases as the frequency increases. In fact, we can see that the rate of oscillation increases as the frequency increases.

To see what happens for $\pi \leq \omega \leq 2\pi$, we consider the sinusoids with frequencies $\omega_1 = \omega_0$ and $\omega_2 = 2\pi - \omega_0$. Note that as ω

varies from π to 2π , ω_2 varies from π
0. It can be easily seen that

$$x_1(n) = A \cos \omega_1 n = A \cos \omega_2 n$$

$$\begin{aligned}x_2(n) &= A \cos \omega_2 n = A \cos(2\pi - \omega_2)n \\&= A \cos(-\omega_2 n) = x_1(n)\end{aligned}$$

Hence ω_2 is an alias of ω_1 . If we had used a sine function instead of cosine function, the result would basically be the same, except for a 180° phase difference between sinusoids $x_1(n)$ and $x_2(n)$. In a case, as we increase the relative frequency ω_0 of a discrete-time sine from π to 2π , its rate of oscillation decreases. For $\omega_0 = 2\pi$ the result is

a constant signal, as in the case for

$\omega_0 = 0$. Obviously, for $\omega_0 = \pi$ (or

$\theta = 45^\circ$) or $\omega_0 = -\pi$ (or $\theta = -45^\circ$)

we have the highest rate of oscillation.

3. a) Describe stable and unstable system with example. [3]

- Stable system: The system is said to be stable only when the output is bounded for bounded input at each and every instant of time.

For example -

$$y(t) = x^2(t)$$

Let the input is $u(t)$ (unit step bound input) then the output $|y(t)| = u^2(t) =$
= bounded output

Hence, the system is stable.

Unstable system: The system is said to be unstable when the output is unbounded for bounded input at each and every instant of time.

For example -

$$y(t) = \int x(t) dt$$

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Let the input is $u(t)$ (unit step bound input) then the output $y(t) = \int u(t)dt$
= ramp signal (unbounded because amplitude of ramp is not finite it goes to infinite when $t \rightarrow \text{infinite}$)
Hence, the system is unstable.

3-b) Explain convolution sum of signals.

- Convolution is a mathematical operation used to express the relation between input and output of an LTI system. It relates input, output and impulse response of LTI system as

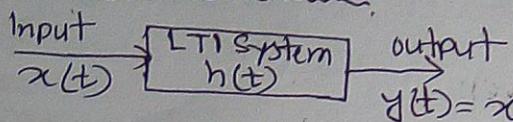
$$y(n) = x(n) * h(n)$$

Where, $y(n)$ = output of LTI
 $x(n)$ = input of LTI
 $h(n)$ = impulse response of LTI
 $*$ = convolution operation

There are two types of convolutions:

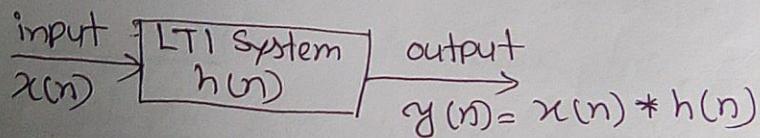
- Continuous convolution.
- Discrete convolution.

- Continuous Convolution -



$$\begin{aligned}
 y(t) &= x(t) * h(t) \\
 &= \int_{-\infty}^{\infty} x(\tau) h(t-\tau) d\tau \\
 (\text{or}) \quad &= \int_{-\infty}^{\infty} x(t-\tau) h(\tau) d\tau
 \end{aligned}$$

- Discrete Convolution -



$$\begin{aligned}
 y(n) &= x(n) * h(n) \\
 &= \sum_{k=-\infty}^{\infty} x(k) h(n-k) \\
 (\text{or}) \quad &= \sum_{k=-\infty}^{\infty} x(n-k) h(k)
 \end{aligned}$$

3. (c) Consider the input $x[n] = \{1, 0, 2, 3\}$ of a LTI system and impulse response $h[n] = \{1, 2, 1, 3\}$. Find out the convolution sum of output $y[n]$ of the LTI system.

- Here given,

$$x[n] = \{1, 0, 2, 3\}$$

$$h[n] = \{1, 2, 1, 3\}$$

| $x(n)$ | 1 | 0 | 2 | 3 |
|------------------------------|---|---|---|---|
| $h(n)$ | 1 | 0 | 2 | 3 |
| 1 | 2 | 0 | 4 | 6 |
| 2 | 1 | 0 | 2 | 3 |
| 3 | 3 | 0 | 6 | 9 |

The convolution sum,

$$y(n) = \{1, 2, 3, 10, 8, 9, 9\}$$

2015

1.a) Same as 2014 J(a, b)

1.b) Same as 2014 J(c)

1.c) Draw the block diagram of the following discrete-time system.

$$y[n] = 0.5x[n-3] + 0.25x[n-2] + 0.5x[n-4] + 0.5y[n-2]$$

where $x(n)$ is the input and $y(n)$ is the output of the system.

Block diagram realization of the system:

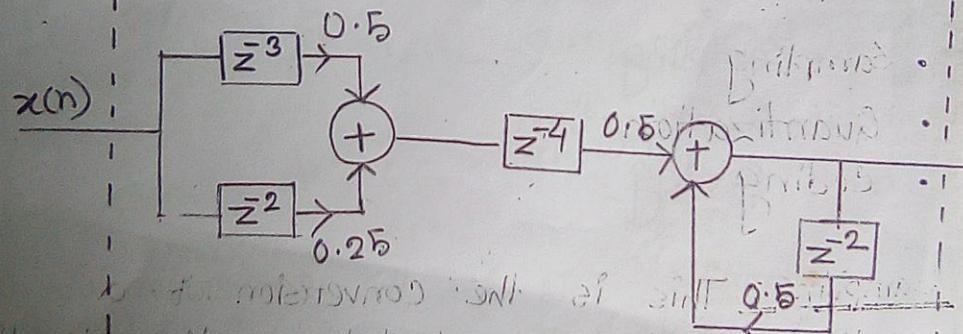


Fig: Block diagram realizations of the system
 $y[n] = 0.5x[n-3] + 0.25x[n-2] + 0.5x[n-4] + 0.5y[n-2]$

2. a) Describe analog signal to digital signal (A/D) conversion process in brief with figure. [5]

- To process analog signals by digital means it is first necessary to convert them into digital form; that is to convert them to a sequence of numbers having finite precision. This process is called analog-to digital (A/D) conversion. A/D conversion has three steps:

- Sampling
- Quantization
- Coding.

Sampling: This is the conversion of a continuous-time signal into a discrete-time signal. It is called samples. Thus if $x_a(t)$ is the input to the sampler, the output is $x_a(nT) = x(n)$, where T is the sampling interval.

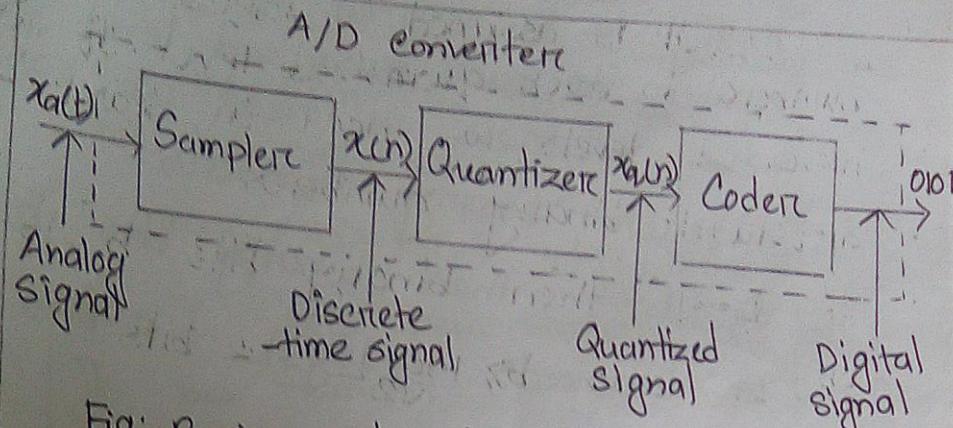


Fig: Basic parts of an analog -to digital (A/D) converter.

Quantization: Quantization is the process of converting a continuous range of values into a finite range of discrete values.

We denote the quantization operation on the samples $x(n)$ as $Q[x(n)]$ and let $x_q(n)$ denote the sequence of quantized samples at the output of the quantizer. Hence

$$x_q(n) = Q[x(n)]$$

Quantization Error, $e_q(n) = x_q(n) - x(n)$

2. c) If the
 $L = 128$, then
will be needed
- Hence, bits

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Coding: It is assigned a unique binary number to each quantized level. If we have L levels we need at least L different binary numbers.

If $L=4$, then binary bits b_1 is

$$2^b \geq L \text{ needs } b \geq \log_2 L = 2 \text{ bits.}$$

2.b) Consider the analog signal $x_a(t) =$

$$10 \sin 400\pi t + 25 \cos 550\pi t - 15 \cos 450\pi t$$

Determine the sampling rate to avoid aliasing and maximum magnitude of the signal. [2]

$$\text{Sampling rate} = 500\pi \text{ rad/sec}$$

$$f_s = 500\pi \text{ Hz} = 1570 \text{ Hz}$$

Max. frequency = 550 Hz

v

2.c) If the number of quantization levels
 $L = 128$, then how many bits per sample
will be required? [1.75]

- Here, $b \geq \log_2 L = \log_2 128 = 7$
So 7 bits per sample will be required
if the number of quantization level
 $L = 128$.

3.a) Describe cross-correlation function.
Write the application of cross-correlation [3.75]

- The cross-correlation between a pair of signals $x(n)$ and $y(n)$ is given by

$$R_{xy}(l) = \sum_{n=-\infty}^{\infty} x(n)y(n-l) \quad l = 0, \pm 1, \pm 2, \dots$$

The index l is the shift (lag) parameter.
The order of subscripts xy indicate
that $x(n)$ is the reference sequence
that remains unshifted in time

whereas the sequence, $y(n)$ is shifted ℓ units in time with respect to $x(n)$.

If we wish to fix $y(n)$ and to shift $x(n)$, then correlation of two sequences can be written as

$$R_{yx}(\ell) = \sum_{n=-\infty}^{\infty} y(n) x(n-\ell)$$

$$= \sum_{n=-\infty}^{\infty} y(n+\ell) x(n) \quad \text{--- (1)}$$

Comparing with eqn (1), we get,

$$R_{yx}(0) = R_{yx}(\ell) = \sum_{n=-\infty}^{\infty} x(n) y(n) -$$

Comparing eqn (1) and with eqn (1) we get,

$$R_{xy}(\ell) = R_{yx}(-\ell)$$

where $R_{xy}(-\ell)$ is the folded version of R_{xy} about $\ell=0$.

We can rewrite eqn (1) as,

$$\begin{aligned} R_{xy}(\ell) &= \sum_{n=-\infty}^{\infty} x(n) y[-(l-n)] \\ &= x(l) * y(-l) \end{aligned}$$

Applications of cross-correlation:

- Measuring fast signal decay
- Laser microscopy
- Measuring pulse broadening and distortion
- Fluorescence spectroscopy
- Measuring musical beats (estimating pitch)
- Scanning pulsar frequencies
- Analyzing spatial patterns
- Used in X-ray diffraction data analysis
- Security system design (pattern recognition utilized)
- Water traffic monitoring.

3.b) Determine the cross-correlation sequence $r_{xy}(l)$ of the sequences

$$x(n) = \{ \dots, 0, 0, 2, -1, 3, 4, \underset{\uparrow}{1}, 2, -3, 0, 0, \dots \}$$

$$y(n) = \{ \dots, 0, 0, 1, -1, 2, -2, 4, \underset{\uparrow}{1}, -2, 1, 0, 0, \dots \}$$

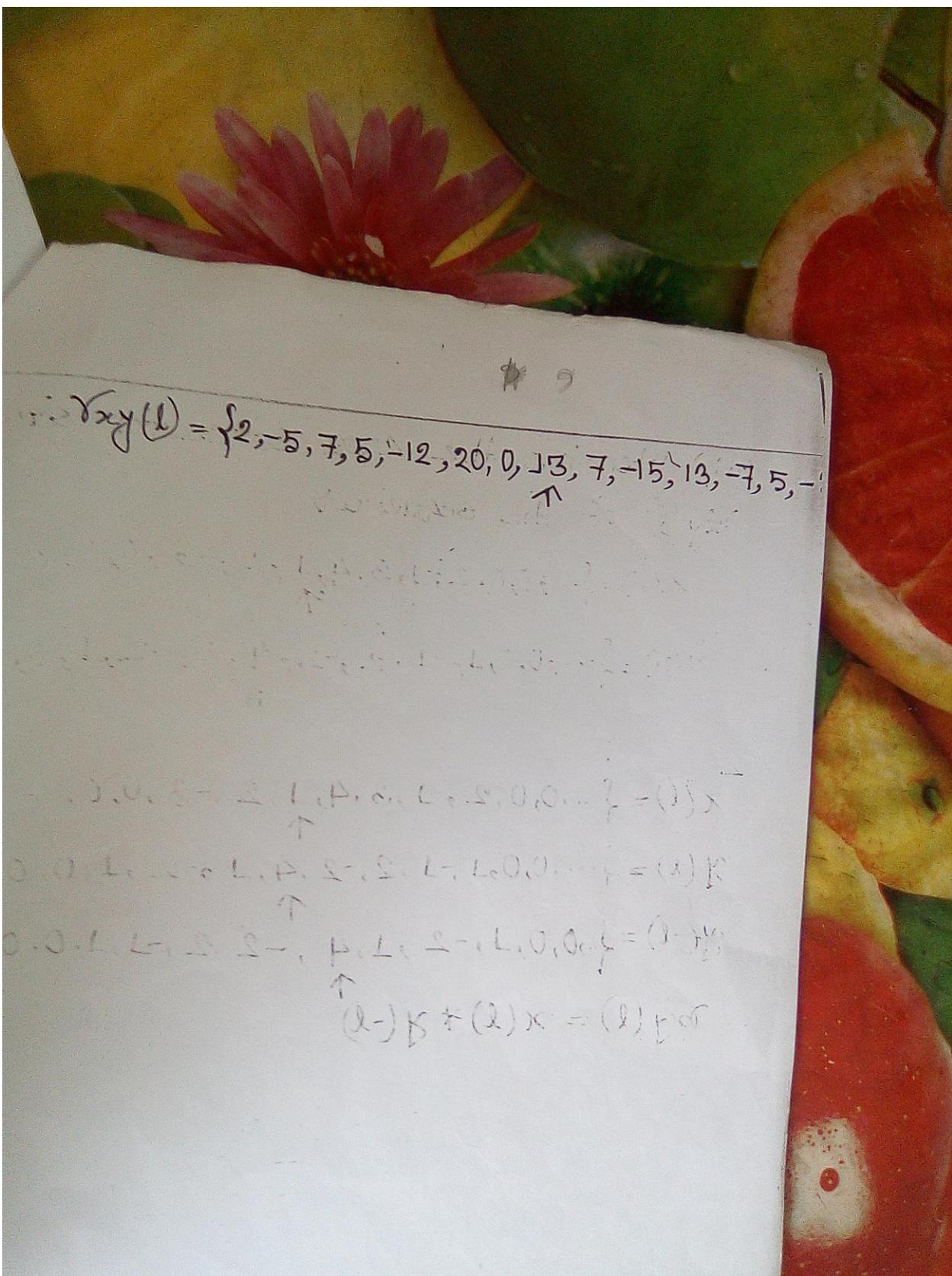
$$\xrightarrow{-} x(l) = \{ \dots, 0, 0, 2, -1, 3, 4, \underset{\uparrow}{1}, 2, -3, 0, 0, \dots \}$$

$$y(l) = \{ \dots, 0, 0, 1, -1, 2, -2, 4, \underset{\uparrow}{1}, -2, 1, 0, 0, \dots \}$$

$$y(-l) = \{ \dots, 0, 0, 1, -2, \underset{\uparrow}{1}, 4, -2, 2, -1, 1, 0, 0, \dots \}$$

$$r_{xy}(l) = x(l) * y(-l)$$

| $x(l)$ | 2 | -1 | 3 | 4 | 1 | 2 | -3 |
|---------|----|----|----|----|----|----|----|
| $y(-l)$ | 1 | 2 | -1 | 3 | 4 | 1 | 2 |
| | -2 | -4 | 2 | -6 | -8 | -2 | -4 |
| | 1 | 2 | -1 | 3 | 4 | 1 | 2 |
| | 4 | 8 | -4 | 12 | 16 | 4 | 8 |
| | -2 | -4 | 2 | -6 | -8 | -2 | -4 |
| | 2 | 4 | -2 | 6 | 8 | 2 | 4 |
| | -1 | -2 | 1 | -3 | -4 | -1 | -2 |
| | 1 | 2 | -1 | 3 | 4 | 1 | 2 |



2018

1.a) Why signal processing is needed? Draw the block diagram of DSP system and briefly introduce the different components of the system. [3]

- Signal processing needed: Signals need to be processed so that the information that they contain can be displayed, analyzed, or converted to another type of signal that may be of use. In real-world, analog products detect signals such as sound, light, temperature or pressure and manipulate them. Converters such as an Analog-to-Digital converter then take the real-world signal and turn it into the digital format of 1's and 0's.

[Same as 2016 1(a)]

✓
1.b) Write the mathematical expression of a discrete time sinusoidal signal and draw the signal with $\omega = \pi/6$ and $\phi = \pi/3$. [3]

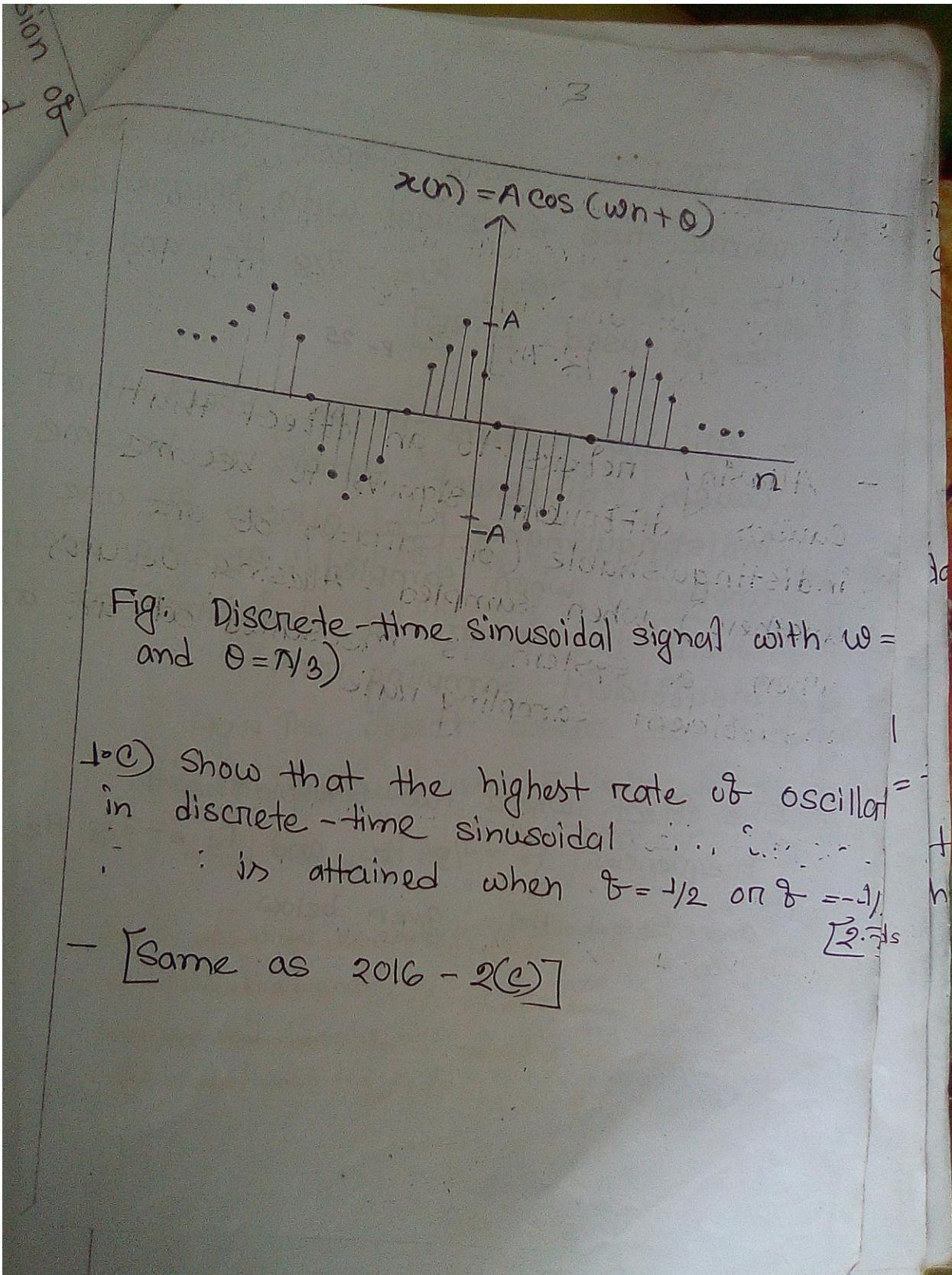
- A discrete-time sinusoidal signal may be expressed as,

$$x(n) = A \cos(\omega n + \phi), -\infty < n < \infty$$

where n is an integer variable, called the sample number, A is the amplitude of the sinusoid, ω is the frequency in radians per sample and ϕ is the phase in radians. If instead of ω we use the frequency variable f defined by

$$\omega = 2\pi f$$

$$x(n) = A \cos(2\pi f n + \phi), -\infty < n < \infty$$



1
2. a) What is aliasing effect. Show the where two sinusoidal with frequencies $F_0 = 1/8$ Hz and $F_1 = -7/8$ Hz and $F_0 = 1$ Hz is used. [2.75] P- 25

- Aliasing refers to an effect that causes different signals to become indistinguishable (or aliases of one another) when sampled. Aliasing occurs when a system is measured at an insufficient sampling rate.

The effect where two sinusoidal with frequencies $F_0 = 1/8$ Hz and $F_1 = -7/8$ Hz and $F_0 = 1$ Hz given below:

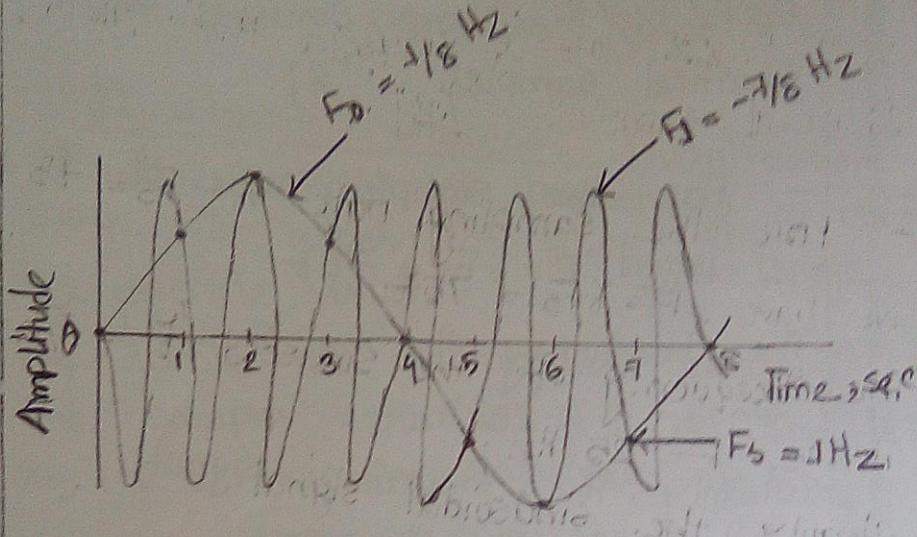


Fig-1 The effect where two sinusoidal with frequencies $F_0 = 1/8 \text{ Hz}$ and $F_1 = -7/8 \text{ Hz}$ and $F_s = 1 \text{ Hz}$

Fig-1, shows an example of aliasing, where two sinusoids with frequencies $F_0 = \frac{1}{8} \text{ Hz}$ and $F_1 = -\frac{7}{8} \text{ Hz}$ yield identical samples when a sampling rate of $F_s = 1 \text{ Hz}$ is used. When $k = -1$, $F_0 = F_1 + F_s = (-\frac{7}{8} + 1) \text{ Hz}$

$$= \frac{1}{8} \text{ Hz.}$$

6

analog

2.b) Consider the signal: $x_a(t) = 3 \cos(10\pi t)$
Suppose $F_s = 75$ Hz. What is the frequency
 $0 < F < F_s/2$ of a sinusoid that yields
identical samples. [3]

- For the sampling rate of $F_s = 75$ Hz
we have, $F = \frac{1}{T} = 75$

The frequency of the sinusoid $F = \frac{1}{3}$.
Hence $F = 25$ Hz.

Clearly, the sinusoidal signal

$$x_a(t) = 3 \cos 2\pi F t \\ = 3 \cos 50\pi t$$

Sampled at $F_s = 25$ samples yields
identical samples. Hence $F = 50$ Hz is
an alias of $F = 25$ for the sampling
rate $F_s = 75$ Hz.

6
2. b) Consider the signal: $x_a(t) = 3 \cos 100\pi t$

Suppose $F_s = 75$ Hz. What is the frequency $0 < F < F_s/2$ of a sinusoid that yields identical samples? [3]

- For the sampling rate of $F_s = 75$ Hz

$$\text{we have } F = \frac{F_s}{T} = 75 \text{ Hz}$$

The frequency of the sinusoid $F = 1/3$.

Hence $F = 25$ Hz.

Clearly, the sinusoidal signal

$$y_a(t) = 3 \cos 2\pi F t \\ = 3 \cos 50\pi t$$

Sampled at $F_s = 25$ samples/s yields identical samples. Hence $F = 50$ Hz is an alias of $F = 25$ for the sampling rate $F_s = 75$ Hz.

Q.1) What is quantization process. Define quantization step size and quantization error with examples. [3]

- Quantization process: Quantization is the process of converting a continuous range of values into a finite range of discrete values.

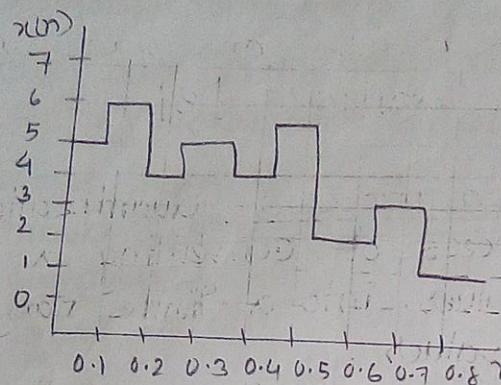
We denote the quantization operation on the samples $x(n)$ as $Q[x(n)]$ and let $x_q(n)$ denote the sequence of quantized samples at the output of the quantizer.

Hence, $x_q(n) = Q[x(n)]$

Quantization step size: The spacing between the two adjacent representation levels is called a quantum or quantization step-size.

3.9) Definition
of LTI system

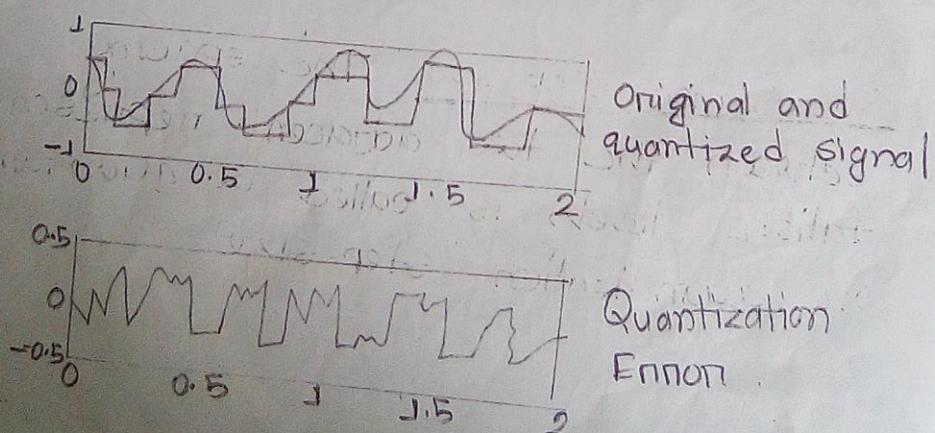
For example -



Quantization error: The difference between an input value and its quantized value is called a quantization error.

Quantization error, $e_q(n) = x_q(n) - x(n)$

For example -



9
3. a) Define LTI system. Discuss the response of LTI systems to arbitrary inputs. [2]

- LTI system: An LTI system is one which possesses two of the basic properties using linearity and time-invariance.

Linearity: An LTI System obeys superposition principle which states that the output of the system to a weight sum of inputs is equal to the corresponding weighted sum of the outputs to each of the individual inputs.

Time-invariance: If the input-output relation of a system does not vary with time, the system is said to be time-invariant

[Same as 2016-1(b)]

3.b) Find $y(n)$ if $x(n) = n+2$ for $0 \leq n \leq 3$
and $h(n) = a^n u(n)$ for all n . [3]

- We have,

$$y(n) = \sum_{k=-\infty}^{\infty} x(k) h(n-k)$$

Given,

$$x(n) = n+2 \text{ for } 0 \leq n \leq 3$$

$$h(n) = a^n u(n) \text{ for all } n$$

$h(n) = 0$ for $n < 0$, so the system is causal.
 $x(n)$ is causal finite sequence whose value
is zero for $n > 3$.

Therefore,

$$\begin{aligned} y(n) &= \sum_{k=0}^{3} x(k) h(n-k) \\ &= \sum_{k=0}^{3} (k+2) a^{n-k} u(n-k) \\ &= 2a^n u(n) + 3a^{n-1} u(n-1) \\ &= 4a^{n-2} u(n-2) + 5a^{n-3} u(n-3) \end{aligned}$$

3.c) Define causal and non-causal systems with examples. Determine if the following system are time-invariant or time-variant $y(n) = x(-n)$. [3]

- Causal system: A system is said to be causal if the output of the system at any time n depends only on present and past inputs, but does not depend on future inputs.

Example : ① $y(t) = x(t)$
② $y(t) = x(t) + x(t-1)$

Non-causal system: A system is said to be non-causal if the output of the system at any time n depends on past, present, future inputs.

Example : ① $y(t) = x(t+2)$
② $y(t) = x(t) + x(t-1) + x(t+1)$

Here given,

$$y(n) = x(-n)$$

Step-1: $y(n) \xrightarrow{n_0} y(n-n_0) = x(-n-n_0) \quad \text{--- } ①$

Step-2: $x(n) \xrightarrow{n_0} x(n-n_0) \rightarrow \text{sys} \rightarrow x[n-n_0] = x(-n+n_0)$

The output of eqn ① and eqn ② are different so it is time-variant system.

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Thank you