



## **CALIFA Electronics & DAQ**



Supported by BMBF 05P15WOFNA and 05P19WOFN1.

The results presented here are based on the experiment s444/s473, which was performed at the beam line/infrastructure Cave C at the GSI Helmholtzzentrum für Schwerionenforschung, Darmstadt (Germany) in the frame of FAIR Phase-0.

GEFÖRDERT VO







## **Tobias Jenegger**

CALIFA WG meeting 18.10.2024

**Inner Cabling** 

**Preamplifier Status** 

DAQ status - Exp S091/118 in 2024

**DAQ Upgrade** 

**Documentation Status** 

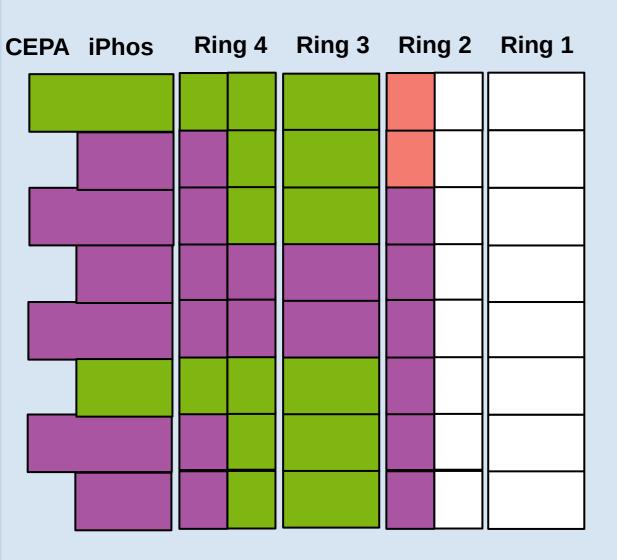
#### TUM Members:

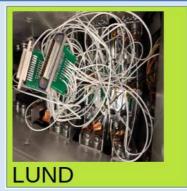
Roman Gernhäuser, Philipp Klenze, Mrunmoy Jena, Gero Bollmann, Tobias Jenegger



## **Inner Cabling – Wixhausen Half**









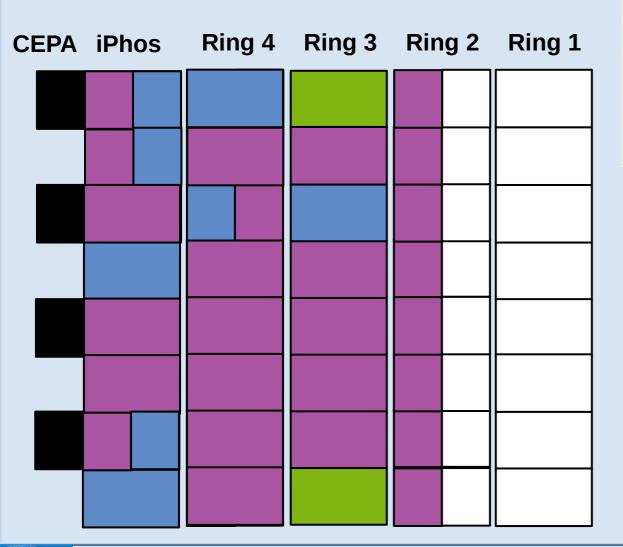
Prototype





## **Inner Cabling – Messel Half**









TuLu





## **Inner Cabling – Upgrade Backward Barrel**



#### TODO:

- material needed
- how much workload?
- cost

Phone call with Anna Lena on Wed.



## **CALIFA Preamplifier Status for Exp. S091/118 - 2024**



CE	PA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
_		<b>DR</b> 30/300	SR 30/300	SR 30/300		

→ **CEPA**: 8 x 3/45pC DR Preamplifier

→ **iPhos:** mixed configuration:

8 x 3/45pC DR PA

8 x 3/30pC DR PA

→ **Barrel – Ring 4&3**: 32 x 3/30pC SR PA

→ Backward Barrel (BB) – Ring 2:

Bricolage of 16 PA (SR/DR)

#### What is still needed?

32 x SR 3/30pC PA for BB (the current BB are spares for different applications)

#### **Modifications:**

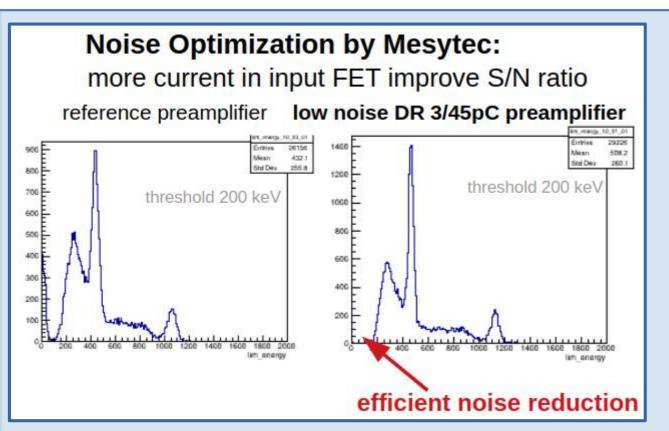
12x DR → to low noise input and 3/45pC range

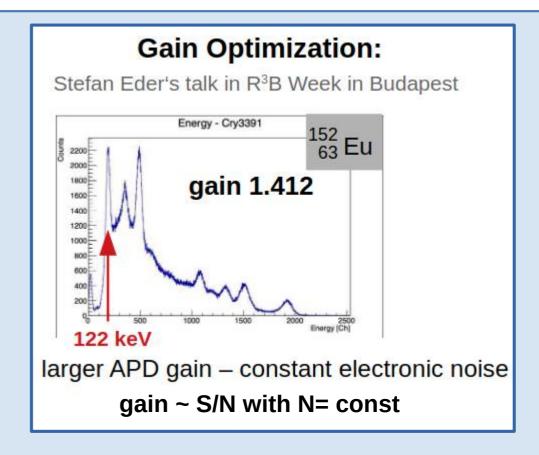
32x SR → lower noise input stage



## **Optimization of S/N in Preamplifiers**







#### 3/45pC DR Preamplifiers:

- → allow to increase gain → lower thresholds
- → 45pC covers full range up to 300 MeV

Tobias Jenegger 6



#### What we still need:



#### "Default Config."

# nfig." "4π Config." 3 Ring 2 Ring 1 CEPA iPhos Ring 4 Ring 3 Ring

CEPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
	<b>DR</b> 30/300	SR 30/300	SR 30/300		
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
	<b>DR</b> 30/300	SR 30/300	SR 30/300		
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
	<b>DR</b> 30/300	SR 30/300	SR 30/300		
	DR 48 30/450	SR 30/300	SR 30/300		
	<b>DR</b> 30/300	SR 30/300	SR 30/300		



CE	EΡΑ	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
		<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 32</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 48</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300
•		<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300

<sup>\*</sup> in both versions all preamps are upgraded to lower noise input stage

Tobias Jenegger



## **Workload & Costs**



#### "Default Config."

**To Buy:** 32 x 3/30pC SR PA for BB

128k€

#### To Modify:

12x DR → to low noise input and 3/45pC range ??

??

??

32x SR → low noise input stage

#### <u>"4π Config."</u>

#### To Buy:

16 x 3/30pC **DR** PA for Ring4 16 x 3/30pC **SR** PA for Ring1

#### To Modify:

12x DR → to low noise input and 3/45pC range ??

32x SR → low noise input stage

??

??

#### Note:

+ 32 FEBEX cards are needed for this configuration LV load balancing may be critical

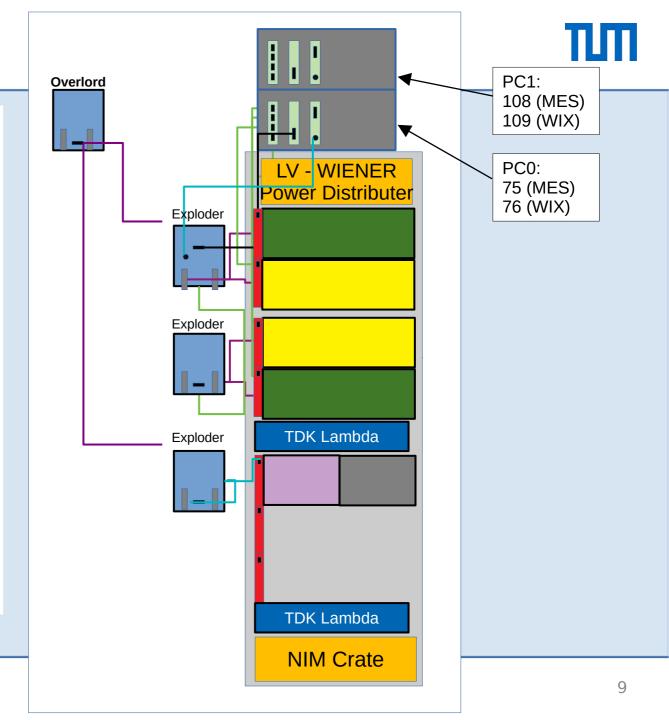
CEPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
	DR 32	SR	SR	SR	SR
	30/450	30/300	30/300	30/300	30/300
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
	DR 32	SR	SR	SR	SR
	30/450	30/300	30/300	30/300	30/300
	<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
	DR 32	SR	SR	SR	SR
	30/450	30/300	30/300	30/300	30/300
	DR 48	SR	SR	SR	SR
	30/450	30/300	30/300	30/300	30/300
	DR 32	SR	SR	SR	SR
	30/450	30/300	30/300	30/300	30/300

CEPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	DR 32 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	DR 32	DR 30/300	SR 30/300	SR 30/300	SR 30/300



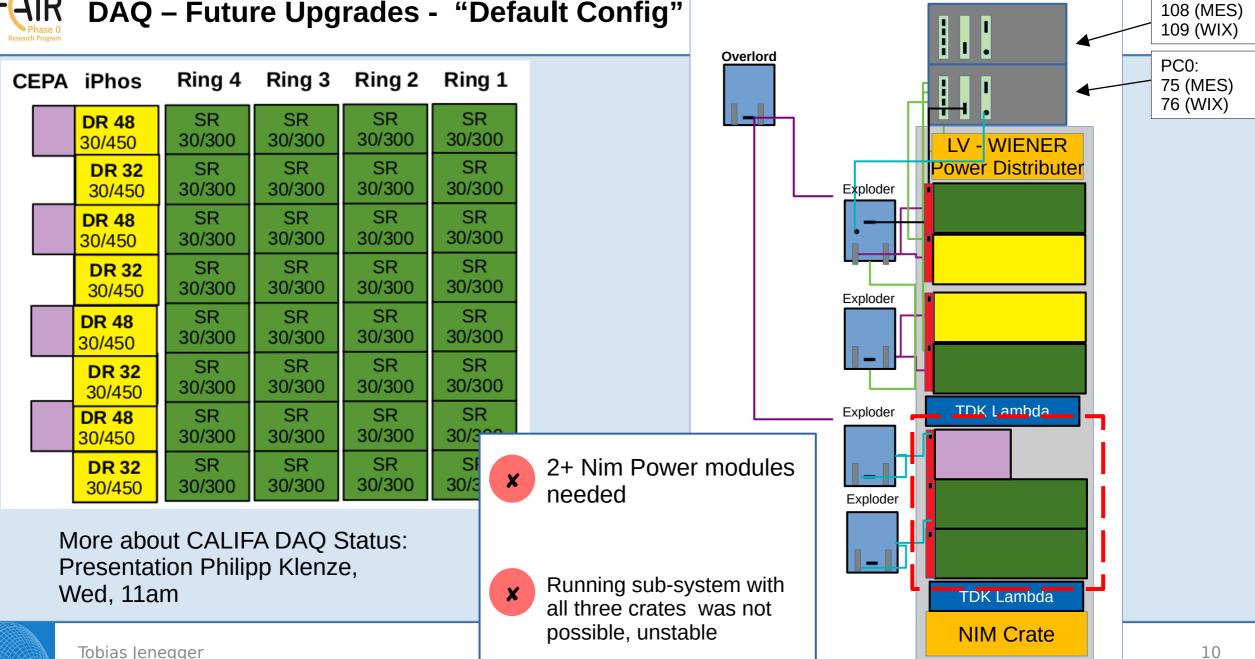
## DAQ Status for Exp. S091/118 - 2024

CI	EPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
,		<b>DR</b> 30/300	SR 30/300	SR 30/300		
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300		
ļ '		<b>DR</b> 30/300	SR 30/300	SR 30/300		





**DAQ – Future Upgrades - "Default Config"** 

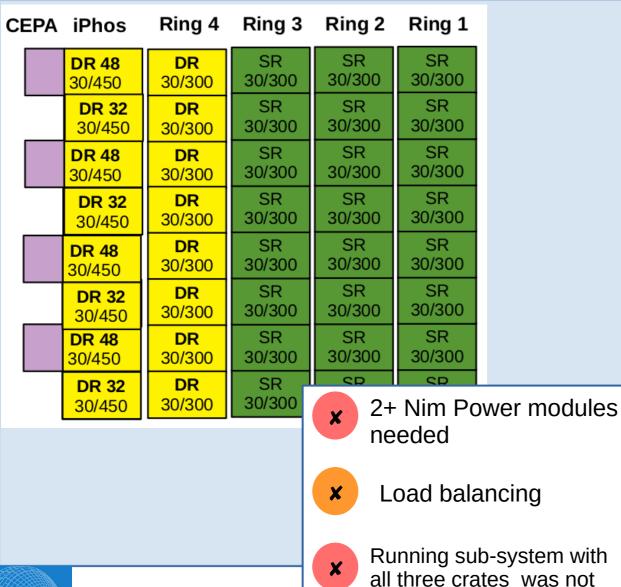


PC1:

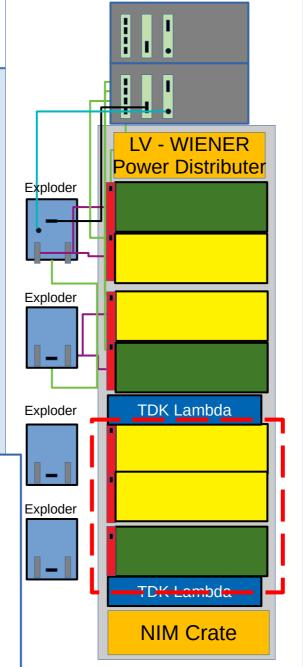


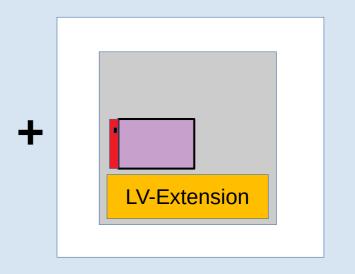
## DAQ – Future Upgrades - $4\pi$ Config.

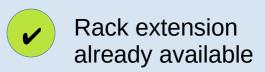




possible, unstable









## **Backup Electronics needed:**



## **Already available or ordered:**

- 5 Exploders
- > 16+ FEBEX cards
- NIM Power Crate

#### Still to buy:

2 x PEXOR cards

TODO:

2 x PEXARIA cards

Costs?

- 2 x TRIXOR cards
- > 1 x DAQ Pc
- 2 x TDK Lambdas



## **Outer Cabling**



## For "Default Config.":

- 48+ 32pin cables (BB) need to be produced
- > 16+ LV cables need to be produced



Workload: 2-3 weeks

Components on stock or easily available

#### For " $4\pi$ Config.":

- > 16+ 32pin cables (BB) need to be produced
- 16+ LV cables need to be produced
- 32+ 64pin DR SCSI cables for Ring 4



Workload: 1-2 weeks

No ~3.5 m SCSI cables available on the market!





LV cables

32 pin cables



## **Documentation Status**



- Info about FAB,FEBEX,PAs, Exploders on google spreadsheet: https://docs.google.com/spreadsheets/d/1TqvlTK1xVxb5rhWSQlSCEBDzf2x7GS8RSkM1WwQoNJM/edit?hl=de&pli=1&gid=1790449867#gid=1790449867
- Info califa-cabling-slowcontrol: https://elog.gsi.de/land/CALIFA/375
- More (more or less structured) info on our wiki: https://wiki.r3b-nustar.de/detectors/califa/overview



#### **General Questions:**

- Do we have an overview what we have documented and what not?
- Where and how should the documentations be stored?
  - → lifetime ( how long will google spreadsheets be available (for free) ?)
  - → accessibility (read and write permissions?)

Maybe open question for R3BWeek-Paris, since all detector groups should be affected



## **Summary & To Dos**



## "Default Config."

## Decision has to be taken:

## <u>"4π Config."</u>

CI	ΞPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
,		<b>DR 32</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
,		<b>DR 32</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
		DR 48 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
'		DR 32 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 48</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300
		<b>DR 32</b> 30/450	SR 30/300	SR 30/300	SR 30/300	SR 30/300

Costs:

Workload cable production:

2-3 weeks

CEPA	iPhos	Ring 4	Ring 3	Ring 2	Ring 1
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	<b>DR</b> 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 48</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300
	<b>DR 32</b> 30/450	DR 30/300	SR 30/300	SR 30/300	SR 30/300

Costs:

Workload cable production:

1-2 weeks

- → provide backup-electronics
- → **Documentation**

Todo:

Add something about light pulser system

Tobias Jenegger