

4.2 Q Consider the expression  $F = (A + C + D) \& (E + C)$ .  
Simplify this expression and then implement it using a CMOS gate and an inverter.

A: Simplified:  $C \mid (E \& (A \mid D)) = F$

Inverted:  $\neg C \& (\neg E \mid (\neg A \& \neg D)) = \neg F$

