ECE/CS 250 – Recitation #9 – Prof. Sorin
Caches and Memory

Objective: In this recitation, you will gain a greater understanding of the importance of caches/memory and how they work.

Complete as much of this as you can during recitation. If you run out of time, please complete the rest at home.

1. Task 1: Caches and Memory in the Real World

1) How much cache is in your laptop? You may need to google this to find out. And to google it, you'll need to know exactly which processor you have. On a Windows machine, open up the Start menu (bottom left – Windows logo), then right click on the "Computer" option. On a Linux machine (like the teer machines), you can find this info by typing "more /proc/cpuinfo" at the command line. I recommend looking at both Windows and Linux (for Linux, use one of the machines you used for homework #1), if you can.
2) How much physical memory do you have? On a Windows machine, you can hit ctrl-alt-delete and then from there open up the Task Manager; in the Task Manager, click on the "Performance" tab. On a Linux machine, you can type "top" at the command line and see much the same information. Once again, I recommend looking at both Windows and Linux.
3) How much disk space do you have? Can you see how much is being used for "swap" (i.e., to hold pages that don't currently fit in physical memory)?

Note: For a Windows machine, if you can't find this information in the ways described above, you can also find it in the Task Manager (hit ctrl-alt-delete then select Task Manager) under the "Performance" tab.

2. Task 2: Cache Examples

You have a 64-bit machine with a cache that is 128KB and 2-way set-associative. Blocks are 64B.

1) Sketch this cache. How many frames does it have? How many sets does it have?
2) Divide up the 64-bit address into its three fields, for purposes of accessing this cache. How many block offset bits are there? How many set index bits are there? How many tag bits are there?
3) For a given sequence of requests, can you explain which ones hit and which ones miss? And for the misses, can you classify them as cold, capacity, or conflict misses? For example, consider

the address sequence (assume these are all caused by load-byte instructions): 67, 125, 18, 10, 64K+67, 128K+67.

4) What are the first three blocks that map to set 2? What are the first three blocks that map to set 7? Think about this in two ways:
    a. You know that set 0 hold the blocks [0:63], [64K:64K+63], [128K:128K+63], etc.
    b. You know that all blocks that map to a set have the same set index bits. Write out the set index bits and set the block offset bits to zero (to get the address of the 0^{th} byte in the block). If you set the tag bits to zero, you get the 0^{th} block that maps to this set. If you set the tag bits to 0000…..0001, you get the 1^{st} block that maps to this set. Etc.