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Analog Digital Interfaces in VLSI

ELEN E6316

Spring 2021 3 / 12 / 2021

Report

The measured output voltages for a 9b digital-to-analog converter are given in the .csv file.

Question 1a:

Find the effective LSB size.

The graph below shows the digital input code represented in decimal format versus the analog output:

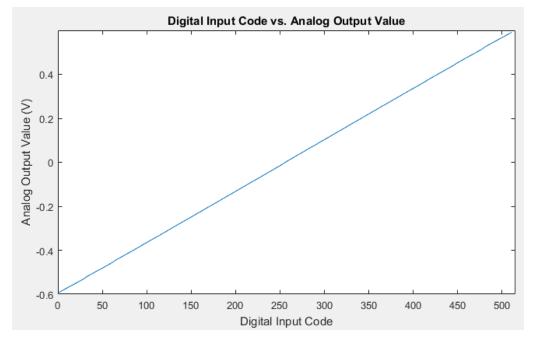


Figure 1. Digital Input Code versus Analog Output Voltage

The effective LSB size can be calculated as follows:

$$V_{LSB,actual} = \frac{V_{max} - V_{min}}{2^N - 1} = \frac{(0.59149) - (-0.59404)}{2^9 - 1} = 2.32002 * 10^{-3} \approx \boxed{2.32002 \, mV}$$

Command Window results for the LSB size:

Figure 2. Effective LSB Size Results in MATLAB

Based on the lecture slides, the offset error for the given DAC can be calculated in the following way:

$$E_{off} = \frac{V_{out}}{V_{LSB}}(at\ code = 0\ ...\ 0) = \frac{-0.59404}{2.34375*10^{-3}} \approx -253.45707\ LSBs$$

Moreover, the full-scale error for the DAC can be calculated as follows:

$$V_{1...1,actual} = 0.59149 \, V$$

$$V_{1...1,ideal} = (2^N - 1) \, * \, V_{LSB} = 511 * 2.34375 * 10^{-3} \approx \, 1.197656 \, V$$

$$E_{FS} = \frac{V_{1...1,actual} - V_{1...1,ideal}}{V_{LSB}} = \frac{0.59149 \ V - 1.197656 \ V}{2.34375 * 10^{-3}} \approx -258.63093 \ LSBs$$

Command Window results for the offset and full-scale error:

```
Command Window

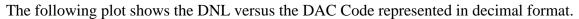
Effective LSB = 2.32002 mV

Offset Error = -253.45707 LSBs
Full Scale Error = -258.63093 LSBs
```

Figure 3. Offset and Full-Scale Error Results in MATLAB

Question 1b:

Plot the DNL versus DAC code. What are the minimum and maximum DNL, and what is the standard deviation of the DNL? Is the DAC monotonic?



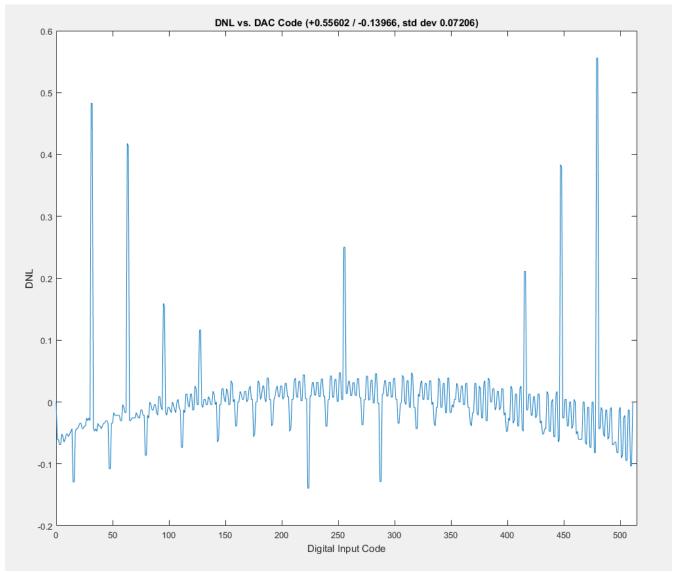


Figure 4. DNL versus the DAC Code

The minimum and maximum values for the DNL are:

$$minimum (DNL) = \boxed{-0.13966 LSBs}$$

$$maximum (DNL) = 0.55602 LSBs$$

The standard deviation of the DNL was also found to be:

$$\sigma(DNL) = \boxed{0.07206}$$

Since the minimum value of the DNL is greater than -1 LSB it can be said that the DAC is monotonic.

$$minimum (DNL) = -0.13966 LSBs > -1 LSB$$

DAC is monotonic.

Command Window results for the mathematical analysis of the DNL:

```
Command Window

Effective LSB = 2.32002 mV

Offset Error = -253.45707 LSBs
Full Scale Error = -258.63093 LSBs

Standard Deviation (DNL) = 0.07206
Minimum DNL = -0.13966 LSBs
Maximum DNL = 0.55602 LSBs
The DAC is monotonic. Minimum DNL > -1 LSB.
```

Figure 5. DNL Analysis Results in MATLAB

Question 1c:

Plot the INL versus DAC code, and find the minimum and maximum values of INL.

The following plot shows the INL versus the DAC Code represented in decimal format.

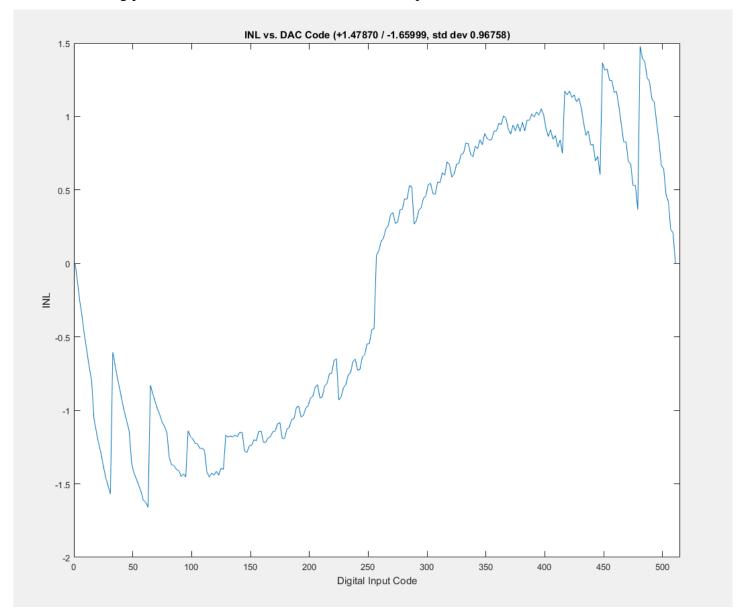


Figure 6. INL versus the DAC Code

The minimum and maximum values for the INL are:

$$minimum (INL) = \boxed{-1.65999 LSBs}$$

$$maximum (INL) = 1.47870 LSBs$$

The standard deviation of the INL was also found to be:

$$\sigma(INL) = \boxed{0.96758}$$

Command Window results for the mathematical analysis of the INL:

```
Command Window

Effective LSB = 2.32002 mV

Offset Error = -253.45707 LSBs
Full Scale Error = -258.63093 LSBs

Standard Deviation (DNL) = 0.07206
Minimum DNL = -0.13966 LSBs
Maximum DNL = 0.55602 LSBs
The DAC is monotonic. Minimum DNL > -1 LSB.

Standard Deviation (INL) = 0.96758
Minimum INL = -1.65999 LSBs
Maximum INL = 1.47870 LSBs
```

Figure 7. INL Analysis Results in MATLAB

As part of the report, the following are included for the submission:

- "DAC_9b_levels.csv"
 - o Original CSV file given as part of the assignment.
- "problem set 6 code.m"
 - o MATLAB code where the DAC output voltages are analyzed.
- "analysis output_DNL.csv"
 - o CSV output file from the code showing the values for the DNL against the DAC levels.
- "analysis output INL.csv"
 - o CSV output file from the code showing the final values for the INL.