Jean-Christophe James Owens

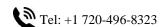Tel: +1 720-496-8323   JeanChristophe.Owens@colorado.edu   github.com/jeow9028

Summary of Skills

- **Software Dev:** C, C++, Embedded C, Verilog, C, Java, Python, Matlab, SQL, Unix, LT-Spice
- **Software Web:** Html, CSS
- **Methods:** Agile / Waterfall methods using GitHub
- **Languages:** Bilingual, French and English
- **Skills:** Leadership, Communication, Organization, Dependable, Adaptable

Education

- Bachelor of Science: **Electrical Engineering**
  University of Colorado Boulder

Engineering Projects

- **Grad Capstone | MIST**   **Spring 2018**
  MIST (Maximizing Imagery Satellite Technology) a grad capstone project. Developed with TAP Lab (Lockheed Martin) and USAF OBAC an infrared data system. Merged multiple data sets from GOES-R satellite and rendered an image. Provided value to USAF improved weather visibility metrics in undefined regions.

- **Ethical Hacking Club | HackCU**   **2016 – 2017 | Present**
  Active member of the Computer Science club focused on expanding knowledge on white-hat methods and tools. Part of the organizing team focused on sponsorship for HackCU. Responsible for contacting networking and hosting a 600+ people event.

- **Electrokart**   **Fall 2017**
  An Embedded Systems class where we used embedded C. Programed an MSP432401R as a master and slave via Bluetooth through UART. Built a chassis and circuit system with a 12 Volt battery, speed controllers and motors. Team was awarded 1st place in final project and received $2,000 prize from Texas Instruments.

Relevant Coursework

Hack for Defense	- 7000		Wireless Systems	- 2360
Embedded Systems	- 2440		Programming Digital Systems	- 3350
Digital Logic	- 2350		Software Tool and Methods	- 3308
Linear System	- 3300		Digital Design Lab	- 3360
Microelectronics	- 3250		**Electromagnetism**	- 3400
Bioelectromagnetics	- 5341		**Implantable Devices**	- 4021

Leadership Experience

Technical Marketing Intern – Xilinx   **June 2018 – January**

Assisted on Xilinx demos to clients, worked primarily on HBM (High Bandwidth Memory). Coded in C and Verilog. Standardized demo format with a GUI platform for FPGA designs.

Organizer – HackCU   **August 2017- Present**

Involved on the sponsorship team and supported the marketing for HackCU IV. Mentored hacks and provided guidance during the events.

Teaching Assistant – University Colorado Boulder   **August 2017- Present**

Provided programming aid for students, was a resource for designing and debugging code.

Miscellaneous

- Tri-citizenship, US, UK, France. Most of my life was spent in Europe. Currently reside and study in Boulder, Colorado.