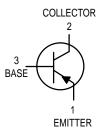
High Voltage Transistors PNP Silicon



MAXIMUM RATINGS

Rating	Symbol	BF421	BF423	Unit		
Collector-Emitter Voltage	VCEO	-300	-250	Vdc		
Collector-Base Voltage	VCBO	-300	-250	Vdc		
Emitter-Base Voltage VEBO -5.		-5.0				
Collector Current — Continuous	IC	-500		-500		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C		
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12		Watts mW/°C		
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150		°C		

THERMAL CHARACTERISTICS

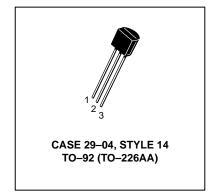
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•			
Collector-Emitter Breakdown Voltage (1) (I _C = -1.0 mAdc, I _B = 0)	BF421 BF423	V(BR)CEO	-300 -250	_ _	Vdc
Collector-Base Breakdown Voltage (I _C = -100 μAdc, I _E = 0)	BF421 BF423	V(BR)CBO	-300 -250		Vdc
Emitter–Base Breakdown Voltage (I _E = –100 μAdc, I _C = 0)	BF421 BF423	V _{(BR)EBO}	-5.0 -5.0	_ _	Vdc
Collector Cutoff Current (V _{CB} = -200 Vdc, I _E = 0)	BF421 BF423	ICBO	_ _	-0.01 	μAdc
Emitter Cutoff Current (V _{EB} = -5.0 Vdc, I _C = 0)	BF421 BF423	IEBO		-100 	nAdc

^{1.} Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.

BF421 BF423





BF421 BF423

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS		-			•
DC Current Gain (I _C = -25 mA, V _{CE} = -20 Vdc)	BF421 BF423	hFE	50 50	_	_
Collector-Emitter Saturation Voltage (I _C = -20 mAdc, I _B = -2.0 mAdc)		VCE(sat)	_	-0.5	Vdc
Base-Emitter Saturation Voltage (IC = -20 mA, I _B = -2.0 mA)		V _{BE(sat)}	_	-2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				•	
Current-Gain — Bandwidth Product (I _C = -10 mAdc, V _{CE} = -10 Vdc, f = 20 MHz)		fT	60	_	MHz
Common Emitter Feedback Capacitance (V _{CB} = -30 Vdc, I _E = 0, f = 1.0 MHz)		C _{re}	_	2.8	pF

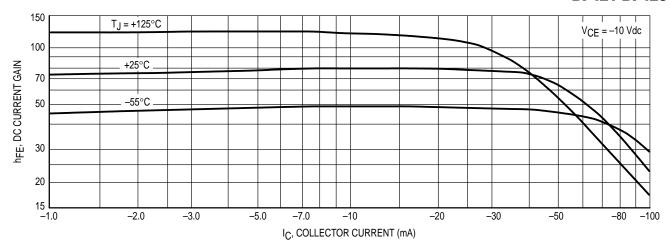


Figure 1. DC Current Gain

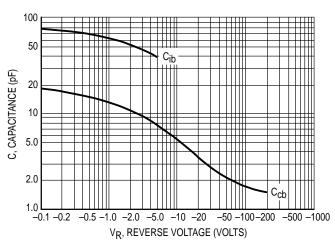


Figure 2. Capacitances

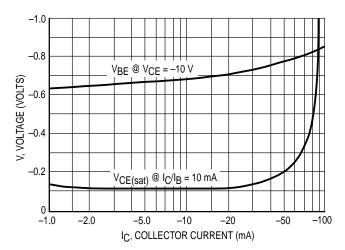


Figure 4. "On" Voltages

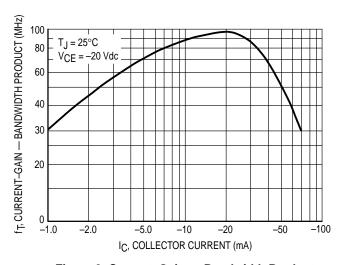


Figure 3. Current-Gain — Bandwidth Product

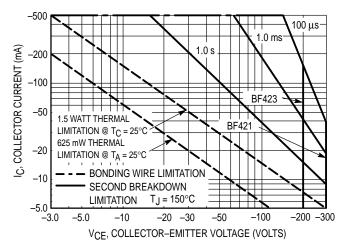
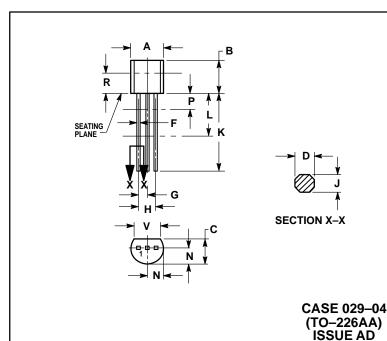


Figure 5. Active Region — Safe Operating Area

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L. DIMENSION P APPLIES BETWEEN F AND L.
 DIMENSION D AND J APPLY BETWEEN L AND K
 MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100	_	2.54
R	0.115		2.93	
V	0.135		3 43	

STYLE 14:

PIN 1. EMITTER 2. COLLECTOR

BASE

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