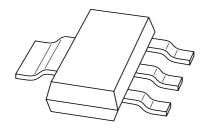
## DISCRETE SEMICONDUCTORS

# DATA SHEET



## BF720; BF722 NPN high-voltage transistors

Product specification Supersedes data of 1996 Dec 05 1999 Apr 21





## **NPN** high-voltage transistors

BF720; BF722

#### **FEATURES**

• Low feedback capacitance.

#### **APPLICATIONS**

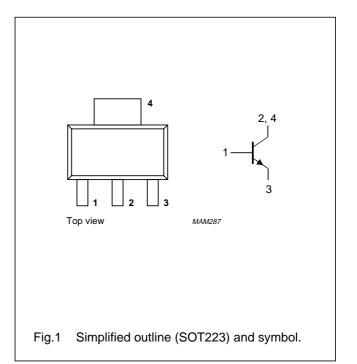
- Class-B video output stages of colour television receivers
- General purpose high voltage circuits.

#### **DESCRIPTION**

NPN transistors in a SOT223 plastic package. PNP complement: BF723.

#### **PINNING**

PIN	DESCRIPTION
1	base
2, 4	collector
3	emitter



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BF720		_	300	V
	BF722		_	250	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BF720		_	300	V
	BF722		_	250	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	1.2	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

## NPN high-voltage transistors

BF720; BF722

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	106	K/W
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	note 1	25	K/W

#### Note

1. Device mounted on printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

#### **CHARACTERISTICS**

 $T_j$  = 25  $^{\circ}C$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 200 V	_	10	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 200 V; T <sub>j</sub> = 150 °C	_	10	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	50	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 25 mA; V <sub>CE</sub> = 20 V	50	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 5 \text{ mA}$	_	0.6	V
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = i <sub>c</sub> = 0; V <sub>CE</sub> = 30 V; f = 1 MHz	_	1.6	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	60	_	MHz

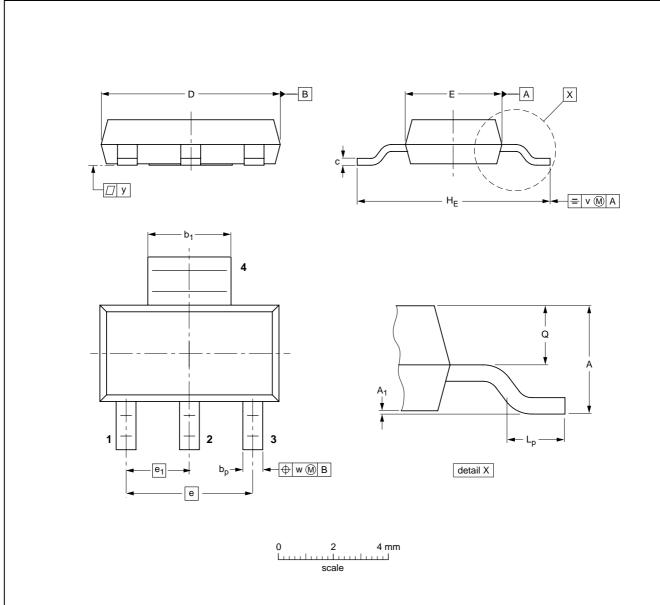
## NPN high-voltage transistors

BF720; BF722

#### **PACKAGE OUTLINE**

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

**SOT223** 



#### **DIMENSIONS** (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	bp	b <sub>1</sub>	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	٧	w	у
mm	1.8 1.5	0.10 0.01	0.80 0.60	3.1 2.9	0.32 0.22	6.7 6.3	3.7 3.3	4.6	2.3	7.3 6.7	1.1 0.7	0.95 0.85	0.2	0.1	0.1

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION			EIAJ	PROJECTION	ISSUE DATE	
SOT223					<del>96-11-11</del> 97-02-28	

#### NPN high-voltage transistors

BF720; BF722

#### **DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

## NPN high-voltage transistors

BF720; BF722

NOTES

## NPN high-voltage transistors

BF720; BF722

NOTES

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