

KSP94

High Voltage Transistor • High Collector-Emitter Voltage: V_{CEO}= -400V • Low Collector-Emitter Saturation Voltage

- Complement to KSP44



1. Emitter 2. Base 3. Collector

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-400	V
V _{CEO}	Collector-Emitter Voltage	-400	V
V _{EBO}	Emitter-Base Voltage	-6	V
I _C	Collector Current	-300	mA
P _C	Collector Power Dissipation	625	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55~150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu A, I_E = 0$	-400			V
BV _{CES}	Collector-Emitter Breakdown Voltage	I_{C} = -100 μ A, V_{BE} =0	-400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -10\mu A, I_C = 0$	-6			V
I _{CBO}	Collector Cut-off Current	V_{CB} = -300V, V_{E} =0			-100	nA
I _{CES}	Collector Cut-off Current	V _{CE} = -400V, V _{BE} =0V			-1	μΑ
I _{EBO}	Emitter Cut-off Current	V_{BE} = -4V, I_{C} =0			-100	nA
h _{FE1}	DC Current Gain	V _{CE} = -10V, I _C = -1mA	40			
h_{FE2}		$V_{CE} = -10V, I_{C} = -10mA$	50		300	
h _{FE3}		$V_{CE} = -10V, I_{C} = -50mA$	45			
h _{FE4}		$V_{CE} = -10V, I_{C} = -100mA$	40			
V _{CE} (sat) ₁	Collector-Emitter Saturation Voltage	$I_C = -10 \text{mA}, I_B = -1 \text{mA}$			-500	mV
V _{CE} (sat) ₂		I_C = -50mA, I_B = -5mA			-750	mV
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -10mA, I _B = -1mA			-750	mV
C _{ob}	Output Capacitance	V _{CB} = -20V, I _E =0, f=1MHz		7		pF

Typical Characteristics

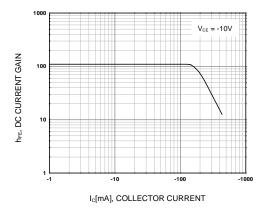


Figure 1. DC current Gain

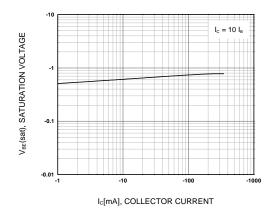


Figure 2. Base-Emitter Saturation Voltage

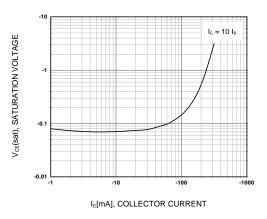


Figure 3. Collector-Emitter Saturation Voltage

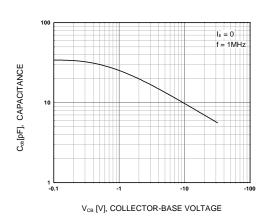
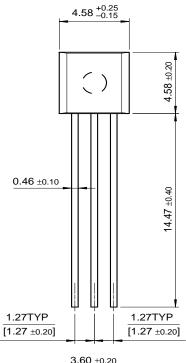
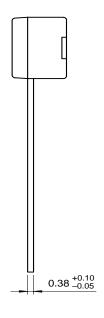


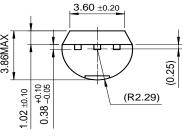
Figure 4. Collector Output Capacitance

Package Dimensions

TO-92







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