

# TS13009

# High Voltage NPN Transistor



**TO-220** 



#### Pin Definition:

- 1. Base
- 2. Collector
- 3. Emitter

#### **PRODUCT SUMMARY**

BV <sub>CEO</sub>	400V
BV <sub>CBO</sub>	700V
Ic	12A
V <sub>CE(SAT)</sub>	1.5V @ I <sub>C</sub> / I <sub>B</sub> = 12A / 3A

### **Features**

- High Voltage
- High Speed Switching

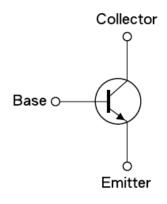
#### **Structure**

- Silicon Triple Diffused Type
- NPN Silicon Transistor

### **Ordering Information**

Part No.	Package	Packing
TS13009CZ C0	TO-220	50pcs / Tube

### **Block Diagram**



**Absolute Maximum Rating** (Ta = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Collector-Base Voltage		$V_{CBO}$	700V	V	
Collector-Emitter Voltage		$V_{\sf CEO}$	400V	V	
Emitter-Base Voltage		$V_{EBO}$	9	V	
Collector Current	DC		12	А	
	Pulse	I <sub>C</sub>	24		
Base Current	DC		6	А	
	Pulse	I <sub>B</sub>	12		
Total Power Dissipation		$P_{D}$	100	W	
Operating Junction Temperature		T <sub>J</sub>	+150	°C	
Operating Junction and Storage Temperature Range		T <sub>STG</sub>	- 55 to +150	°C	

Note: Single Pulse. P<sub>W</sub> = 300uS, Duty ≤2%



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**Electrical Specifications** (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit	
Static							
Collector-Base Voltage	$I_C = 1 \text{mA}, I_B = 0$	BV <sub>CBO</sub>	700		1	V	
Collector-Emitter Breakdown Voltage	I <sub>C</sub> =10mA, I <sub>E</sub> =0	BV <sub>CEO</sub>	400			V	
Emitter-Base Breakdown Voltage	$I_E = 1 \text{mA}, I_C = 0$	BV <sub>EBO</sub>	9			V	
Collector Cutoff Current	V <sub>CE</sub> =400V, I <sub>B</sub> =0	I <sub>CEO</sub>			1	mA	
Collector Cutoff Current	V <sub>CB</sub> =700V, I <sub>E</sub> =0	I <sub>CBO</sub>	-		1	mA	
Emitter Cutoff Current	$V_{EB} = 9V, I_{C} = 0$	I <sub>EBO</sub>	I		1	mA	
Collector-Emitter Saturation Voltage	I <sub>C</sub> =5A, I <sub>B</sub> =1A	V <sub>CE(SAT)1</sub>			1		
	I <sub>C</sub> =8A, I <sub>B</sub> =1.6A	V <sub>CE(SAT)2</sub>			1.5	V	
	I <sub>C</sub> =12A, I <sub>B</sub> =3A	V <sub>CE(SAT)3</sub>			3		
Base-Emitter Saturation Voltage	I <sub>C</sub> =5A, I <sub>B</sub> =1A	V <sub>BE(SAT)1</sub> V <sub>BE(SAT)2</sub>		1.2	V		
	I <sub>C</sub> =8A, I <sub>B</sub> =1.6A				1.6	V	
DC Current Gain	$V_{CE}$ =5V, $I_C$ = 5A	h <sub>FE</sub>	8		40		
	$V_{CE}$ =5V, $I_C$ = 8A		6		30		
Dynamic							
Frequency	$V_{CE} = 10V, I_{C} = 0.5A$	f <sub>T</sub>	4		I	MHz	
Output Capacitance	V <sub>CB</sub> =10V, f =0.1MHz	Cob	I	180	1	pF	
Resistive Load Switching Time (Ratings)							
Delay Time	V <sub>CC</sub> =125V, I <sub>C</sub> =8A, I <sub>B1</sub> =I <sub>B2</sub> =1.6A, t <sub>P</sub> =25uS Duty Cycle ≤1%	t <sub>d</sub>	-	0.06	0.1	uS	
Rise Time		t <sub>r</sub>		0.45	1	uS	
Storage Time		t <sub>STG</sub>	-	2.8	3.3	uS	
Fall Time		t <sub>f</sub>		0.3	0.5	uS	

Note: pulse test: pulse width ≤300uS, duty cycle ≤2%



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### **Electrical Characteristics Curve** (Ta = 25°C, unless otherwise noted)

Figure 1. Static Characteristics

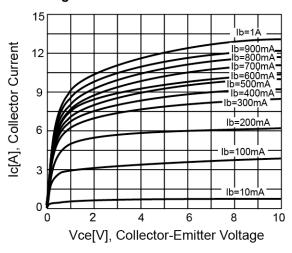


Figure 3.  $V_{CE(SAT)}$  v.s.  $V_{BE(SAT)}$ 

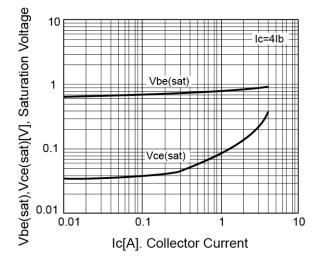


Figure 2. DC Current Gain

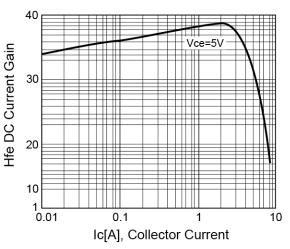
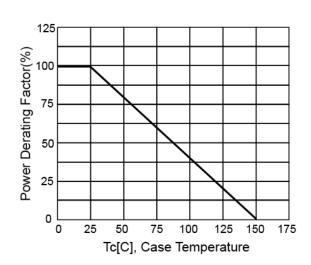


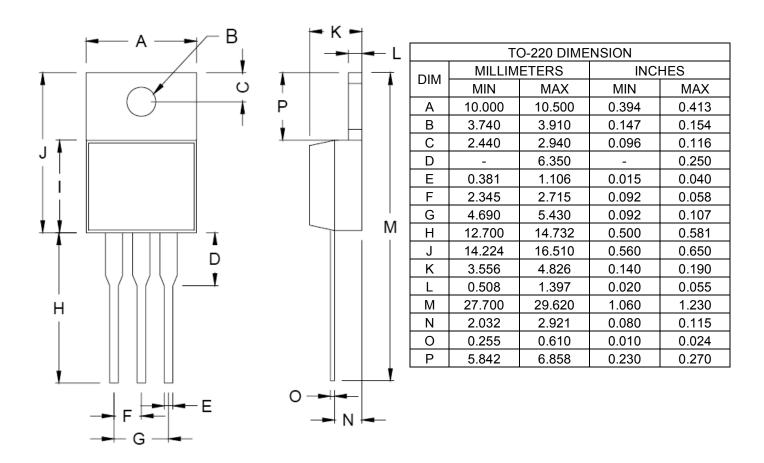
Figure 4. Power Derating



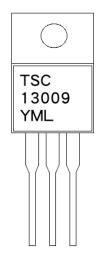




# **TO-220 Mechanical Drawing**



## **Marking Diagram**



Y = Year Code

M = Month Code

(A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)

L = Lot Code



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