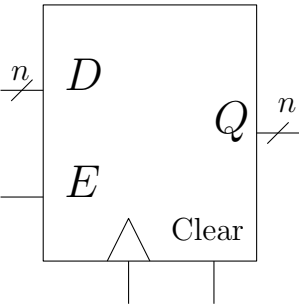


Registre  $n$  bits



$D_i$   $n$

$Adr$   $k$

RAM

Capacit :  $n * 2^k$  bits

Store

Load

$n$   $D_o$