# libCEED: A Case Study in Hidden Benefits of the Bridge Pattern

Jeremy L Thompson, Natalie Beams, Jed Brown, and Yohann Dudouit

> University of Colorado Boulder jeremy.thompson@colorado.edu

> > July 31, 2020

## libCEED Team

Developers: Ahmad Abdelfattah<sup>1</sup>, Valeria Barra<sup>2</sup>, Natalie Beams<sup>1</sup>, Jed Brown<sup>2</sup>, Jean-Sylvain Camier<sup>3</sup>, Veselin Dobrev<sup>3</sup>, Yohann Dudouit<sup>3</sup>, Leila Ghaffari<sup>2</sup>, Tzanio Kolev<sup>3</sup>, David Medina<sup>4</sup>, Thilina Rathnayake<sup>5</sup>, Jeremy L. Thompson<sup>2</sup>, & Stan Tomov<sup>5</sup>

Grant: Exascale Computing Project (17-SC-20-SC)

- 1: University of Tennesse
- 2: University of Colorado, Boulder
- 3: Lawrence Livermore National Laboratory
- 4: Occalytics LLC
- 5: University of Illinois at Urbana-Champaign



## Overview

libCEED is an extensible library that provides a portable algebraic interface and optimized implementations of high-order operators

We have optimized implementations for multiple architectures

Bridge design pattern offers performance portability as well as improved debugability and internal design documentation

(ロ) (部) (注) (注) (注) の(○)

## Overview

- Introduction
- libCEED Design
- Backend Development
- Future Work
- Questions

## Center for Efficient Exascale Discretizations

### DoE exascale co-design center

- Design discretization algorithms for exascale hardware that deliver significant performance gain over low order methods
- Collaborate with hardware vendors and software projects for exascale hardware and software stack
- Provide efficient and user-friendly unstructured PDE discretization component for exascale software ecosystem

















# libCEED Philosophy

libCEED provides purely algebraic interface for matrix-free evaluation of arbitrary polynomial order PDE operators

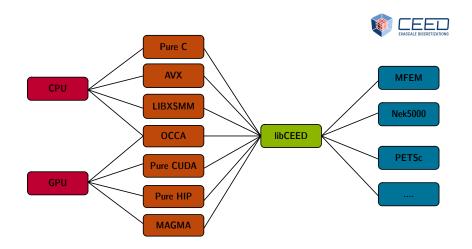
libCEED design approach:

- Optimized implementations for multiple architectures
- Runtime selection of backend implementation
- Single source user PDE quadrature point functions

Repository: https://github.com/CEED/libCEED



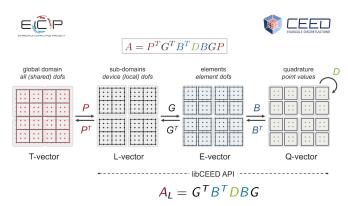
## libCEED Backends



Natural use case for bridge pattern



# <u>libCEED</u> Operator Decomposition



- G CeedElemRestriction, local gather/scatter
- B CeedBasis, provides basis operations such as interp and grad
- D CeedQFunction, representation of PDE at quadrature points
- A<sub>I</sub> CeedOperator, aggregation of Ceed objects for local action of operator

## User QFunction Source

#### User QFunction Code:

#### Compiled Assembly:

```
dv[k][i+1][i] -= wJ*(Fu[Fuviscidx[i][0]]*dXdxdXdxT[k][0] +
P804 ·
        c5 7d 28 d0
                                vmovapd %vmm0,%vmm10
                         Fu[Fuviscidx[j][1]] * dXdxdXdxT[k][1] +
b091:
                                vfmadd231pd %ymm11,%ymm7,%ymm10
       c4 42 c5 b8 d3
h096 ·
       c5 fd 28 84 24 c8 04 vmovapd 0x4c8(%rsp).%vmm0
       00 00
b09d:
   dv[k][j+1][i] -= wJ*(Fu[Fuviscidx[j][0]]*dXdxdXdxT[k][0] +
                               vfnmadd213pd (%rdi,%rax,1),%ymm1,%ymm10
h09f ·
        c4 62 f5 ac 14 07
b0a5:
       c5 7d 11 14 07
                                vmovupd %ymm10,(%rdi,%rax,1)
                         Fu[Fuviscidx[j][1]] * dXdxdXdxT[k][1] +
b0aa:
       c5 7d 59 94 24 68 04 vmulpd 0x468(%rsp),%vmm0,%vmm10
h0h1 ·
        00 00
```

### Similar optimized code on other architectures

## End Goal: Optimized Backends

#### CPU:

- AVX instructions for vector operations, FMA
- LIBXSMM for JiT optimized small matrix multiplication

#### GPU:

- JiT to generate device kernels from user code
- Fused kernels to minimize launches and data movement

# Development Challenges

#### CPU:

- Optimized code can be difficult to read
- External library calls can be opaque

#### GPU:

- Debugging more challenging on devices
- Fused kernels can't be incrementally tested

# Bridge Pattern FTW

Strong encapsulation of backend implementation details from interface

- Reference backends provide straightforward implementation
  - /cpu/self/ref/serial
  - /gpu/cuda/ref
  - /gpu/hip/ref
- Debugging backends provide additional debugging tools
  - /cpu/self/memcheck
- Optimized backends selectively re-implement objects
  - /cpu/self/xsmm/blocked
  - /gpu/cuda/gen



## Hidden Benefits

- Stable API and ABI across implementations
- Flexible and distributed development
- Clear reference code for new developers
- Improved debugability ,for users and developers
- ...

### Future Work

- Further performance enhancements (GPU and CPU)
- Improved mixed mesh and operator composition support
- Expanded non-linear and multi-physics examples
- Preconditioning based on libCEED operator decomposition
- Algorithmic differentiation of user quadrature functions
- We invite contributors and friendly users



## Questions?

Advisors: Jed Brown<sup>1</sup> & Adriana Gillman<sup>1</sup>

Developers: Ahmad Abdelfattah<sup>1</sup>, Valeria Barra<sup>2</sup>, Natalie Beams<sup>1</sup>, Jed Brown<sup>2</sup>, Jean-Sylvain Camier<sup>3</sup>, Veselin Dobrev<sup>3</sup>, Yohann Dudouit<sup>3</sup>, Leila Ghaffari<sup>2</sup>, Tzanio Kolev<sup>3</sup>, David Medina<sup>4</sup>, Thilina Rathnayake<sup>5</sup>, & Stan Tomov<sup>5</sup>

Grant: Exascale Computing Project (17-SC-20-SC)

- 1: University of Tennesse
- 2: University of Colorado, Boulder
- 3: Lawrence Livermore National Laboratory
- 4: Occalytics LLC
- 5: University of Illinois at Urbana-Champaign

# libCEED: A Case Study in Hidden Benefits of the Bridge Pattern

Jeremy L Thompson, Natalie Beams, Jed Brown, and Yohann Dudouit

> University of Colorado Boulder jeremy.thompson@colorado.edu

> > July 31, 2020

