

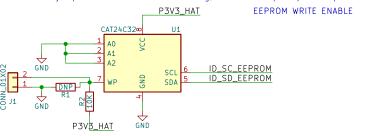
This is based on the official Raspberry Pi spec to be able to call an extension board a HAT. https://github.com/raspberrypi/hats/blob/master/designquide.md

40-Pin HAT Connector

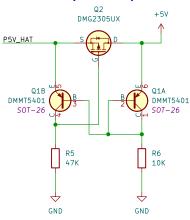
P3V3_HAT	1	40HAT P3V3	J2 P5V	1 2	P5V_HAT
	× 3	BCM2	P5V	4	P5V_HAT
	× 5	BCM2 BCM3	GND	6	GND
	× 7	BCM3 BCM4	BCM14	8	TXD
GND	9	GND	BCM14 BCM15	10	RXD
	×11 ×13 ×15 ×17	BCM17	BCM13	12 ×	
	213	BCM17 BCM27	GND	14	GND
	<u></u>	BCM27 BCM22	BCM23	16 ×	
	217	P3V3	BCM23	18 🗘	
	×19 ×21 ×23 ×25		GND	18 ×	GND
	21	BCM10		22 🗸	
	23	BCM9	BCM25	22 × 24 ×	
GND	25	BCM11	BCM8	26	
ID_SD_EEPROM	27	GND	BCM7	26 × 28	ID_SC_EEPROM
DTR	29	ВСМ0	BCM1	30	GND
RTS	31	BCM5 BCM6	GND BCM12	32 ×	
	v 33			34	GND
	× 33 × 35	BCM13	GND		
	× 37	BCM19	BCM16	36 × 38 ×	
GND	39	BCM26	BCM20	40 ×	
		GND	BCM21	X	

HAT EEPROM

The HAT spec requires this EEPROM with system information to be in place in order to be called a HAT. It should be set up as write protected (WP pin held high), so it may be desirable to either put a jumper as shown to enable writing, or to hook up a spare IO pin to do so.

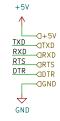


5V back power protection

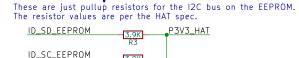


Mounting Holes

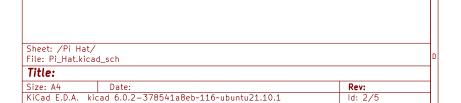


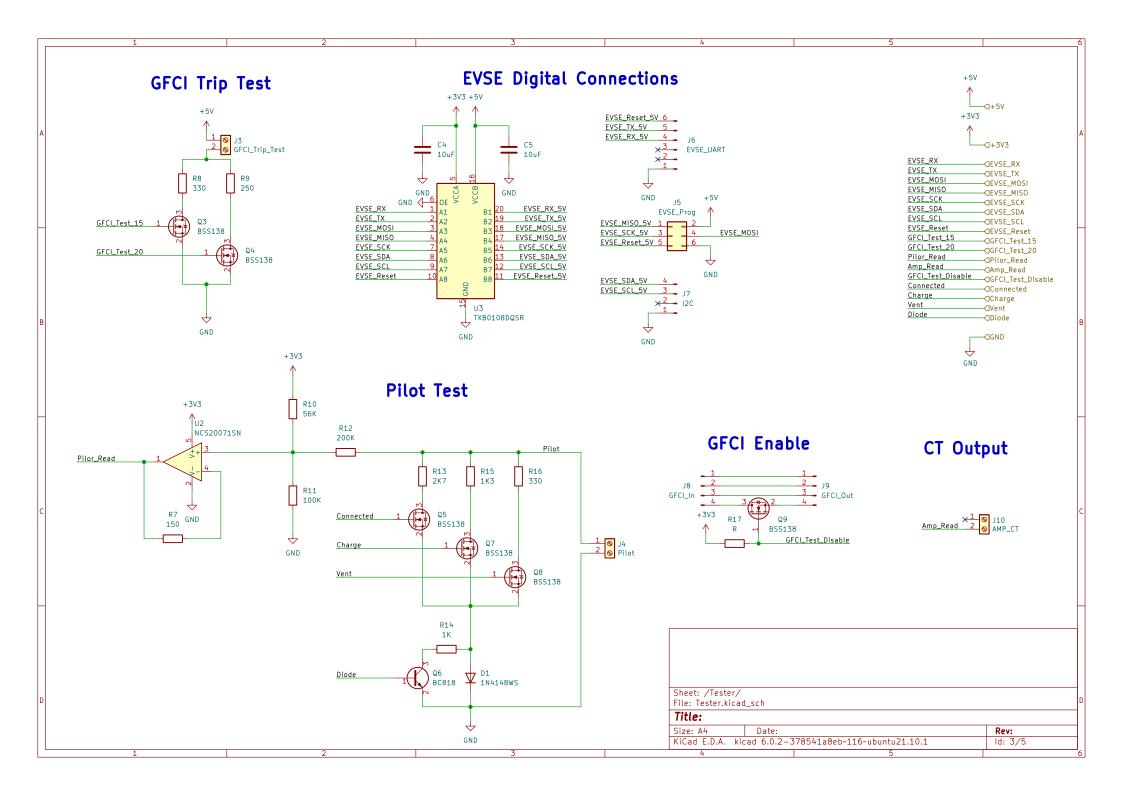


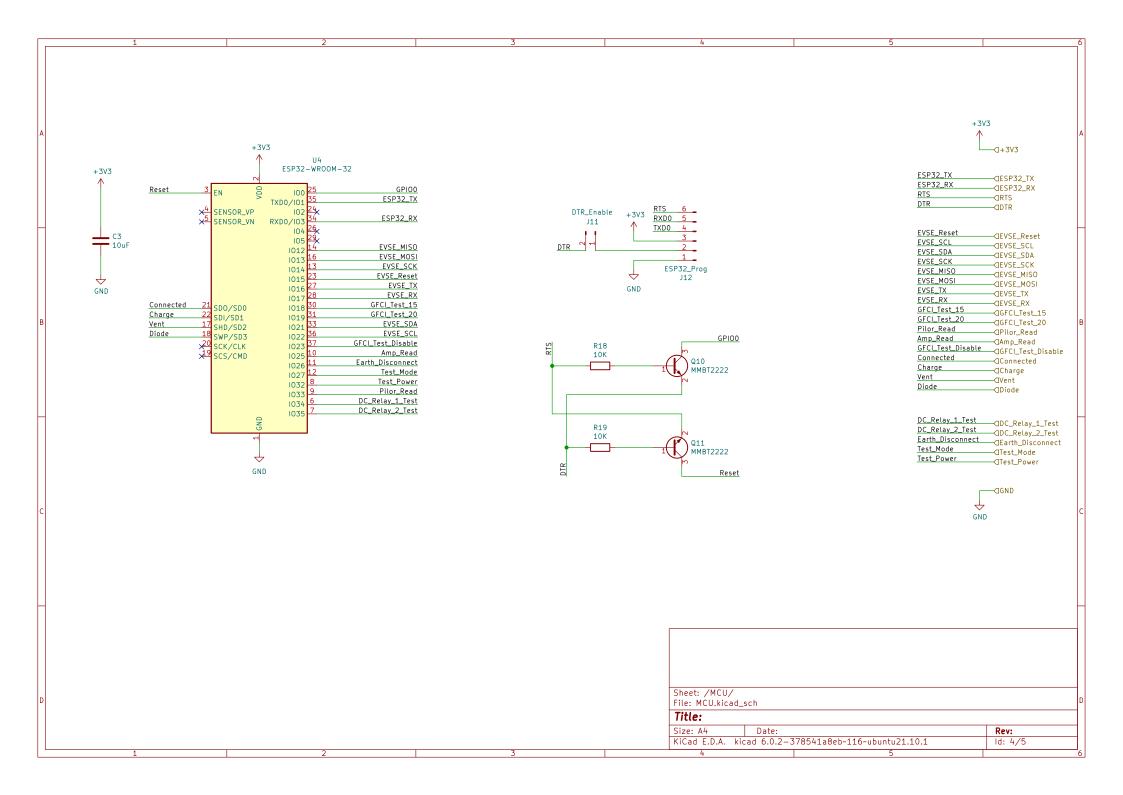
Pullup Resistors







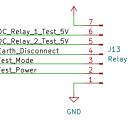




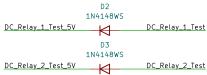
Level shift Inputs

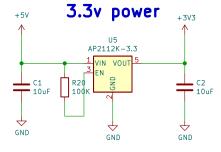
Relay Board Connector













DC_Relay_1_Test
DC_Relay_2_Test
Earth_Disconnect

DC_Relay_2_Test
ODC_Relay_2_Test
GEarth_Disconnect Test_Mode -⊲Test_Mode Test_Power —⊲Test_Power

> ⊸GND GND

Sheet: /Relay Board/ File: Relay_Board.kicad_sch

Title: Date:

Size: A4 KiCad E.D.A. kicad 6.0.2-378541a8eb~116~ubuntu21.10.1 Id: 5/5