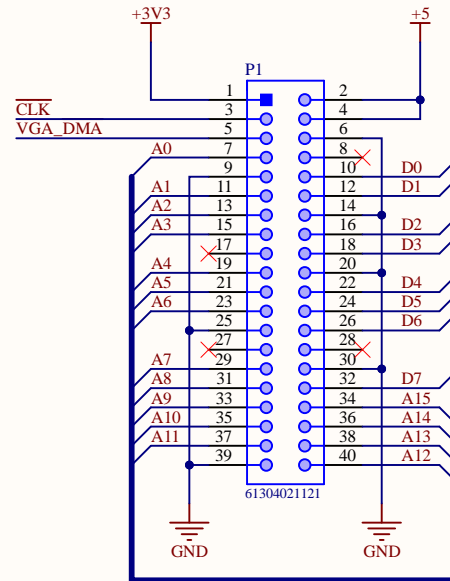
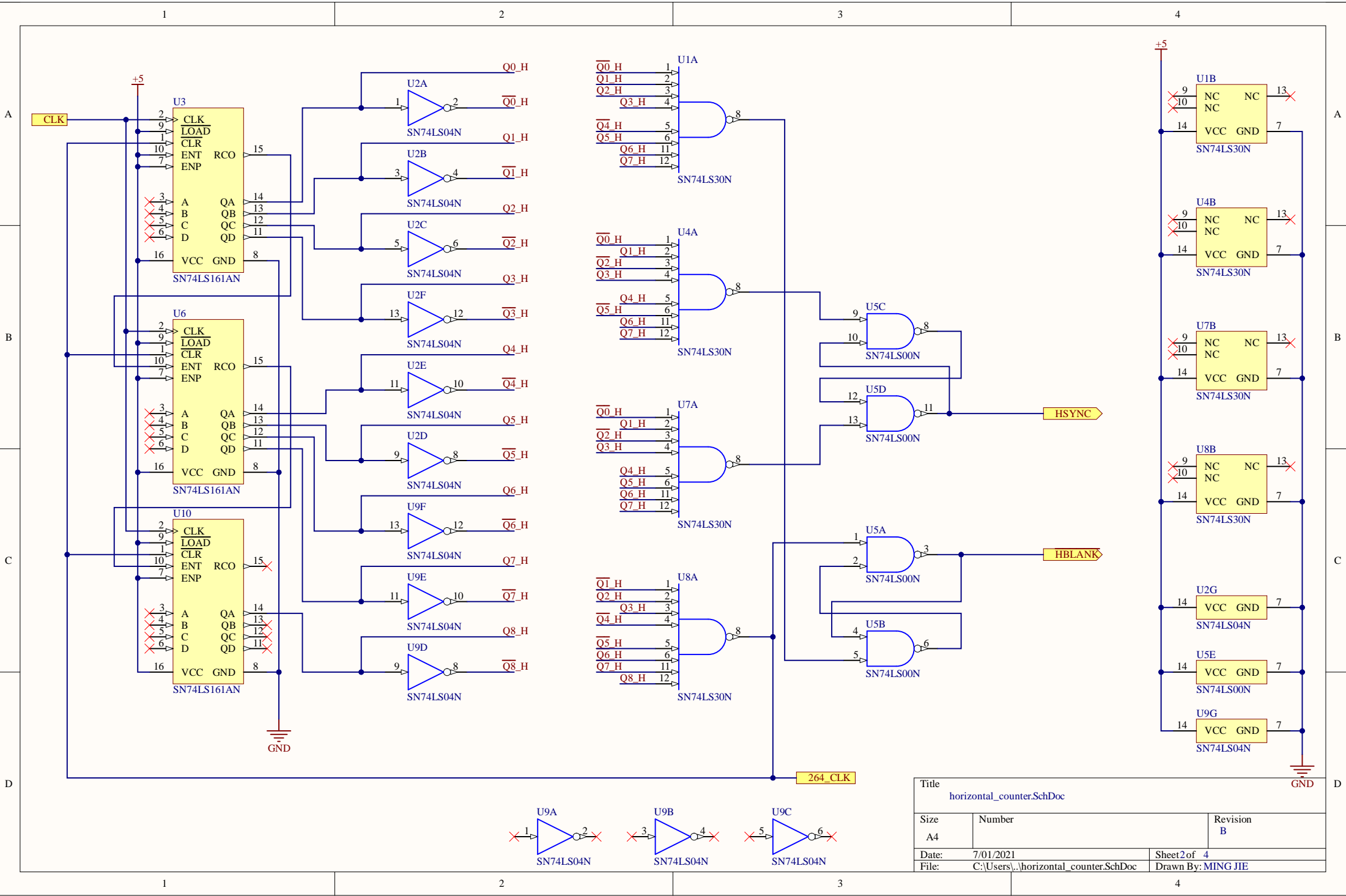


VGA BACKPLANE

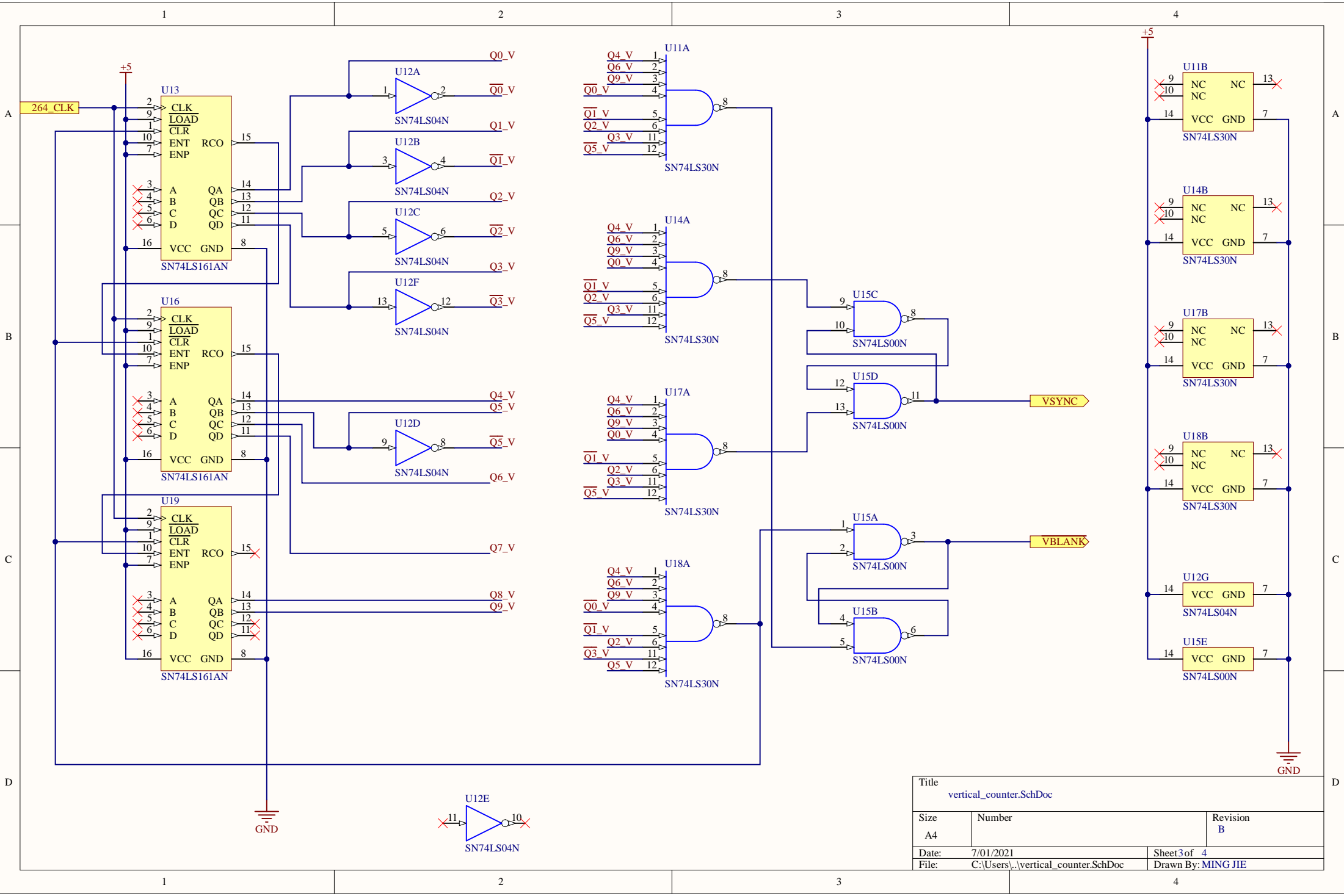
Compatible with RPI GPIO
Use RPI to probe address and data bus, be careful with 5V logic levels

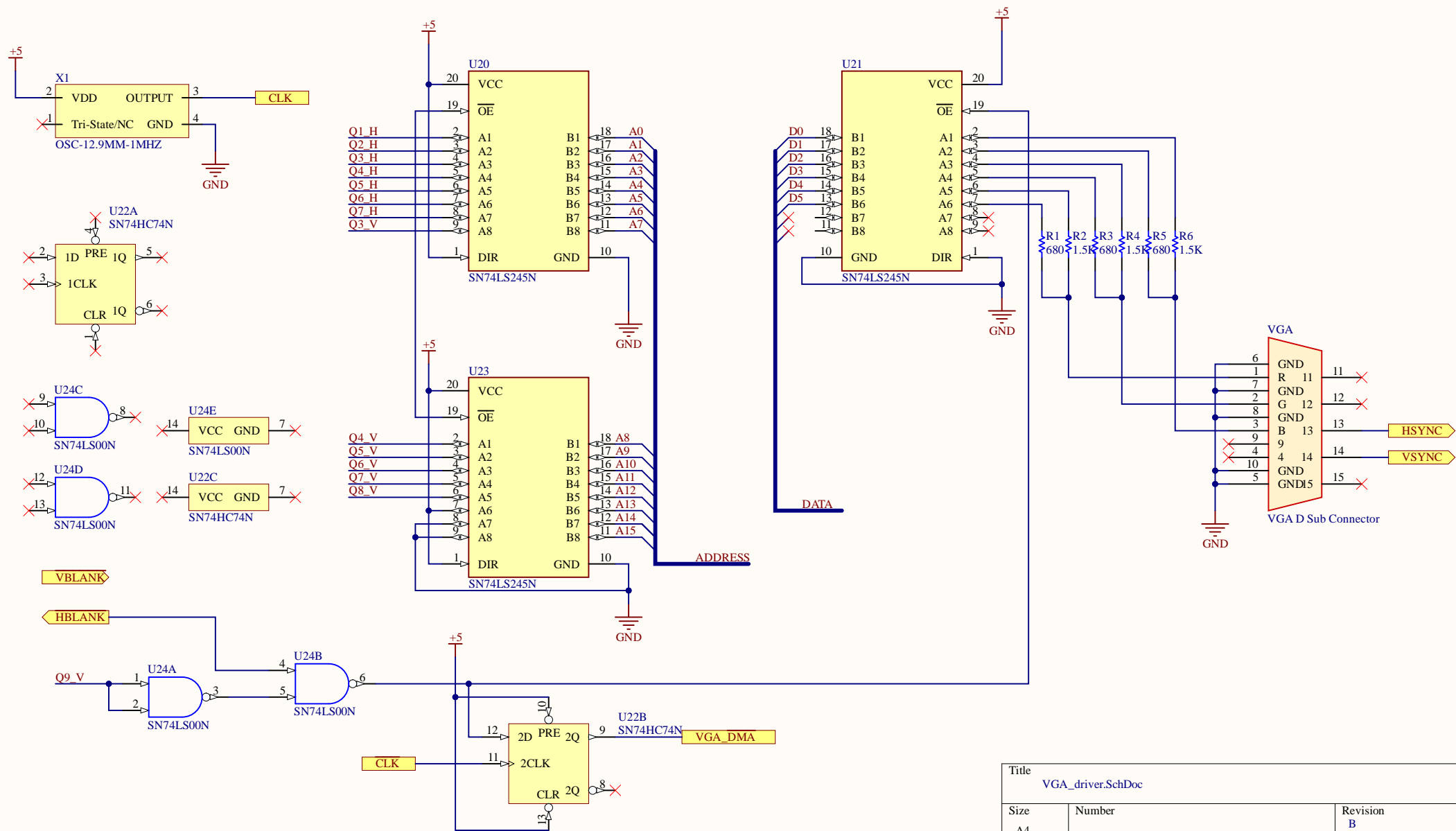


Title 6502_connection.SchDoc			
Size A4	Number		Revision B
Date:	7/01/2021	Sheet 1 of	4
File:	C:\Users\...\6502_connection.SchDoc	Drawn By: MING JIE	

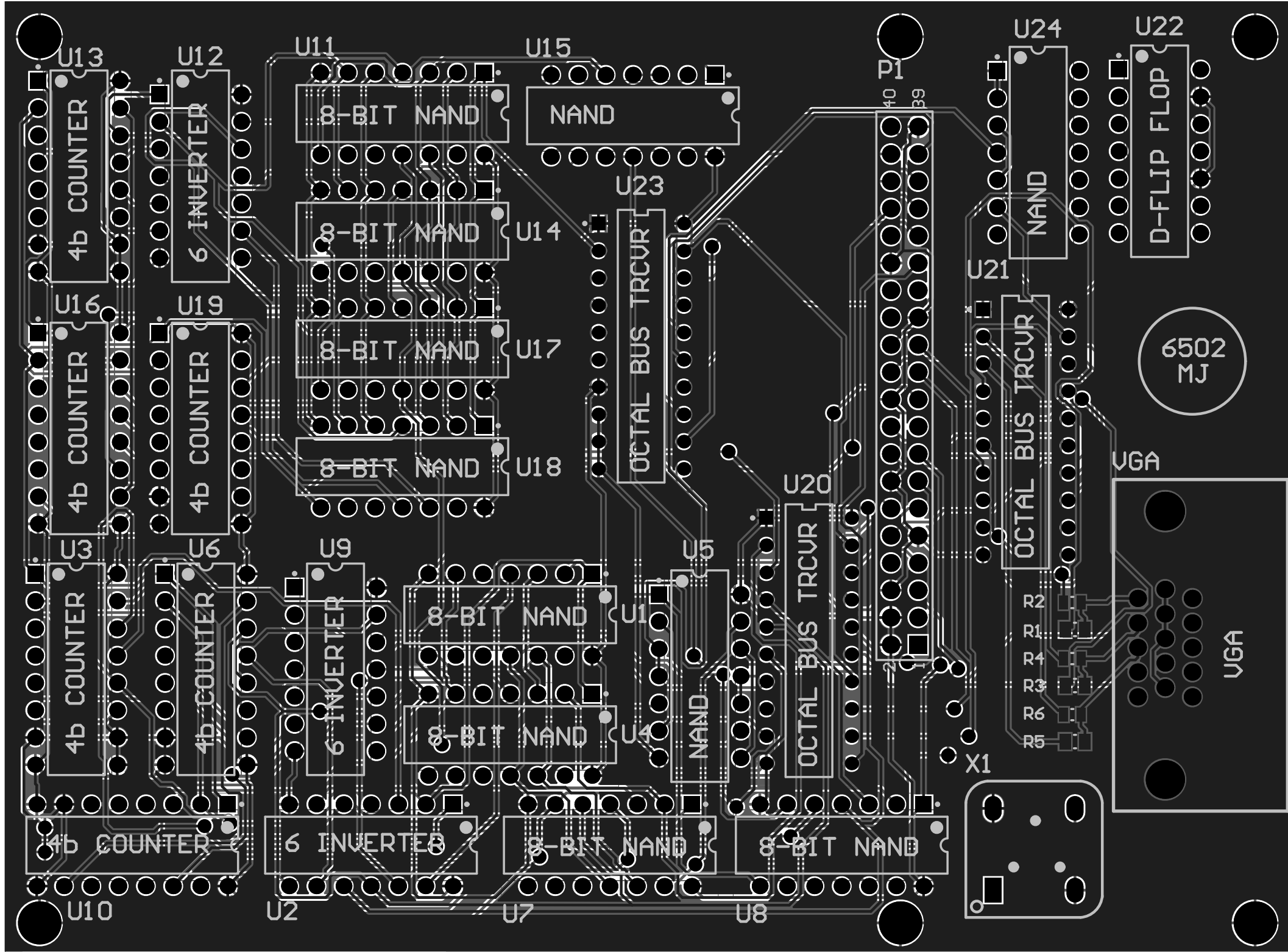


Title		
horizontal_counter.SchDoc		
Size	Number	Revision
A4		B
Date:	7/01/2021	Sheet2 of 4
File:	C:\Users\...\horizontal_counter.SchDoc	Drawn By: MING JIE





Title VGA_driver.SchDoc		
Size A4	Number	Revision B
Date:	7/01/2021	Sheet 4 of 4
File:	C:\Users\...\VGA_driver.SchDoc	Drawn By: MING JIE



Comment	Description	Designator	Footprint	LibRef	Quantity
61304021121	THT Vertical Pin Header WR-PHD, Pitch 2.54 mm, Dual Row, 40 pins	P1	61304021121	CMP-1502-01135-1	1
CRCW0805680RFKEA		R1, R3, R5	RESC2013X50X30NL20T20	CMP-2001-04066-1	3
RC0805FR-071K5L	RES 1.50K OHM 1/8W 1% 0805 SMD	R2, R4, R6	RESC2013X60X35LL10T20	CMP-1659-00010-2	3
SN74LS30N	8-input positive-NAND gates, N0014A, TUBE	U1, U4, U7, U8, U11, U14, U17, U18	N0014A	CMP-1627-00459-2	8
SN74LS04N	Hex inverters, N0014A, TUBE	U2, U9, U12	N0014A	CMP-1615-00911-2	3
SN74LS161AN	Synchronous 4-Bit Binary Counters, N0016A, TUBE	U3, U6, U10, U13, U16, U19	N0016A	CMP-1623-00512-2	6
SN74LS00N	Quad 2-input positive-NAND gates, N0014A, TUBE	U5, U15, U24	N0014A	CMP-1627-00476-2	3
SN74LS245N	IC TXRX NON-INVERT 5.25V 20DIP	U20, U21, U23	FP-N0020A-MFG	CMP-1620-00419-3	3
SN74HC74N	Dual D-Type Positive-Edge-Triggered Flip-Flops With Clear and Preset, N0014A, TUBE	U22	N0014A	CMP-1616-00096-3	1
VGA D Sub Connector	VGA D Sub Connector	VGA	VGA	VGA-002	1
OSC-12.9MM-1MHZ	12.9MM x 12.9MM, 1MHz Can Oscillator	X1	OSC_THT_12-9MM	OSC-12.9MM-1MHZ	1