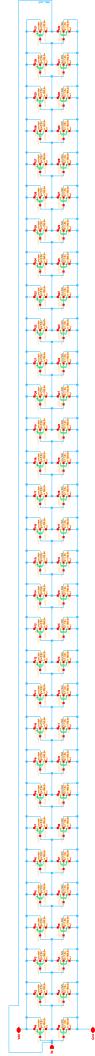
Name of the circuit/cell

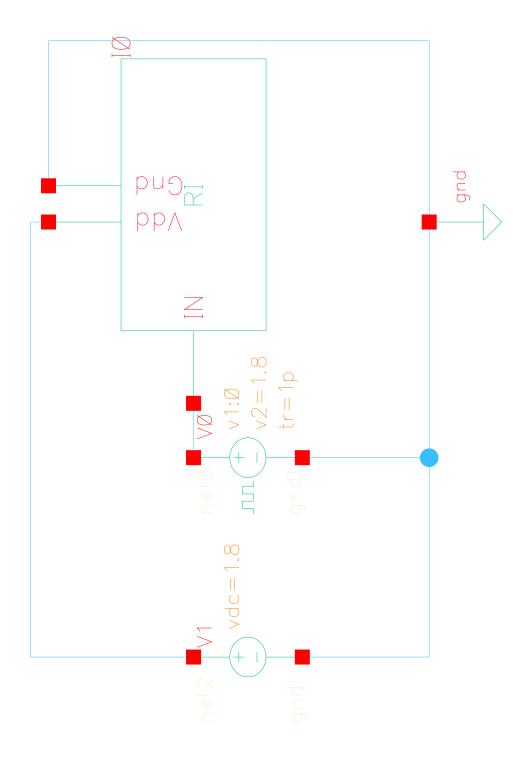
RING\_OSC by Jeremy Shaw, CpE151

Well Labeled and Clear Schematic



Jeremy Shaw CpE151

Well Labeled and Clear Test Bench Schematic

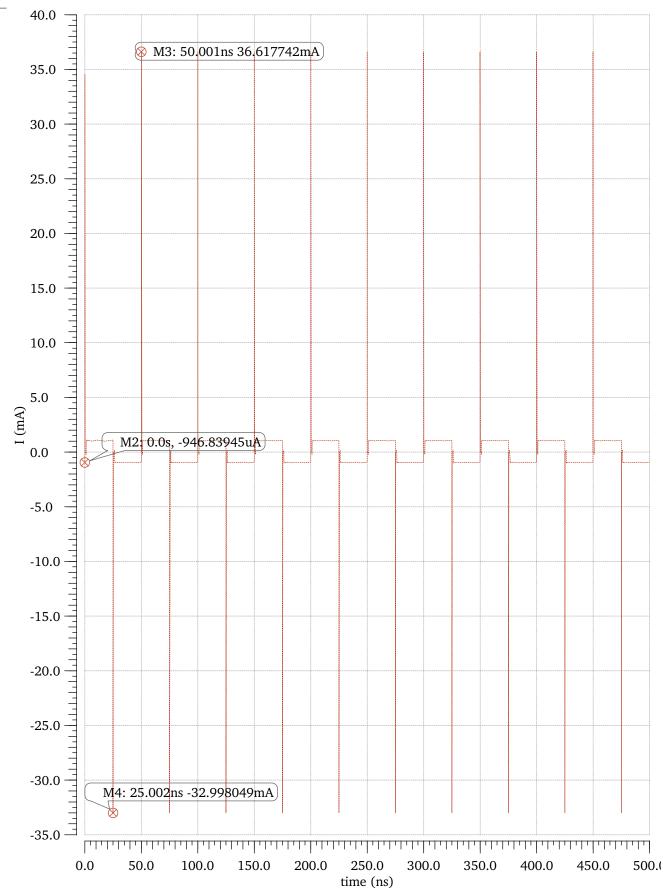


Correct Pre-Layout Simulation Results with Markers

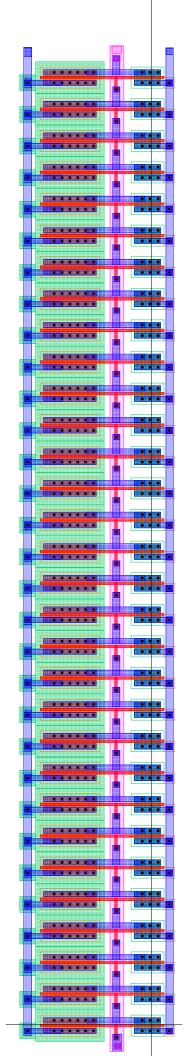
### **Transient Response**

### Sun Oct 13 20:07:35 2019

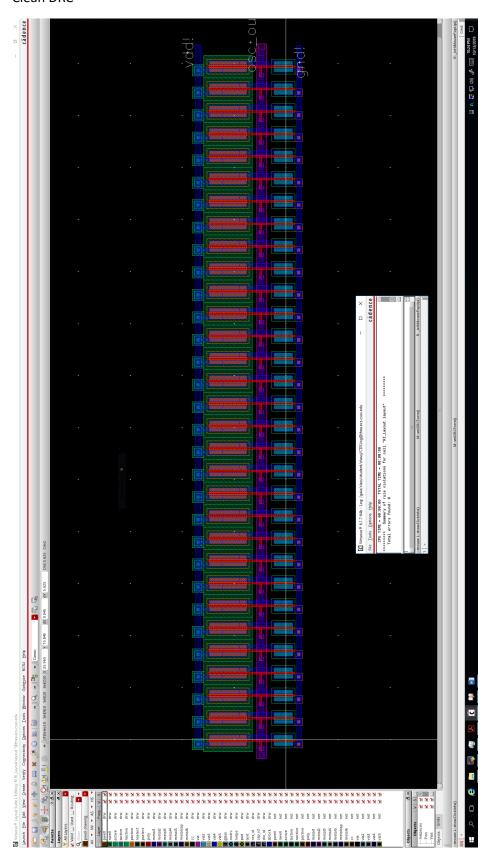




Printed to PDF Layout



# Clean DRC



### @(#)\$CDS: LVS version 6.1.7-64b 09/27/2016 19:41 (sjfhw305) \$

 $Command\ line: /software/cadence/installs/IC617/tools.lnx86/dfII/bin/64bit/LVS\ -dir/gaia/class/student/shawj/151/0start/LVS\ -l\ -s\ -t\ /gaia/class/student/shawj/151/0start/LVS/layout/gaia/class/student/shawj/151/0start/LVS/schematic$ 

Like matching is enabled.

Net swapping is enabled.

Using terminal names as correspondence points.

 $Net-list\ summary\ for\ /gaia/class/student/shawj/151/0start/LVS/layout/netlist$ 

count	
33	nets
3	terminals
31	pmos
31	nmos

Net-list summary for /gaia/class/student/shawj/151/0start/LVS/schematic/netlist

count	
33	nets
3	terminals
31	pmos
31	nmos

Terminal correspondence points

N30	N0	gnd!
N32	N20	osc_out
N31	N1	vdd!

Devices in the netlist but not in the rules: pmos nmos

The net-lists match.

un-matched rewired size errors pruned active total	layout schematic instances  0 0 0 0 0 0 0 0 0 0 62 62 62 62
un-matched merged pruned active total	nets 0 0 0 0 0 0 33 33 33 33
un-matched matched but	terminals 0 0

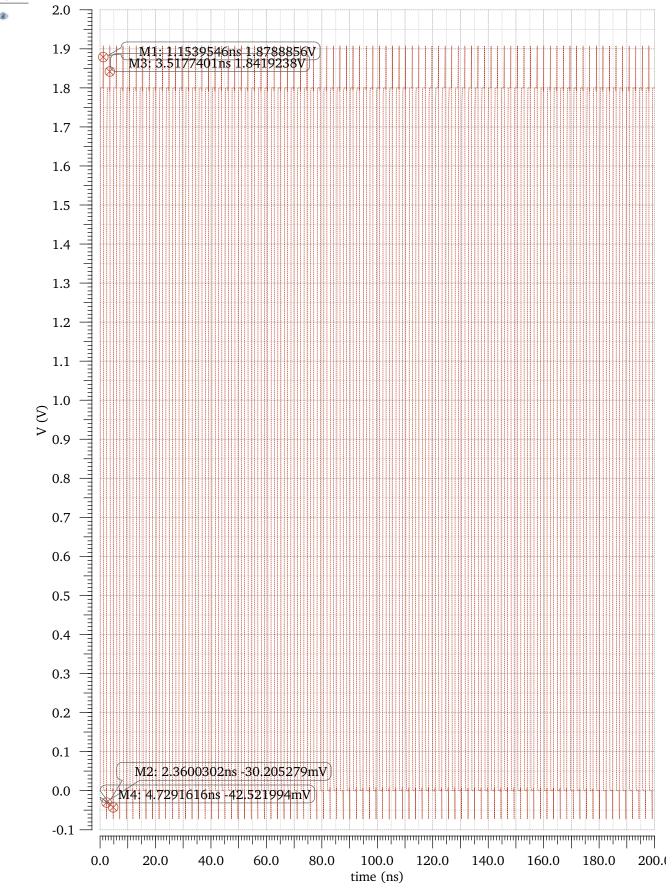
different type 1 1 total 3 3
Probe files from /gaia/class/student/shawj/151/0start/LVS/schematic
devbad.out:
netbad.out:
mergenet.out:
termbad.out: ? Terminal osc_out's type in the schematic: output, in the layout: inputOutput
prunenet.out:
prunedev.out:
audit.out:
Probe files from /gaia/class/student/shawj/151/0start/LVS/layout
devbad.out:
netbad.out:
mergenet.out:
termbad.out: ? Terminal osc_out's type in the layout: inputOutput, in the schematic: output
prunenet.out:
prunedev.out:
audit.out:

Correct Post-Layout Simulation results with Markers

### **Transient Response**

#### Sun Oct 13 21:58:05 2019





## **Transient Response**

#### Sun Oct 13 21:50:48 2019



