

Mirror Adder & Extra Credit

CpE 151

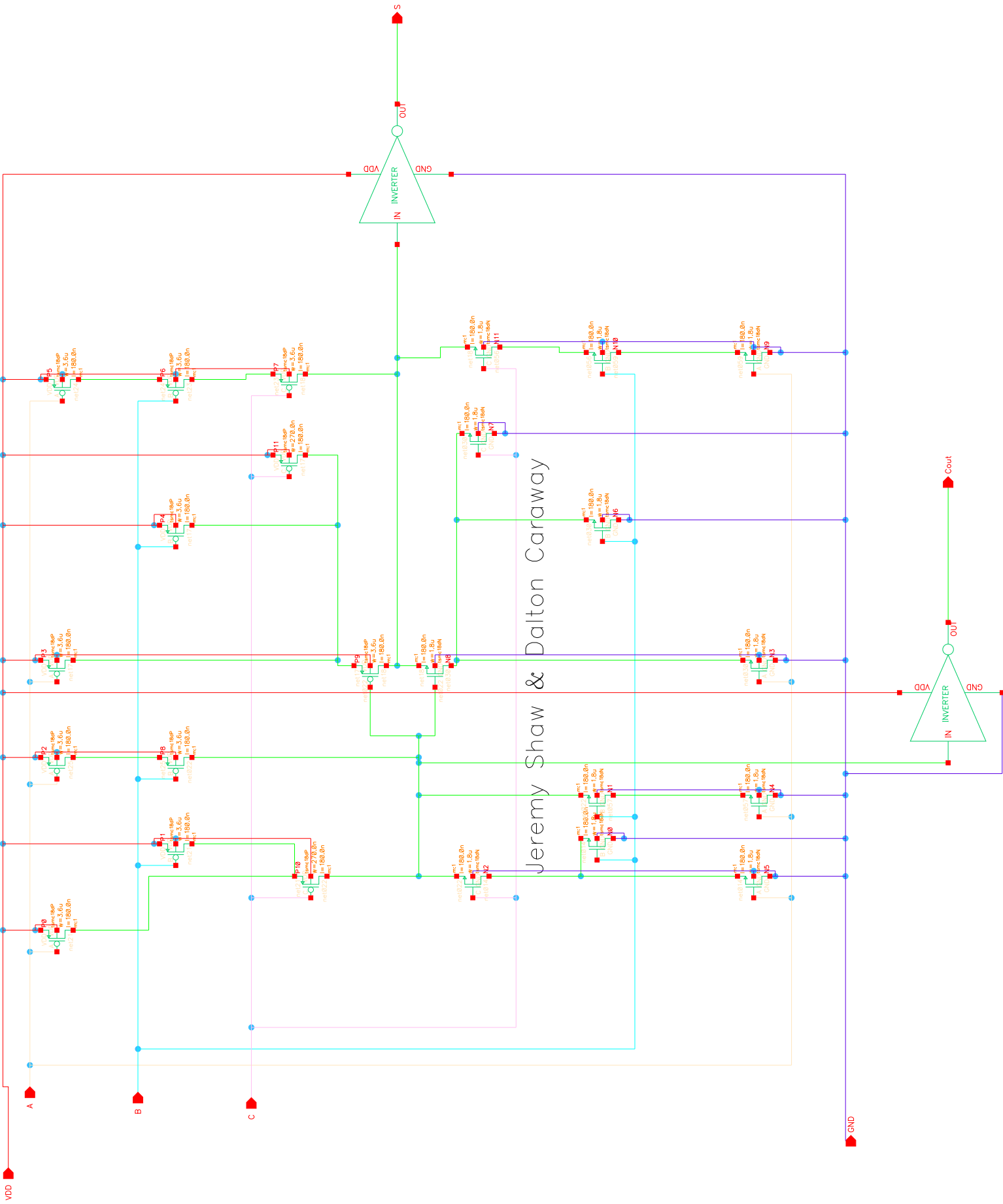
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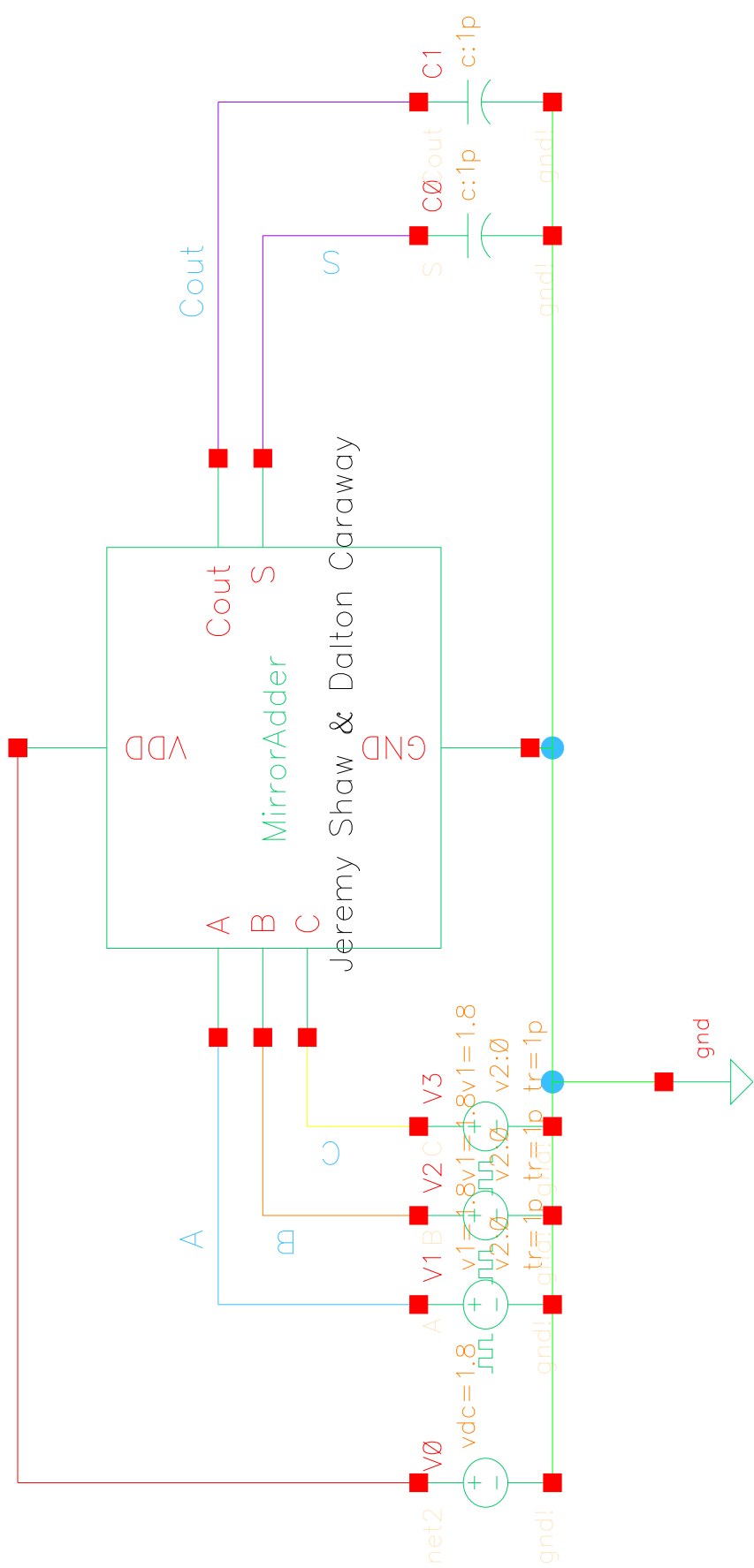
21 NOV 19

Mirror Adder

Schematic, Testbench Schematic, Testbench Output, & **Layout**

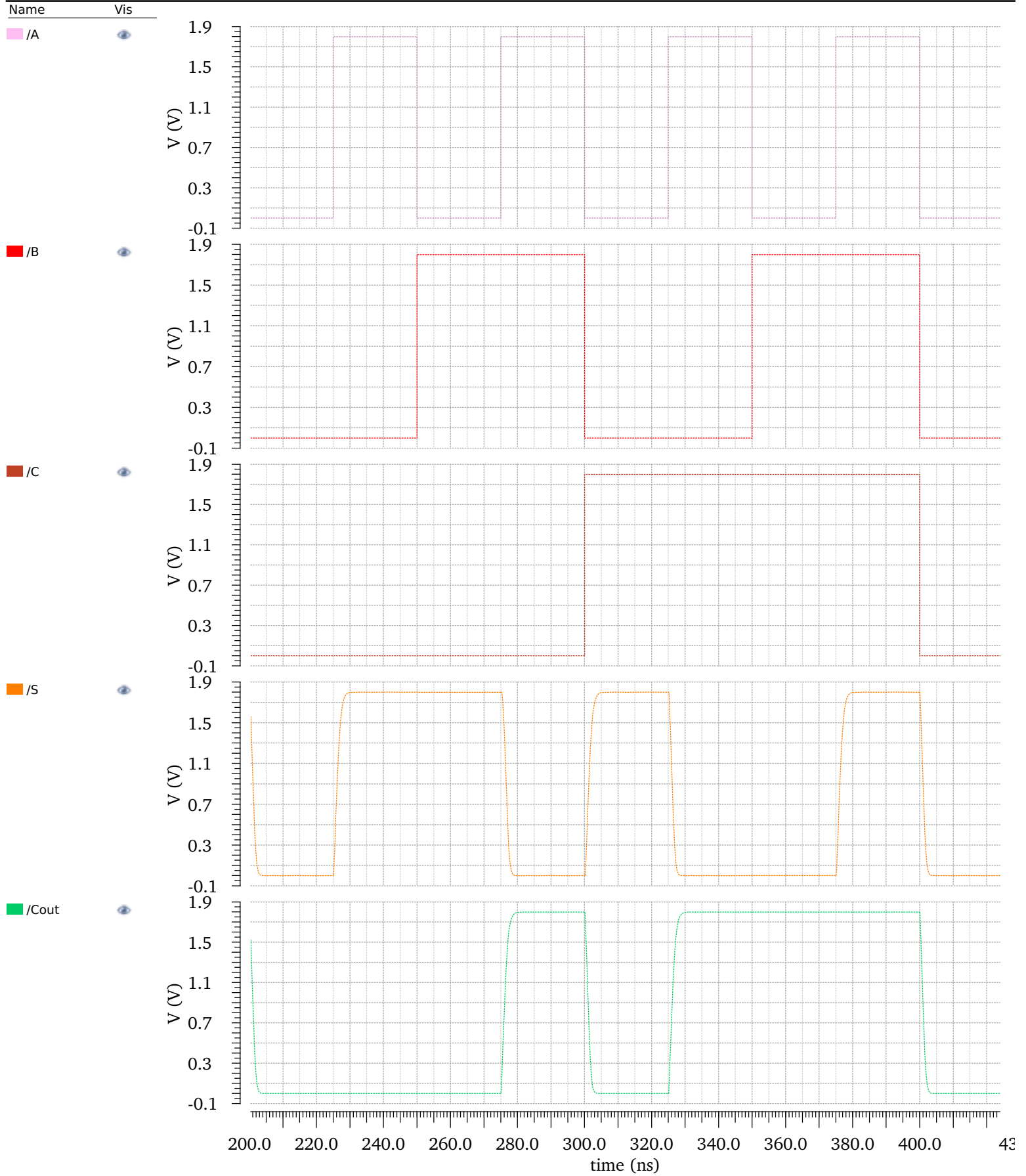
A	B	C	Cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1





Transient Response

Wed Nov 20 20:20:23 2019



cout

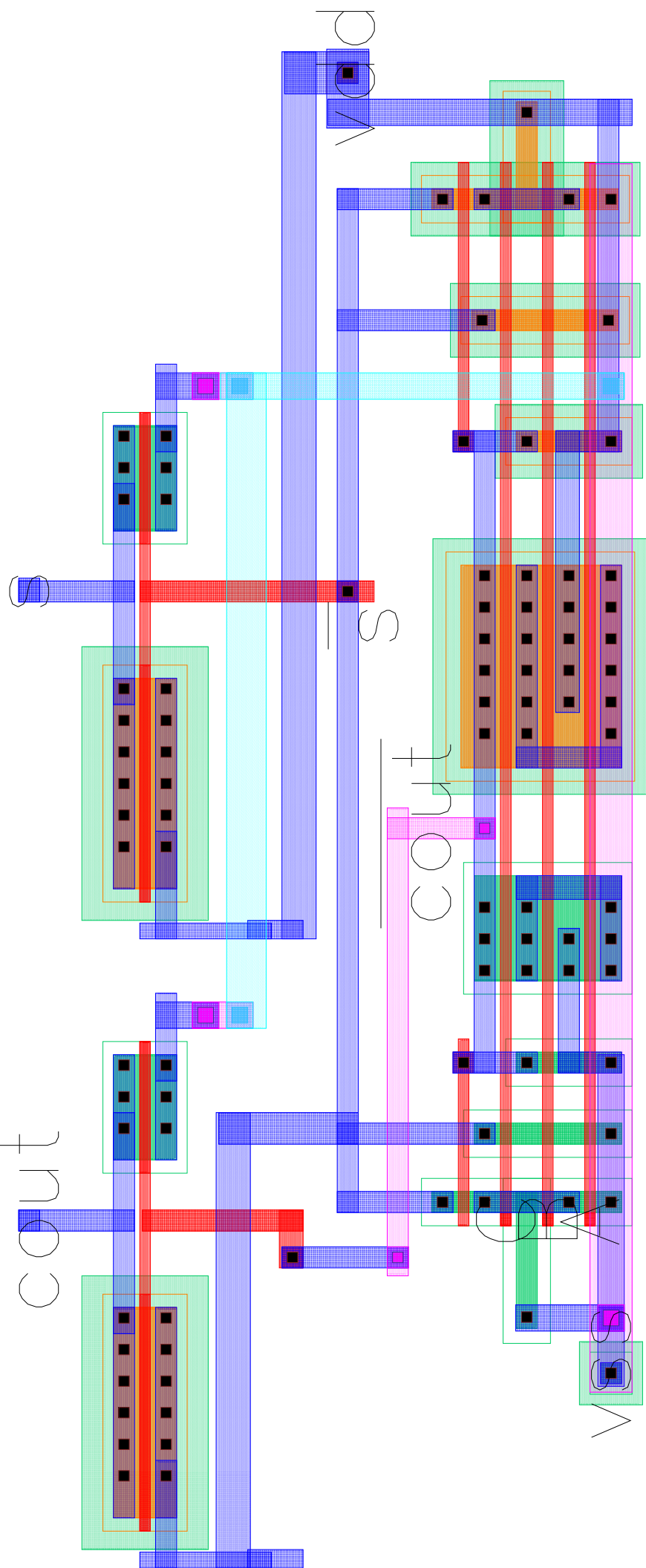
S

cout

cout

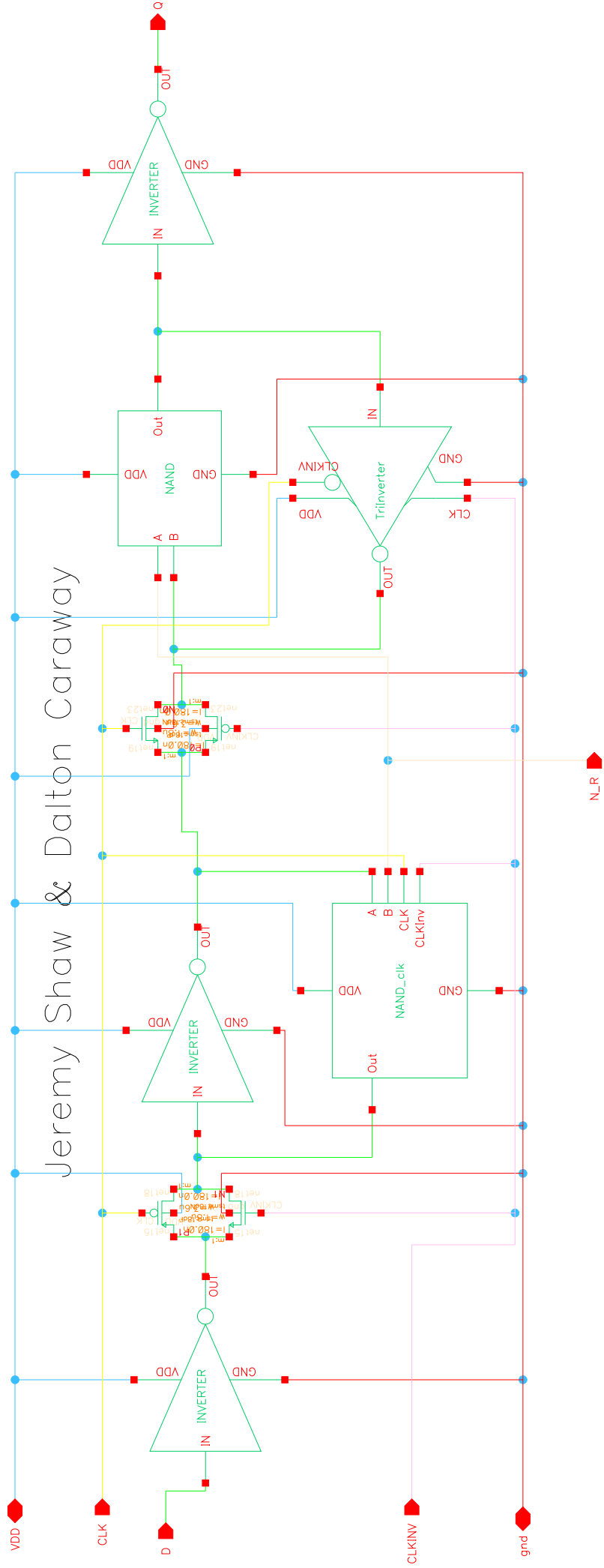
cout

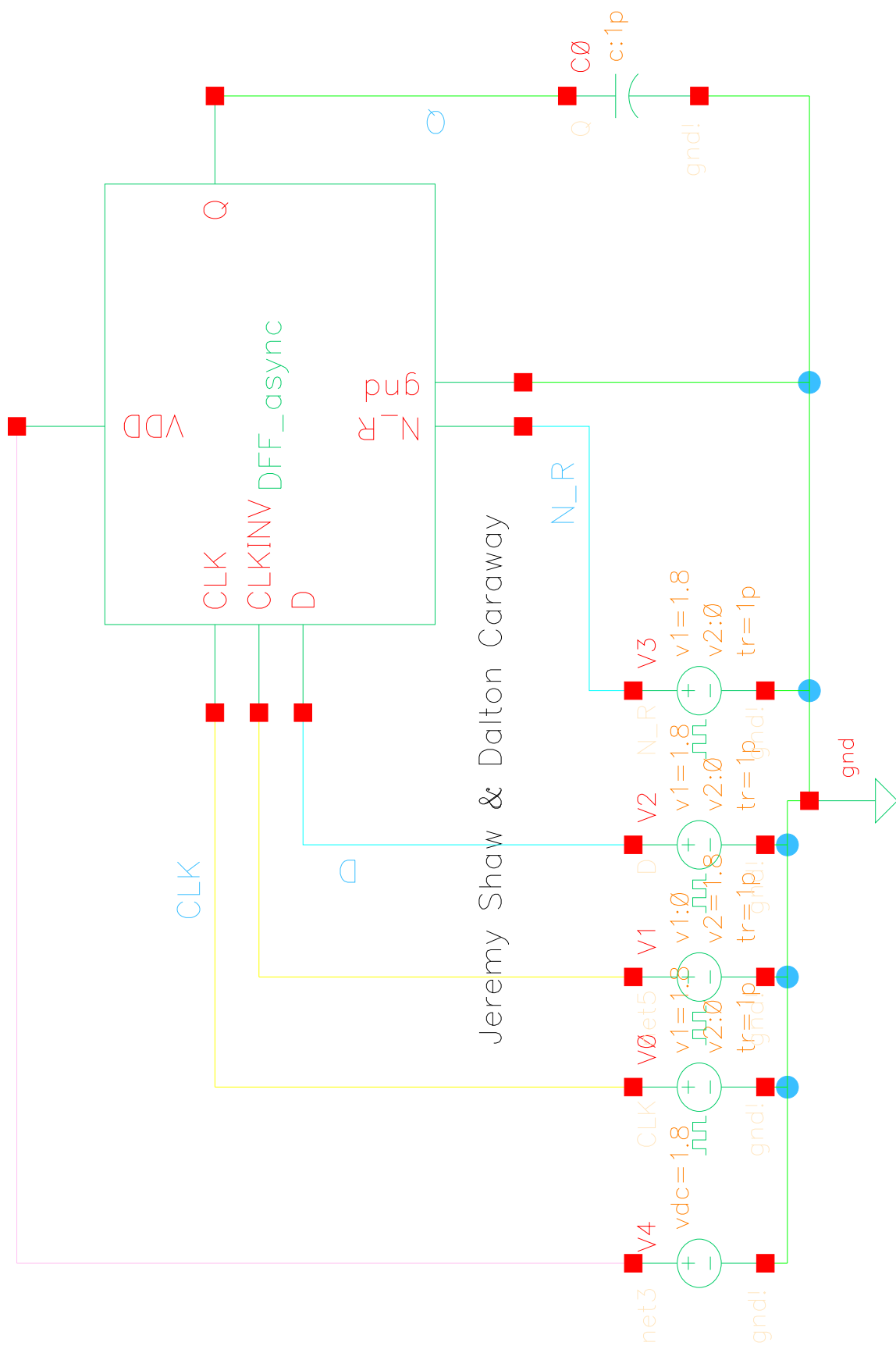
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DFF with Asynchronous Reset

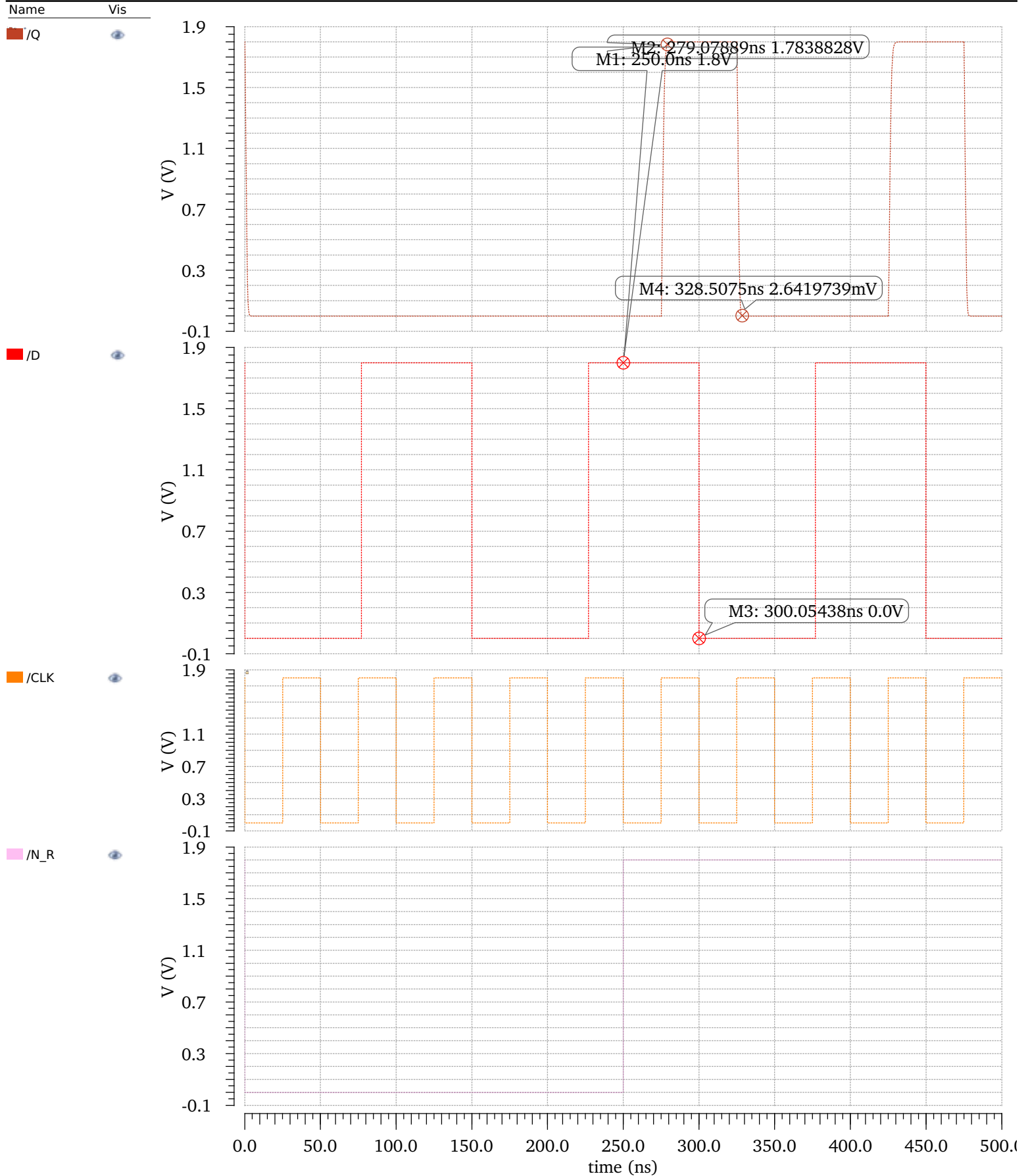
Schematic, Testbench Schematic & Testbench Output





Transient Response

Thu Nov 21 00:43:42 2019



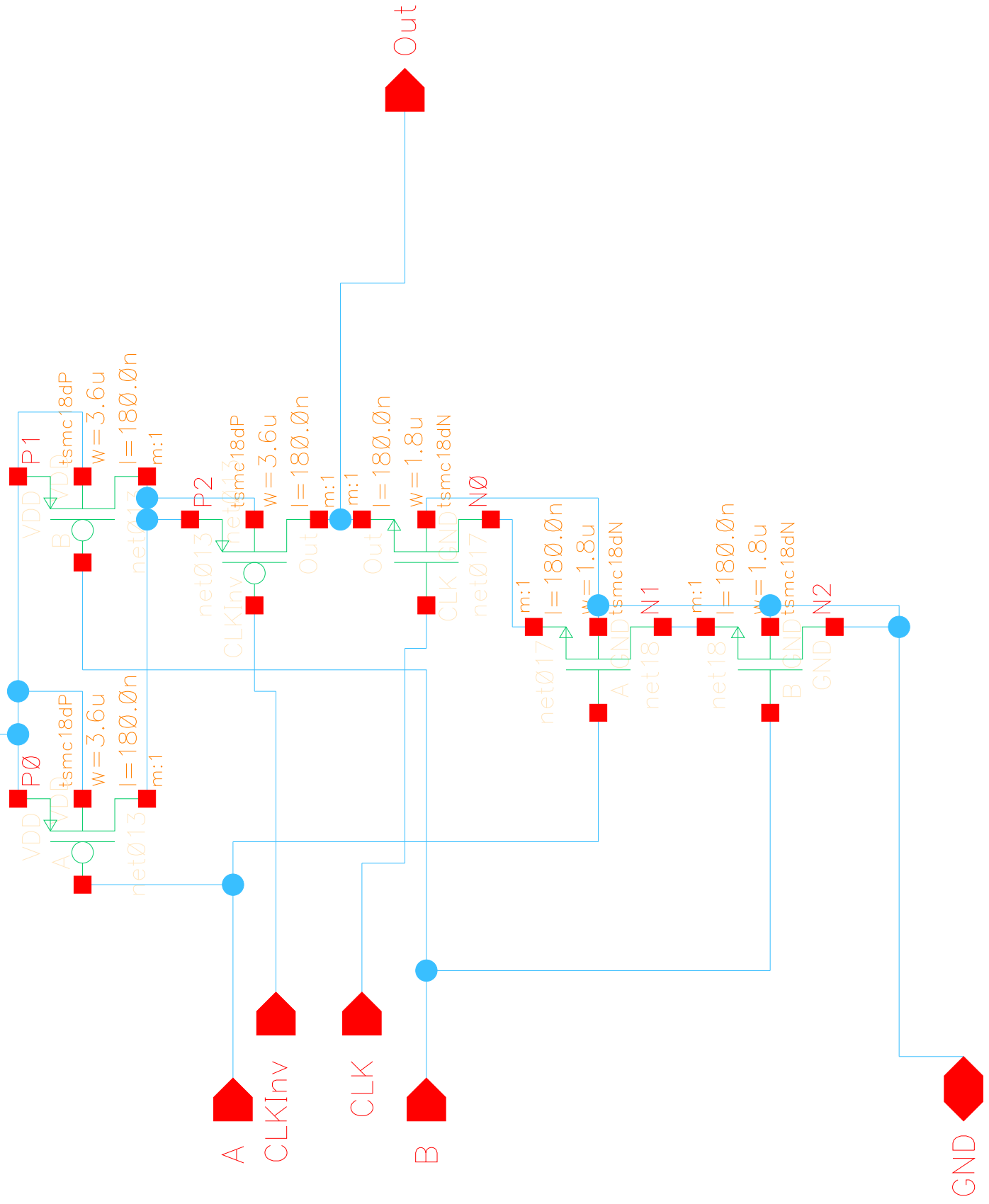
NAND2, clocked.

Schematic, Testbench Schematic & Testbench Output

A	B	Out
0	0	1
0	1	1
1	0	1
1	1	0

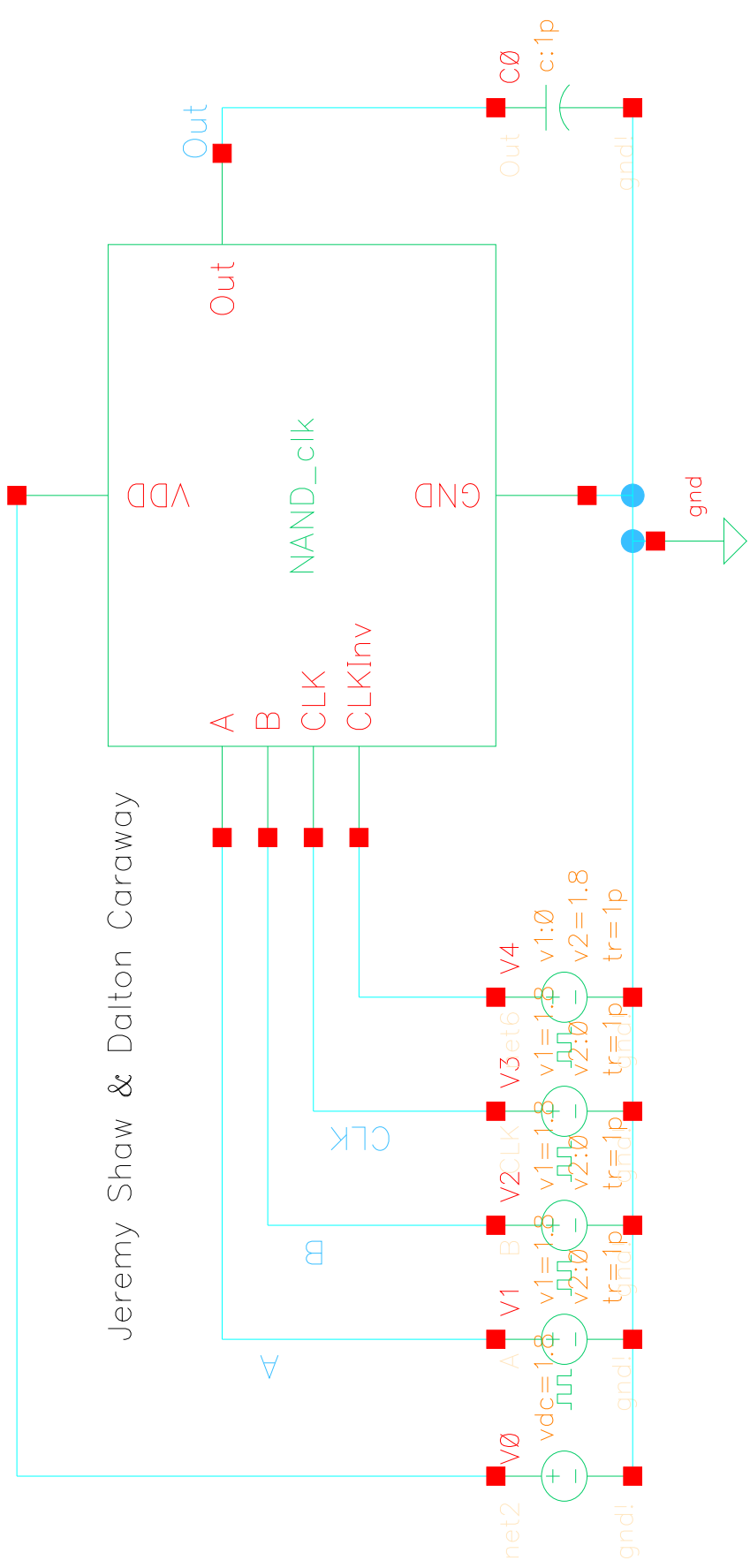
VDD

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GND

Jeremy Shaw & Dalton Caraway



Transient Response**Thu Nov 21 00:54:47 2019**