FY - OPTO_BOARD_2_MISC_PG5

OPTO_BOARD_2_PG1
PORTBbits.RA0 = ETHERNET LED1
PORTBbits.RA1 = ETHERNET LED2
PORTBbits.RA2 = ADCONVERTER ENC TOP
PORTBbits.RA3 = ADCONVERTER ENC BOTTOM
PORTBbits.RA4 = Led1
PORTBbits.RA5 = Led2
3.3V, 2mA

PORTBbits.RB0 = SPARE_SWITCH_INPUT_TOP
PORTBbits.RB1 = SPARE_SWITCH_INPUT_BOTTOM
PORTBbits.RB2 = Led3
PORTBbits.RB3 = IO_Expander_Enable I^2C
PORTBbits.RB4 = Output_Enable
PORTBbits.RB5 = Externall_WDT
PORTBbits.RB6 = INCIRCUIT PROGRAM/DEBUG
PORTBbits.RB7 = INCIRCUIT PROGRAM/DEBUG
5.0V, 25mA

PORTDbits.RD0 = M10_ Top
PORTDbits.RD1 = Track 7 enable
PORTDbits.RD2 = Bezet_Weerstand_ Top
PORTDbits.RD3 = M10_ Bottom
PORTDbits.RD4 = Track 18 enable
PORTDbits.RD5 = SERIAL CLOCK I^2C
PORTDbits.RD6 = SERIAL DATA I^2C
PORTDbits.RD7 = Bezet_Weerstand_ Bottom
5.0V, 8mA

FY - OPTO_BOARD_1_SWBOT_PG4

OPTO_BOARD_1_BOT_PG1
PORTCbits.RC0 = Brake Bottom
PORTCbits.RC1 = SPARE1
PORTCbits.RC2 = PWM Bottom
PORTCbits.RC3 = F13_ Bottom
PORTCbits.RC4 = F10_ Bottom
PORTCbits.RC5 = F11_ Bottom
PORTCbits.RC6 = F12_ Bottom
PORTCbits.RC7 = EOS_10_ Bottom
5.0V, 25mA

PORTFbits.RF0 = CL_10_ bit1 Bottom
PORTFbits.RF1 = CL_10_ bit2 Bottom
PORTFbits.RF2 = CL_10_ bit3 Bottom
PORTFbits.RF3 = CL_10_ bit4 Bottom
PORTFbits.RF4 = CL_10_Heart_ Bottom
PORTFbits.RF5 = SPARE2
PORTFbits.RF6 = SPARE3
PORTFbits.RF7 = EOS_11_ Bottom
3.3V, 2mA

FY - OPTO_BOARD_1_SWTOP_PG3

OPTO_BOARD_1_TOP_PG1
PORTGbits.RG0 = PWM Top
PORTGbits.RG1 = SPARE1
PORTGbits.RG2 = Brake Top
PORTGbits.RG3 = F13_ Top
PORTGbits.RG4 = F10_ Top
PORTGbits.RG5 = F11_ Top
PORTGbits.RG6 = F12_ Top
PORTGbits.RG7 = EOS_10_ Top
5.0V, 2mA

PORTJbits.RJ0 = CL_10_ bit1 Top
PORTJbits.RJ1 = CL_10_ bit2 Top
PORTJbits.RJ2 = CL_10_ bit3 Top
PORTJbits.RJ3 = CL_10_ bit4 Top
PORTJbits.RJ4 = CL_10_Heart_ Top
PORTJbits.RJ5 = SPARE2
PORTJbits.RJ6 = SPARE3
PORTJbits.RJ7 = EOS_11_ Top
5.0V, 8mA

FY - OPTO_BOARD_3_OCC_PG7

OPTO_BOARD_3_PG1
PORTEbits.RE0 = OCC_IN_5B
PORTEbits.RE1 = OCC_IN_6
PORTEbits.RE2 = OCC_IN_7
PORTEbits.RE3 = OCC_IN_SPARE
PORTEbits.RE4 = OCC_IN_16B
PORTEbits.RE5 = OCC_IN_17
PORTEbits.RE6 = OCC_IN_18
PORTEbits.RE7 = OCC_IN_SPARE
5.0V, 8mA

PORTHbits.RH0 = OCC_OUT_5B
PORTHbits.RH1 = OCC_OUT_6
PORTHbits.RH2 = OCC_OUT_7
PORTHbits.RH3 = OCC_OUT_8A
PORTHbits.RH4 = OCC_OUT_16B
PORTHbits.RH5 = OCC_OUT_17
PORTHbits.RH6 = OCC_OUT_18
PORTHbits.RH7 = OCC_OUT_19A
3.3V, 2mA

Connections from bottom of PCB to top

X1	X2	X3	X4	X5	X6	X7	X8	X9	X10	X11	X12	X13	X14
	30			30	31	30	30	30	30	30	30	30	30
	|			29	29	29	29	29	29	29	29	29	29
	26			28	28	28	28	28	28	28	28	28	28
	25			27	27	27	27	27	27	27	27	27	27
	|			26	26	26	26	26	26	26	26	26	26
	21			25	25	13	13	13	13	25	25	25	25
	20			24	24	25	25	25	25	12	12	12	12
	|			23	23	12	12	12	12	24	24	24	24
	16			22	22	24	24	24	24	11	11	11	11
	15			21	21	11	11	11	11	23	23	23	23
	|			20	20	23	23	23	23	10	10	10	10
	11			19	19	10	10	10	10	22	22	22	22
	10			18	18	22	22	22	22	9	9	9	9
	|			17	17	9	9	9	9	21	21	21	21
	6			16	16	21	21	21	21	8	8	8	8
	5			15	15	8	8	8	8	20	20	20	20
	|			14	14	20	20	20	20	7	7	7	7
	1			13	13	7	7	7	7	19	19	19	19
				12	12	19	19	19	19	6	6	6	6
				11	11	6	6	6	6	18	18	18	18
				10	10	18	18	18	18	5	5	5	5
				9	9	5	5	5	5	17	17	17	17
				8	8	17	17	17	17	4	4	4	4
				7	7	4	4	4	4	16	16	16	16
				6	6	16	16	16	16	3	3	3	3
				5	5	3	3	3	3	15	15	15	15
				4	4	15	15	15	15	2	2	2	2
				3	3	2	2	2	2	14	14	14	14
				2	2	14	14	14	14	1	1	1	1
				1	1	1	1	1	1				

Title	FY - IO_MAPPING_OF_uC	
Size A4	Document Number <Doc>	Rev 1.0
Date: Wednesday, December 23, 2015	Sheet 1 of 1	

12V_OUT
5V_OUT
0V
FY - INTERCONNECTION PCBs

FY - OPTO_BOARD_3_OCC_PG7_71
FY - OPTO_BOARD_3_OCC_PG7_70
X23-11
X23-13
X23-10
X23-12
X23-9
X23-8
LCL19_HEART
F13
FY - OPTO_BOARD2_MISC_PG5_57

FY - OPTO_BOARD_1_SWTOP_PG3_16
FY - PWM_SUPPLY_BOARD_PG1_104
FY - OPTO_BOARD_1_SWTOP_PG3_17
FY - OPTO_BOARD_1_SWTOP_PG3_18
FY - OPTO_BOARD_1_SWBOT_PG4_30
FY - PWM_SUPPLY_BOARD_PG1_105
FY - OPTO_BOARD_1_SWBOT_PG4_31
FY - OPTO_BOARD_1_SWBOT_PG4_32
FY - PWM_SUPPLY_BOARD_PG1_55
FY - PWM_SUPPLY_BOARD_PG2_101
FY - OPTO_BOARD_1_SWTOP_PG3_19
FY - OPTO_BOARD_1_SWBOT_PG4_33
FY - PWM_SUPPLY_BOARD_PG1_58
FY - PWM_SUPPLY_BOARD_PG2_100
FY - OPTO_BOARD_1_SWTOP_PG3_12
FY - OPTO_BOARD_1_SWBOT_PG4_26
FY - PWM_SUPPLY_BOARD_PG2_103
FY - PWM_SUPPLY_BOARD_PG1_74
TC - TRACK_OUTPUT_BOARD_PG1_17
TC - TRACK_OUTPUT_BOARD_PG1_18
FY - PWM_SUPPLY_BOARD_PG3_78
FY - PWM_SUPPLY_BOARD_PG5_79
FY - PWM_SUPPLY_BOARD_PG1_62
FY - PWM_SUPPLY_BOARD_PG1_63
TC - TRACK_OUTPUT_BOARD_PG1_19
TC - TRACK_OUTPUT_BOARD_PG1_20
FY - PWM_SUPPLY_BOARD_PG1_76
FY - PWM_SUPPLY_BOARD_PG2_102
FY - PWM_SUPPLY_BOARD_PG1_75
TC - TRACK_OUTPUT_BOARD_PG1_21
TC - TRACK_OUTPUT_BOARD_PG1_22
FY - PWM_SUPPLY_BOARD_PG4_81
FY - PWM_SUPPLY_BOARD_PG5_82
FY - PWM_SUPPLY_BOARD_PG1_64
FY - PWM_SUPPLY_BOARD_PG1_65
TC - TRACK_OUTPUT_BOARD_PG1_23
TC - TRACK_OUTPUT_BOARD_PG1_24
FY - PWM_SUPPLY_BOARD_PG1_77
FY - OPTO_BOARD2_MISC_PG5_56
FY - PWM_SUPPLY_BOARD_PG1_84
FY - OPTO_BOARD_1_SWTOP_PG3_23
FY - OPTO_BOARD_1_SWTOP_PG3_22
FY - OPTO_BOARD_1_SWTOP_PG3_21
FY - OPTO_BOARD_1_SWTOP_PG3_20
FY - OPTO_BOARD_1_SWTOP_PG3_24
FY - OPTO_BOARD2_MISC_PG5_60
FY - PWM_SUPPLY_BOARD_PG1_85
FY - OPTO_BOARD_1_SWBOT_PG4_37
FY - OPTO_BOARD_1_SWBOT_PG4_36
FY - OPTO_BOARD_1_SWBOT_PG4_35
FY - OPTO_BOARD_1_SWBOT_PG4_34
FY - OPTO_BOARD_1_SWBOT_PG4_38
LCL20_HEART
F23
FY - OPTO_BOARD2_MISC_PG5_61
FY - OPTO_BOARD_3_OCC_PG7_72
FY - OPTO_BOARD_3_OCC_PG7_73

Part Of
Fiddle Yard MMDC
BOARD TOP
Part Of
Fiddle Yard MMDC
BOARD BOTTOM
Vout+
Vout 0V
Vin+
Vin 0V
S/R
D25
470E
100E 1W

K10
K11
K20
K21
K30
K32
CL10
CL20
Bridge 10L
Bridge 10R
Bridge 20L
Bridge 20R

red
blue
yellow
green
grey
black
white

EOS10
EOS11
EOS20
EOS21
F11
F12
F10
F20
F13
F21
F22
F23
+12V
0V
Photo /Logic
NPN
PNP
Q

Bridge 10L
1 red +TR7SW
6 black Q-F13
4 green -TR7SW
5 grey +F13
7 white -F13
2 blue - TR6
3 yellow +TR6

Bridge 10R
1 red SPARE
6 black SPARE
4 green SPARE
5 grey SPARE
7 white SPARE
2 blue - TR7
3 yellow +TR7

Bridge 20L
1 red +TR18SW
6 black Q-F23
4 green -TR18SW
5 grey +F23
7 white -F23
2 blue - TR17
3 yellow +TR17

Bridge 20R
1 red SPARE
6 black SPARE
4 green SPARE
5 grey SPARE
7 white SPARE
2 blue - TR18
3 yellow +TR18

FY_TOP_CL_10
FY_BOT_CL_20
B1
B2
B3
B4
HB

Title
FY - MMDC_SENS_ACT_BOARD_PG2
Size A2
Document Number PG2
Rev 1.0
Date: Thursday, December 24, 2015
Sheet 1 of 1

FY - OPTO_BOARD_1_SWBOT_PG4

FY - INTERCONNECTION PCBs

12V_OUT 5V_OUT

0V

5V

0V

TX1

RX1

390E

D86 LED

0.1uF C7 CAP

HCPL-0631

PC847

1K

0E

X11 X1 D25

FY - MMDC_SENS_ACT_BOARD_PG2_33
PWM_SUPPLY_BOARD_PG1_86
FY - MMDC_SENS_ACT_BOARD_PG2_31
FY - PWM_SUPPLY_BOARD_PG2_81
FY - MMDC_SENS_ACT_BOARD_PG2_26
FY - MMDC_SENS_ACT_BOARD_PG2_30
PWM_SUPPLY_BOARD_PG1_108
PWM_SUPPLY_BOARD_PG1_109
FY - MMDC_SENS_ACT_BOARD_PG2_34
FY - MMDC_SENS_ACT_BOARD_PG2_35
FY - MMDC_SENS_ACT_BOARD_PG2_36
FY - MMDC_SENS_ACT_BOARD_PG2_37
FY - MMDC_SENS_ACT_BOARD_PG2_38
FY - MMDC_SENS_ACT_BOARD_PG2_32

12V
0V
F22
15V TR Meas
EOS_20
F23
F20
F21
TX1
RX1
CL_20_B4
CL_20_B3
CL_20_B2
CL_20_B1
CL_20_H
SPARE
SPARE
EOS_21

D44 D55 D53 D54 LED

D45 D48 D46 D47 LED

D49 D52 D50 D51 LED

D56 D59 D57 D58 LED

NC

NC

PORTC PORTF AMP

5V
VSS
RC0
RC1
RC2
RC3
RC4
RC5
RC6
RC7

3.3V
VSS
RF0
RF1
RF2
RF3
RF4
RF5
RF6
RF7

PART OF FY - uC_BOARD

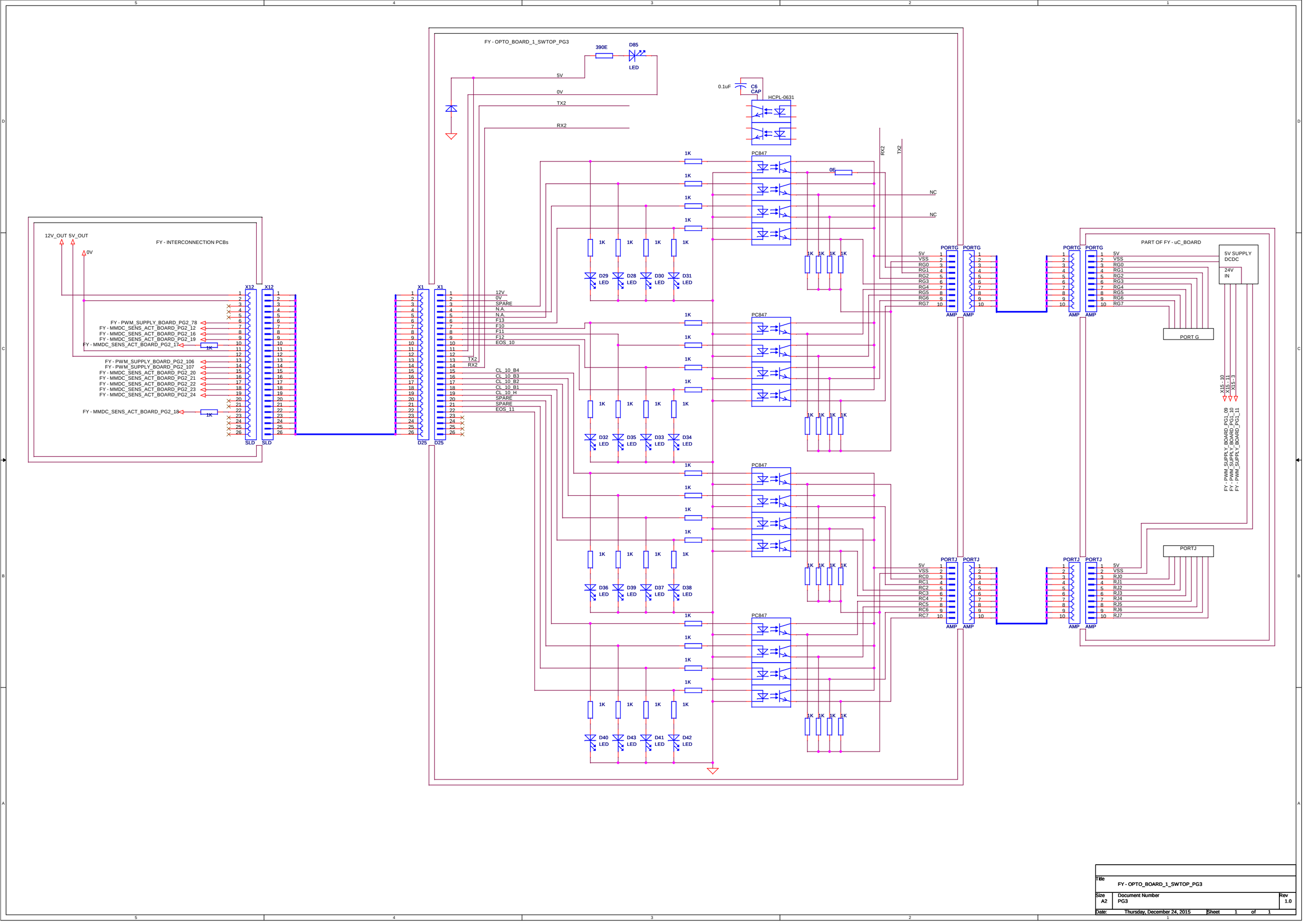
5V SUPPLY DCDC

24V IN

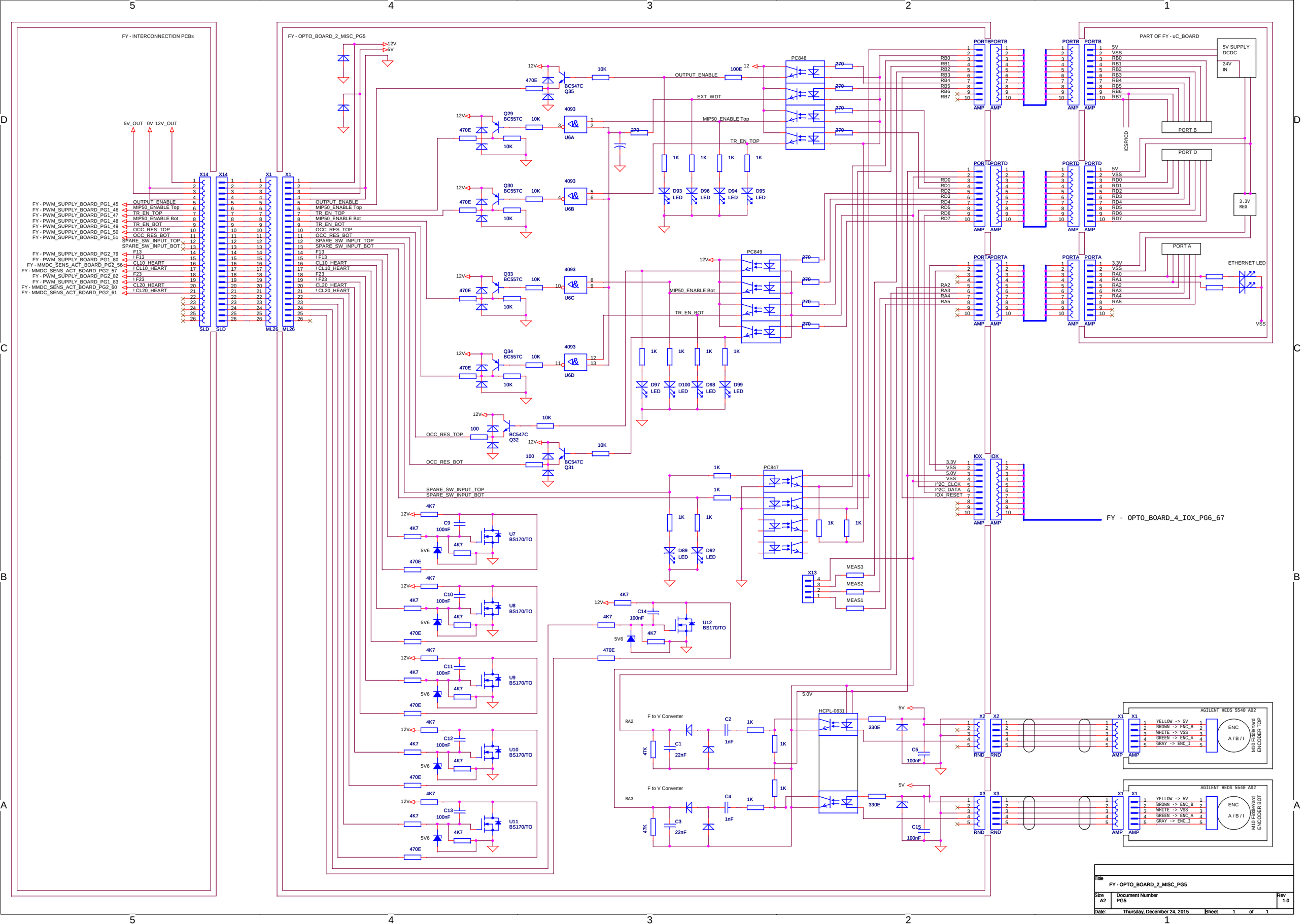
PORT C

PORTF

Title	FY - OPTO_BOARD_1_SWBOT_PG4		
Size A2	Document Number PG4		Rev 1.0
Date: Thursday, December 24, 2015	Sheet 1 of 1		



Title	FY - OPTO_BOARD_1_SWTOP_PG3	
Size A2	Document Number PG3	Rev 1.0
Date: Thursday, December 24, 2015	Sheet 1 of 1	


FY - INTERCONNECTION PCBs
FY - OPTO_BOARD_2_MISC_PG5
PART OF FY - uC_BOARD
5V SUPPLY DCDC
24V IN
PORT B
PORT D
PORT A
3.3V REG
ETHERNET LED
ICSP/ICD
FY - OPTO_BOARD_4_IOX_PG6_67
OUTPUT_ENABLE
EXT_WDT
MIP50_ENABLE Top
TR_EN_TOP
MIP50_ENABLE Bot
TR_EN_BOT
OCC_RES_TOP
OCC_RES_BOT
SPARE_SW_INPUT_TOP
SPARE_SW_INPUT_BOT
PC848
PC849
PC847
HCPL-0631
F to V Converter
AGILENT HEDS 5540 A02
M10 FiddleYard ENCODER TOP
M10 FiddleYard ENCODER BOT
Title
FY - OPTO_BOARD_2_MISC_PG5
Size A2
Document Number PG5
Rev 1.0
Date: Thursday, December 24, 2015
Sheet 1 of 1

FY - OPTO_BOARD_3_OCC_PG7

FY - INTERCONNECTION PCBs

12V_OUT 5V_OUT 0V

X21 D9: OCC_OUT_5B, OCC_OUT_6, OCC_OUT_7, OCC_OUT_8A, OCC_OUT_16B, OCC_OUT_17, OCC_OUT_18, OCC_OUT_19A

FY - OPTO_BOARD_2_MISC_PG5_56

X20 D9: FY - OPTO_BOARD_2_MISC_PG5_60, OCC_IN_5B, OCC_IN_6, OCC_IN_7, OCC_IN_SPARE, OCC_IN_16B, OCC_IN_17, OCC_IN_18, OCC_IN_SPARE

X19 D9

X18 D9: FY - MMDC_SENS_ACT_BOARD_PG2_70, FY - MMDC_SENS_ACT_BOARD_PG2_71, FY - MMDC_SENS_ACT_BOARD_PG2_72, FY - MMDC_SENS_ACT_BOARD_PG2_73

X17

X13 SLD — X1 ML26

OCC_OUT_5B, OCC_OUT_6, OCC_OUT_7, OCC_OUT_8A, OCC_OUT_16B, OCC_OUT_17, OCC_OUT_18, OCC_OUT_19A, 0V, 5V, PWM, BRAKE, OCC_IN_5B, OCC_IN_6, OCC_IN_7, OCC_IN_SPARE, OCC_IN_16B, OCC_IN_17, OCC_IN_18, OCC_IN_SPARE

12V

0V

1K D84 LED

1K — D68 LED, D79 LED, D77 LED, D78 LED

1K — D69 LED, D72 LED, D70 LED, D71 LED

100E — 1K — D73 LED, D76 LED, D74 LED, D75 LED

100E — 1K — D80 LED, D83 LED, D81 LED, D82 LED

PC847, PC847, PC847, PC848

270

1K

PORTH AMP: 3.3V, VSS, RH0, RH1, RH2, RH3, RH4, RH5, RH6, RH7

PART OF FY - uC_BOARD

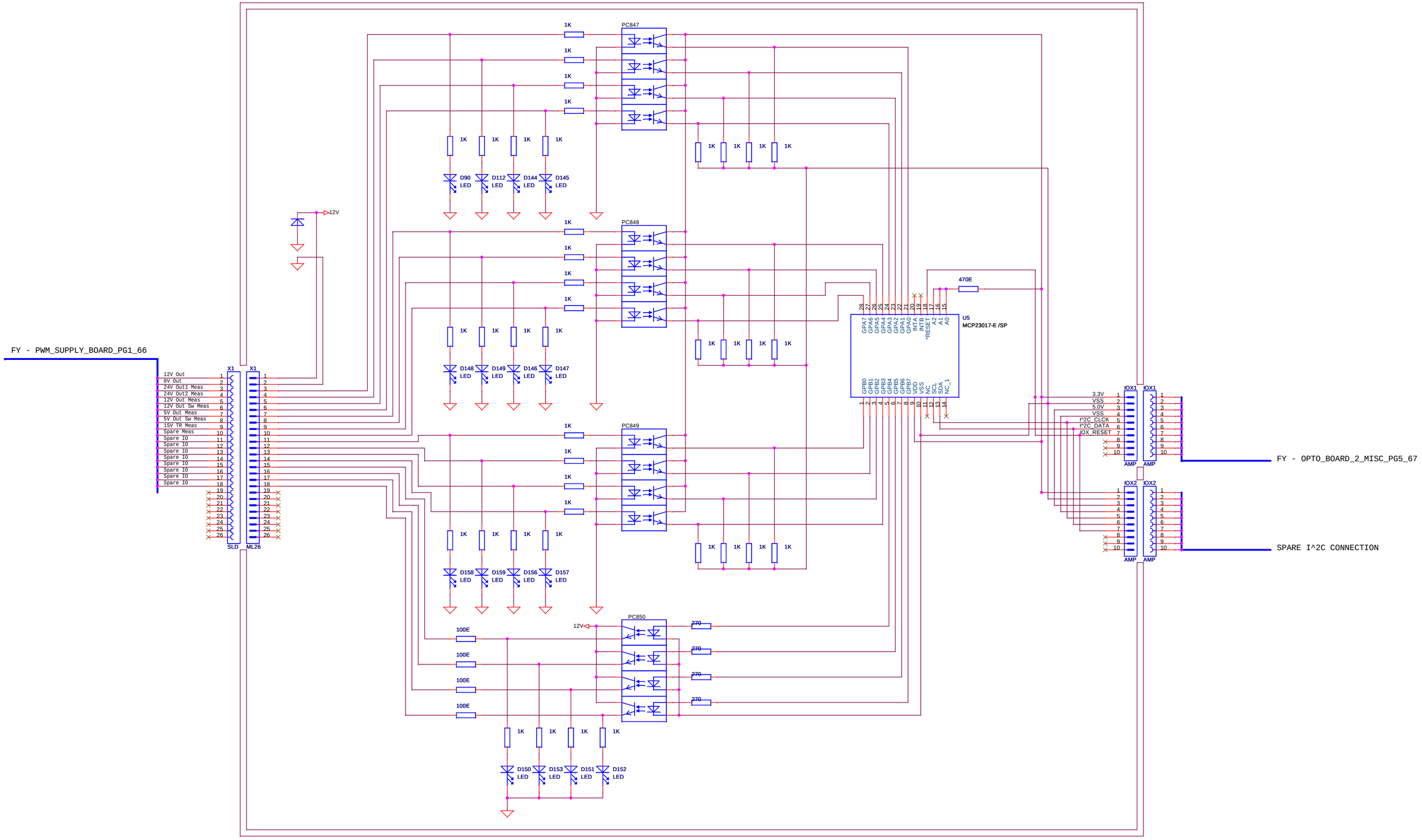
3.3V REG — 5V SUPPLY DCDC — 24V IN

PORT H

PORTE AMP: 5V, VSS, RE0, RE1, RE2, RE3, RE4, RE5, RE6, RE7

PORT E

Title	FY - OPTO_BOARD_3_OCC_PG7	
Size A2	Document Number PG7	Rev 1.0
Date: Sunday, April 22, 2012	Sheet 1 of 1	



Title	FY - OPTO_BOARD_4_IOX_PG6	
Size A2	Document Number PG6	Rev 1.0
Date: Thursday, February 23, 2012	Sheet 1 of 1	

FY - INTERCONNECTION PCBs

K31 = SPARE

Fiddle Yard MIP50 PID Motor Controllers

Fiddle Yard Supply Board

Part Of Fiddle Yard MMDC BOARD TOP

Part Of Fiddle Yard MMDC BOARD BOTTOM

From Mains & Safety Rack X11 PG99

From Mains & Safety Rack X18 PG99

Powersupply PIC18 DCDC

FY - OPTO_BOARD_1_SWTOP_PG3_09

FY - OPTO_BOARD_1_SWTOP_PG3_10

FY - OPTO_BOARD_1_SWTOP_PG3_11

FY - OPTO_BOARD_4_IOX_PG6_66

ENABLE_ALL_OUTPUTS_SIGNAL

OCC_RES_BOT

OCC_RES_TOP

ENABLE_ALL_OUTPUTS_SIGNAL

Spare Meas In
OCC_RES_TOP
OCC_RES_TOP_R
OCC_RES_BOT
OCC_RES_BOT_R

Title	FY - PWM_SUPPLY_BOARD_PG1
Size A2	Document Number PG1
Rev	1.0
Date:	Thursday, December 24, 2015
Sheet	1 of 1

L1
N
Earth
X11 X11
HVD3-Q5 HVD3-Q5
ABB CIRCUIT BREAKER K-10A
S10
J5
MAINS FILTER
S1
MAINS SWITCH
LP1
MAINS ON LED
LP3
MAINS ERROR LED
RE1
L1' N'
R-4NO
S3
SW PUSHBUTTON
PNOZ8
J6
A1 A2 S21 S12 S22 S52 Y2 Y1 13 33
G1
F1
K1 K2 K3
EARTH
14 34
LP2
SW PUSHBUTTON LED
X1 X1
HVD3-Q5 HVD3-Q5
L1 230V
N
Earth
X2 X2
HVD3-Q5 HVD3-Q5
L1 230V
N
Earth
X8 X8
LEMO 1B3 LEMO 1B3
S100
SW EMO
X9 X9
LEMO 1B3 LEMO 1B3
S200
SW EMO
X10 X10
LEMO 1B3 LEMO 1B3
S300
SW SMOKE
24V 10A
CON15
PREMIUM
J7
24V -->
15V 7A
KNIEL
J8
15V -->
12V 5A
KNIEL2
J9
12V -->
0V
S2
SW-2
RE2
R-4NO
R2
MF 1.1A
X11 X11
AMP AMP
24V uPROC non switched
0V uPROC non switched
X16 X16
AMP AMP
24V LICHTING switched
0V LICHTING switched
X17 X17
AMP AMP
12V LICHTING switched
0V LICHTING switched
X18 X18
AMP AMP
15V TRACK SUPPLY switched
0V TRACK SUPPLY switched
X3 X3
AMP AMP
TRACK SUPPLY RELAY COIL
TRACK SUPPLY RELAY 24V 1.1A max
X15 X15
E E
EARTH connection M5

DRAWN BY: JSIF

Title: MODELTRAIN_MAINS & SAFETY_RACK

Size	Document Number	Rev
A3	PG99	1.0

Date: Saturday, January 14, 2012 Sheet 1 of 1