

# Lattice DSI Display Solutions

## See the difference



**Integrate low-cost mobile DSI displays or application processors into your design**






- Supports 1080P HD, 900Mbps/lane (Rx/Tx)
- Connects any embedded architecture or mobile chipset to a MIPI DSI Display
- Supports single and dual displays (split, duplicate, mux)

### DSI Transmit Example Block Diagram



Visit [www.latticesemi.com/dsitx](http://www.latticesemi.com/dsitx) or [www.latticesemi.com/dsirx](http://www.latticesemi.com/dsirx) to learn more.

### Amazingly Small Packages

36 WLCSP	49 WLCSP	81 WLCSP	121 fcBGA	132 csBGA
				
2.5 x 2.5mm 0.4mm pitch	3.2 x 3.2mm 0.4mm pitch	3.8 x 3.8mm 0.4mm pitch	6 x 6mm 0.5mm pitch	8 x 8mm 0.5mm pitch

# Lattice CSI-2 Camera Solutions

## Easy Connect and Customize



**Ease the adoption of low cost image sensors with Lattice Semiconductor's CSI-2 interface solutions**

- Converts raw image data into various protocols: CSI-2, subLVDS, CMOS/RGB
- Supports multiple image sensors (merge, mux)
- Achieves 900Mbps/lane

### CSI-2 Transmit Example Block Diagram



Visit [www.latticesemi.com/csi2tx](http://www.latticesemi.com/csi2tx) or [www.latticesemi.com/csi2rx](http://www.latticesemi.com/csi2rx) to learn more.

	36 WLCSP (2.5 x 2.5mm) 0.4mm pitch	49 WLCSP (3.2 x 3.2mm) 0.4mm pitch	49 WLCSP (3.2 x 3.2mm) 0.4mm pitch	81 WLCSP (3.8 x 3.8mm) 0.4mm pitch	121 fcBGA (6 x 6mm) 0.5mm pitch	132 csBGA (8 x 8mm) 0.5mm pitch
LUTs	1280	2000	2100	4300	2100	4000
D-PHY speed/lane (Mbps)	900	420	900	900	900	750
D-PHY interfaces Rx/Tx	1*/1*	1/1	1/1	2/2	2/2	2/2
MachXO3L	XO3L-1300		XO3L-2100	XO3L-4300	XO3L-2100	
MachXO2		XO2-2000ZE				XO2-4000H

\*Not full 4 data lane support

**Applications Support**  
[www.latticesemi.com/support](http://www.latticesemi.com/support)



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