1. Conductors have how many valence electrons in thier atoms?	(c) The voltate.
(a) Three to four	(d) The rati
(b) Five to seven	8. The polarity
(c) Eight	(a) Using a
(d) One or two	(b) Using an
2. Examples of semiconductor materials are:	(c) Using an
(a) Copper and iron	(d) Using a
(b) Silicon and germanium	9. LEDs emit lig
(c) Arsenic and gallium	(a) Photosy
(d) Hydrogen and oxygen	(b) Electrol
3. P-type and N-type materials are made by:	<ul><li>(c) Entropy</li><li>(d) Combus</li></ul>
(a) Adding electrons directly to the semiconductor.	10. Calculate the limiting resis
(b) Removing electrons directly from the semiconductor.	15mA with a
(c) Doping the semiconductor with a substance that has five or three valence electrons.	<ul><li>(a) 600 Ω</li><li>(b) 800 Ω</li><li>(c) 5 Ω</li></ul>
(d) Running a current through the semicon-	(d) 90 $\Omega$
ductor.	11. Transistors a

4. A PN junction is what kind of electronic device?

5. \_\_\_\_\_ is the process of turning \_\_\_\_\_.

(b) Rectification, AC into DC

(c) Rectification, DC into AC

rectifiers used in electronic circuits.

7. Peak inverse voltage (PIV) is

(a) Commutation, voltage into current

(d) Inversion, capacitance into inductance

6. Bridge rectificers are the most common type of

(a) The reverse bias at which breakover oc-

(b) The forward bias at which the diode con-

(a) Diode

(b) Rectifier

(c) OR gate

(d) SCR

(a) True

(b) False

ducts.

- tage required for a diode to oper-
- io of heat to amps characteristic.
- of a diode can be confirmed by:
  - voltmeter
  - n ohmmeter
  - n ammeter
  - caliper
- ght by which process?
  - nthesis
  - uminescence
  - stion
- e required resistance of a current tor needed for a LED operated at 12V DC source.
- re made by
  - (a) Joining three layers of semiconductor materials.
  - (b) Joining two layers of semiconductor materials.
  - (c) Joining four layers of semiconductor materials.
  - (d) Joining a capacitor and inductor in series.
- 12. FET devices are use in applications requiring low-imedance input and/or high power consumption.
  - (a) True
  - (b) False
- 13. Phase controls is used by SCRs to
  - (a) Vary the timing of a switch.
  - (b) Align the phases of current and voltage.
  - (c) Vary the amount of power delivered to a load.
  - (d) Ensure lights on a circuit all turn on at the same time.
- 14. Diacs can be thought of as AC switches and Triacs can be used in phase control applications.

	<ul><li>(a) True</li><li>(b) False</li></ul>	(a) Working in a carpeted area, using zip-lock bags
15.	PC (Printed Circuit Boards) use instead	(b) Working in an area with tape, cardboard and plastic boxes.
	of wires to electrically connect components.  (a) tin foil bonded strips glued to the board	(c) Using a ESD grounding strap, grounded work surface and ESD bags
	<ul><li>(b) chemically etched copper circuits</li><li>(c) gold solder lines layed onto the board</li></ul>	(d) Using radios, and non-ESD devices in the same work area.
	(d) None of the above.	20. Op amps have which qualities?
16.	PC (Printed Circuit Boards) use instead of wires to electrically connect components.	(a) High input impedance, low output impedance, low gain.
	(a) tin foil bonded strips glued to the board	(b) Low input impedance, high output impedance, low gain.
	(b) chemically etched copper circuits	(c) Low input impedance, low output
	(c) gold solder lines layed onto the board	impedance, high gain.
	<ul><li>(c) gold solder lines layed onto the board</li><li>(d) None of the above.</li></ul>	. ,

(a) a hermetically sealed case containing a tiny

(d) a device that uses optical transmission

wafer of semiconductor material.

18. A major advantage of microprocessors is its

19. Exmaples of safe ESD working practices in-

(b) a PNPN connection.

(a) True

(b) False

clude:

(c) an operation amplifier.

paths to transfer a signal.

ability to provide very precise control.

- (a) XAND
- (b) XOR
- (c) NAND
- (d) OR
- 22. Which gate gives a 1 only when both inputs are 0?
  - (a) NAND
  - (b) NOR
  - (c) AND
  - (d) OR