



Features

- 4A Peak Source/Sink Drive Current
- Wide Operating Voltage Range: 4.5V to 35V
- -40°C to +125°C Extended Operating Temperature Range
- Logic Input Withstands Negative Swing of up to 5V
- Outputs May be Connected in Parallel for Higher Drive Current
- Matched Rise and Fall Times
- Low Propagation Delay Time
- Low, 10μA Supply Current
- Low Output Impedance

Applications

- Efficient Power MOSFET and IGBT Switching
- Switch Mode Power Supplies
- Motor Controls
- DC to DC Converters
- Class-D Switching Amplifiers
- Pulse Transformer Driver







Description

The IXDD604/IXDF604/IXDI604/IXDN604 dual high-speed gate drivers are especially well suited for driving the latest IXYS MOSFETs and IGBTs. Each of the two outputs can source and sink 4A of peak current while producing voltage rise and fall times of less than 10ns. The input of each driver is virtually immune to latch up, and proprietary circuitry eliminates cross conduction and current "shoot-through." Low propagation delay and fast, matched rise and fall times make the IXD_604 family ideal for high-frequency and high-power applications.

The IXDD604 is a dual non-inverting driver with an enable. The IXDN604 is a dual non-inverting driver, the IXDI604 is a dual inverting driver, and the IXDF604 has one inverting driver and one non-inverting driver.

The IXD_604 family is available in a standard 8-pin DIP (PI), 8-pin SOIC (SIA), 8-pin Power SOIC with an exposed metal back (SI), and an 8-pin DFN (D2) package.

Ordering Information

Part Number	Logic Configuration	Package Type	Packing Method	Quantity
IXDD604D2TR		8-Pin DFN	Tape & Reel	2000
IXDD604PI	ENA	8-Pin DIP	Tube	50
IXDD604SI	INA — A OUTA	8-Pin Power SOIC with Exposed Metal Back	Tube	100
IXDD604SITR	ENB B OUTB	8-Pin Power SOIC with Exposed Metal Back	Tape & Reel	2000
IXDD604SIA	INВ В ОИТВ	8-Pin SOIC	Tube	100
IXDD604SIATR		8-Pin SOIC	Tape & Reel	2000
IXDF604PI		8-Pin DIP	Tube	50
IXDF604SI	INA AO OUTA	8-Pin Power SOIC with Exposed Metal Back	Tube	100
IXDF604SITR		8-Pin Power SOIC with Exposed Metal Back	Tape & Reel	2000
IXDF604SIA	INB B OUTB	8-Pin SOIC	Tube	100
IXDF604SIATR		8-Pin SOIC	Tape & Reel	2000
IXDI604PI		8-Pin DIP	Tube	50
IXDI604SI	INA ————————————————————————————————————	8-Pin Power SOIC with Exposed Metal Back	Tube	100
IXDI604SITR	, , , , , , , , , , , , , , , , , , ,	8-Pin Power SOIC with Exposed Metal Back	Tape & Reel	2000
IXDI604SIA	INB BOO OUTB	8-Pin SOIC	Tube	100
IXDI604SIATR	, , , , , , , , , , , , , , , , , , ,	8-Pin SOIC	Tape & Reel	2000
IXDN604PI		8-Pin DIP	Tube	50
IXDN604SI	INA — A OUTA	8-Pin Power SOIC with Exposed Metal Back	Tube	100
IXDN604SITR		8-Pin Power SOIC with Exposed Metal Back	Tape & Reel	2000
IXDN604SIA	INB B OUTB	8-Pin SOIC	Tube	100
IXDN604SIATR	· 	8-Pin SOIC	Tape & Reel	2000



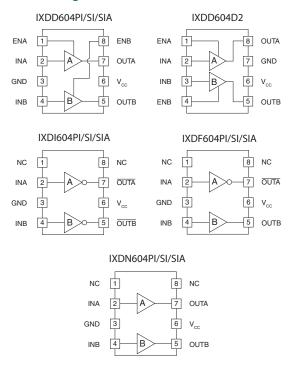
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1 Specifications

1.1 Pin Configurations



1.2 Pin Definitions

Pin Name	Description
INA	Channel A Logic Input
INB	Channel B Logic Input
ENA	Channel A Enable Input - Drive pin low to disable Channel A and force Channel A Output to a high impedance state
ENB	Channel B Enable Input - Drive pin low to disable Channel A and force Channel A Output to a high impedance state
OUTA OUTA	Channel A Output - Sources or sinks current to turn-on or turn-off a discrete MOSFET or IGBT
OUTB OUTB	Channel B Output - Sources or sinks current to turn-on or turn-off a discrete MOSFET or IGBT
V _{CC}	Supply Voltage - Provides power to the device
GND	Ground - Common ground reference for the device

1.3 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Supply Voltage	V _{CC}	-0.3	40	V
Input Voltage	V_{INx} , V_{ENx}	-5	V _{CC} +0.3	V
Output Current	I _{OUT}	-	±4	Α
Junction Temperature	T _J	-55	+150	°C
Storage Temperature	T _{STG}	-65	+150	°C

Absolute maximum electrical ratings are at 25°C

Absolute maximum ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

1.4 Recommended Operating Conditions

Parameter	Symbol	Range	Units
Supply Voltage	V _{CC}	4.5 to 35	V
Operating Temperature Range	T _A	-40 to +125	°C



1.5 Electrical Characteristics: $T_A = 25^{\circ}C$

Test Conditions: $4.5\text{V} \le \text{V}_{CC} \le 35\text{V}$, one channel (unless otherwise noted).

Parameter	Conditions	Symbol	Minimum	Typical	Maximum	Units
Input Voltage, High	4.5V ≤ V _{CC} ≤ 18V	V _{IH}	3.0	-	-	V
Input Voltage, Low	4.5V ≤ V _{CC} ≤ 18V	V _{IL}	-	-	0.8	V
Input Current	$0V \le V_{IN} \le V_{CC}$	I _{IN}	-	-	±10	μΑ
High EN Input Voltage	IXDD604 only	V _{ENH}	2/3V _{CC}	-	-	V
Low EN Input Voltage	IXDD604 only	V _{ENL}	-	-	1/3V _{CC}	V
Output Voltage, High	-	V _{OH}	V _{CC} -0.025	-	-	V
Output Voltage, Low	-	V _{OL}	-	-	0.025	V
Output Resistance, High State	V _{CC} =18V, I _{OUT} =-10mA	R _{OH}	-	1.3	2.5	Ω
Output Resistance, Low State	V _{CC} =18V, I _{OUT} =10mA	R _{OL}	-	1.1	2	22
Output Current, Continuous	Limited by package power dissipation	I _{DC}	-	-	±1	Α
Rise Time	V _{CC} =18V, C _{LOAD} =1000pF	t _r	-	9	16	
Fall Time	V _{CC} =18V, C _{LOAD} =1000pF	t _f	-	8	14	
On-Time Propagation Delay	V _{CC} =18V, C _{LOAD} =1000pF	t _{ondly}	-	29	50	20
Off-Time Propagation Delay	V _{CC} =18V, C _{LOAD} =1000pF	t _{offdly}	-	35	50	ns
Enable to Output-High Delay Time	IXDD604 only, V _{CC} =18V	t _{ENOH}	-	35	55	
Disable to High Impedance State Delay Time	IXDD604 only, V _{CC} =18V	t _{DOLD}	-	40	55	
Enable Pull-Up Resistor	-	R _{EN}	-	200	-	kΩ
	V _{CC} =18V, V _{IN} =3.5V		-	1	3	mA
Power Supply Current	V _{CC} =18V, V _{IN} =0V	I _{CC}	-	<1	10	μА
	V _{CC} =18V, V _{IN} =V _{CC}		-	<1	10	μΛ

1.6 Electrical Characteristics: -40°C $\leq T_{\mbox{\scriptsize A}} \leq +125^{\circ}\mbox{\scriptsize C}$

Test Conditions: $4.5 \text{V} \leq \text{V}_{\text{CC}} \leq 35 \text{V}$, one channel (unless otherwise noted).

Parameter	Conditions	Symbol	Minimum	Maximum	Units
Input Voltage, High	4.5V ≤ V _{CC} ≤ 18V	V_{IH}	3.3	-	V
Input Voltage, Low	4.5V ≤ V _{CC} ≤ 18V	V_{IL}	-	0.65	V
Input Current	$0V \le V_{IN} \le V_{CC}$	I _{IN}	-10	10	μΑ
Output Voltage, High	-	V _{OH}	V _{CC} -0.025	-	V
Output Voltage, Low	-	V _{OL}	-	0.025	V
Output Resistance, High State	V _{CC} =18V, I _{OUT} =-10mA	R _{OH}	-	3	Ω
Output Resistance, Low State	V _{CC} =18V, I _{OUT} =10mA	R _{OL}	-	2.5	52
Output Current, Continuous	Limited by package power dissipation	I _{DC}	-	±1	Α
Rise Time	V _{CC} =18V, C _{LOAD} =1000pF	t _r	-	16	
Fall Time	V _{CC} =18V, C _{LOAD} =1000pF	t _f	-	14	
On-Time Propagation Delay	V _{CC} =18V, C _{LOAD} =1000pF	t _{ondly}	-	65	
Off-Time Propagation Delay	V _{CC} =18V, C _{LOAD} =1000pF	t _{offdly}	-	65	ns
Enable to Output-High Delay Time	IXDD604 only, V _{CC} =18V	t _{ENOH}	-	65	
Disable to High Impedance State Delay Time	IXDD604 only, V _{CC} =18V	t _{DOLD}	-	65	
	V _{CC} =18V, V _{IN} =3.5V		-	3.5	mA
Power Supply Current	V _{CC} =18V, V _{IN} =0V	I _{CC}	-	150	^
	V_{CC} =18V, V_{IN} = V_{CC}		-	150	- μ A

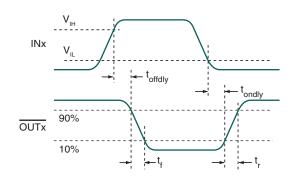


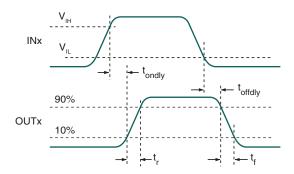
1.7 Thermal Characteristics

Package	Parameter	Symbol	Rating	Units
D2 (8-Pin DFN)			35	°C/W
PI (8-Pin DIP)	Thormal Resistance Junction-to-Ambient	θ_{JA}	125	
SI (8-Pin Power SOIC)	Thermal Resistance, Junction-to-Ambient		85	- C/VV
SIA (8-Pin SOIC)			120	
SI (8-Pin Power SOIC)	Thermal Resistance, Junction-to-Case	$\theta_{\sf JC}$	10	°C/W

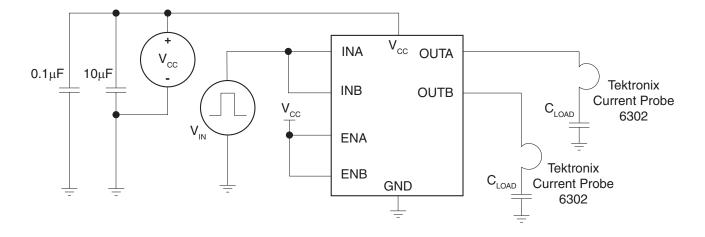
2 IXD_604 Performance

2.1 Timing Diagrams





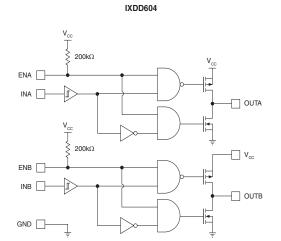
2.2 Characteristics Test Diagram





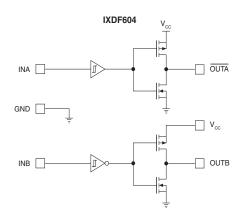
3 Block Diagrams & Truth Tables

3.1 IXDD604



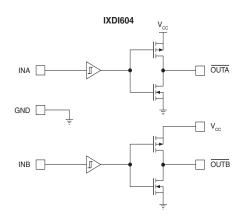
IN _X	ENχ	OUT _X
0	1 or open	0
1	1 or open	1
0	0	Z
1	0	Z

3.3 IXDF604



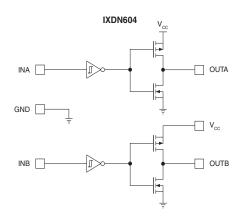
INA	OUTA
0	1
1	0
INB	OUTB
0	0
1	1

3.2 IXDI604



IN _X	OUT _X
0	1
1	0

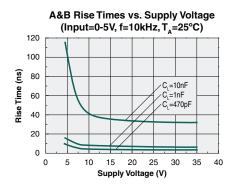
3.4 IXDN604

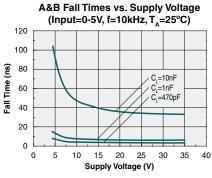


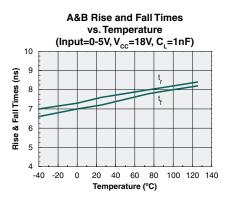
INχ	OUTX
0	0
1	1

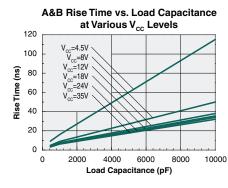


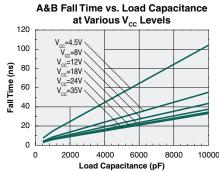
4 Typical Performance Characteristics

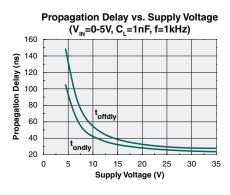


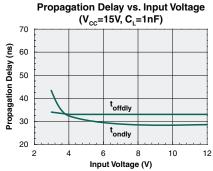


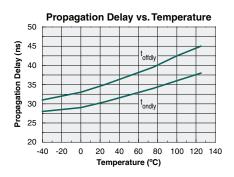


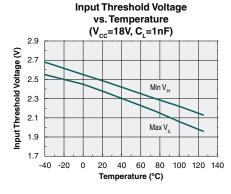


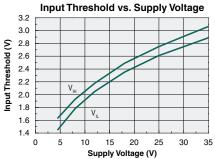


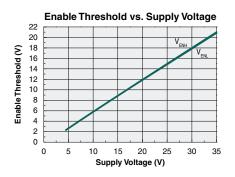




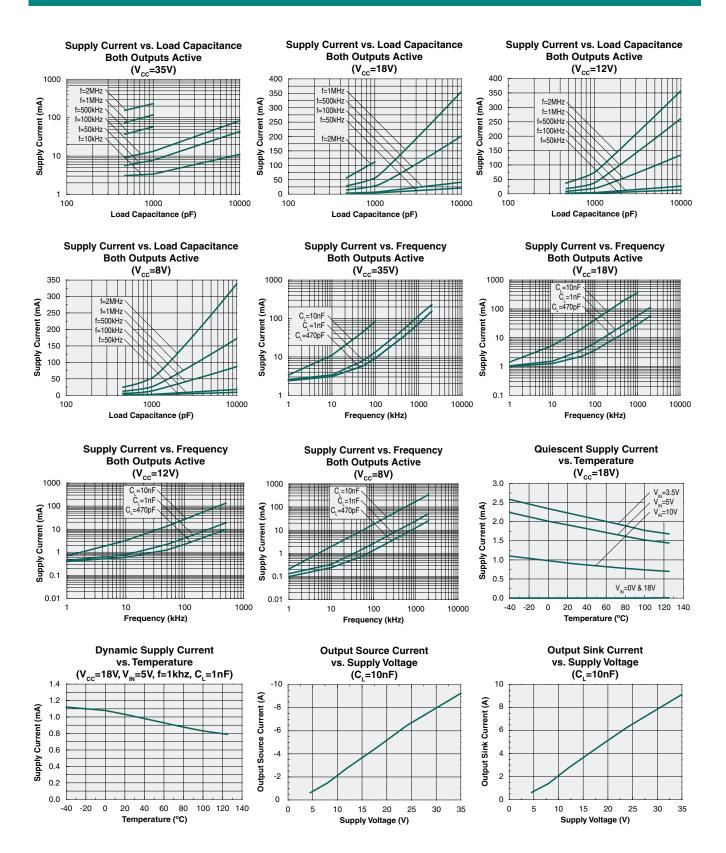




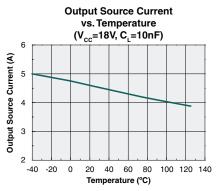


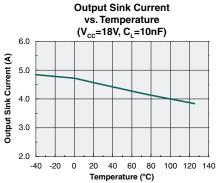


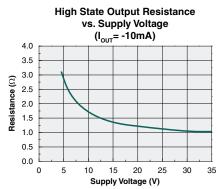


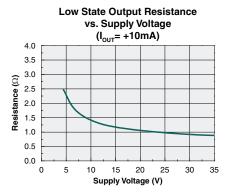














5 Manufacturing Information

5.1 Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
IXD_604 All Versions	MSL 1

5.2 ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

5.3 Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time	Maximum Reflow Cycles
IXD_604SI / IXD_604SIA / IXD_604D2	260°C for 30 seconds	3
IXD_604PI	250°C for 30 seconds	-



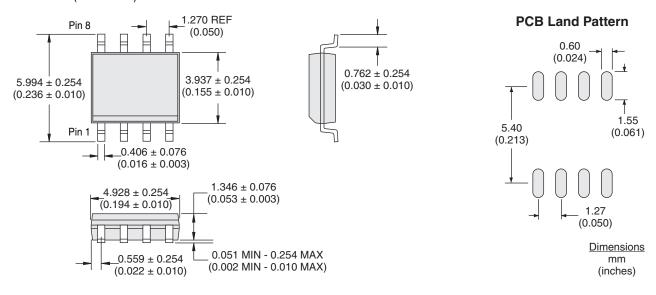




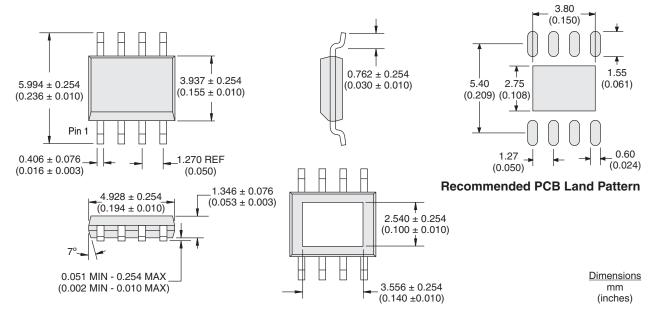


5.4 Mechanical Dimensions

5.4.1 SIA (8-Pin SOIC)



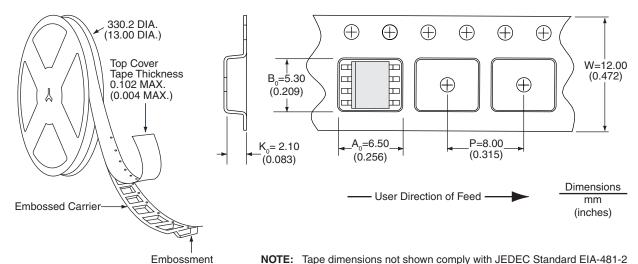
5.4.2 SI (8-Pin Power SOIC with Exposed Metal Back)



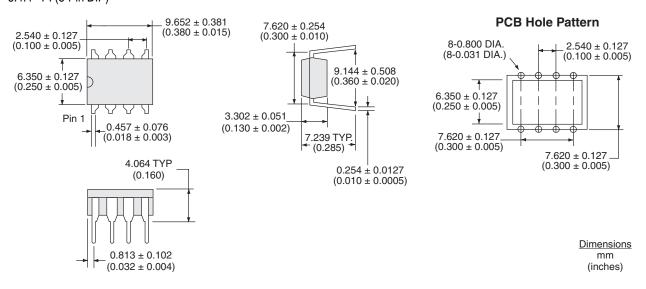
Note: The exposed metal pad on the back of the SI package should be connected to GND. It is not suitable for carrying current.



5.4.3 Tape & Reel Information for SI and SIA Packages

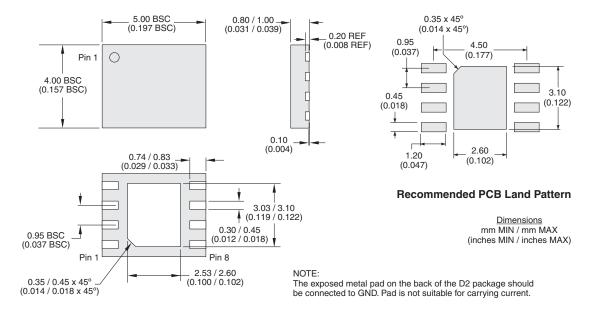


5.4.4 PI (8-Pin DIP)

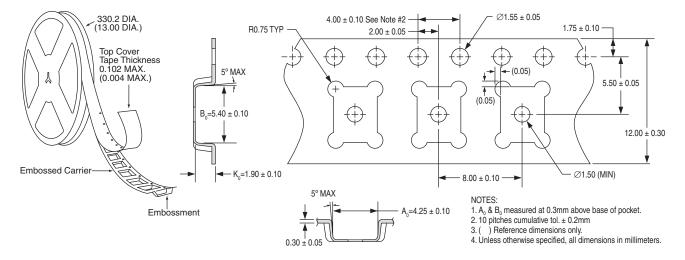




5.4.5 D2 (8-Pin DFN)



5.4.6 Tape & Reel Information for D2 Package



For additional information please visit our website at: www.ixysic.com

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