10% Discount Coupon

Redeemable by Participants of the "Floating-Point Indoctrination Lectures" by Professor W. Kahan at Sun Microsystems, 1988, By Special Arrangement with Morgan Kaufmann Publishers, Inc

Toward the purchase of any of the following titles:

COMPUTER ARCHITECTURE: A QUANTITATIVE APPROACH

David A. Patterson (University of California, Berkeley) and John L. Hennessy (Stanford University). February 1990; 594 pages, plus appendices; cloth; ISBN 1-55860-069-8; Retail Price: \$54.95

"...This will be the book of the decade in computer systems." C. Gordon Bell

Contents: Fundamentals of Computer Design. Performance and Cost. Instruction Set Design: Alternatives and Principles. Instruction Set Examples and Measurements of Use. Basic Processor Implementation. Pipelining. Vector Processors. Memory Hierarchy Design. Input/Output. Future Directions: Parallel and Special Purpose Processors. Appendices A) Computer Arithmetic B) Complete Instruction Set Tables C) Detailed Instruction Set Measurements D) Time Versus Frequency Measurements E) Survey of RISC Architectures. Index. Bibliography.

CACHE & MEMORY HIERARCHY DESIGN: A PERFORMANCE DIRECTED APPROACH

Steven A. Przybylski (MIPS Computer Systems). 223 pages, cloth; ISBN 1-55860-136-8; Retail Price \$33.95.

Contents: Introduction. Background Material. The Cache Design Problem and Its Solution. Performance-Directed Cache Design. Multi-level Cache Hierarchies. Summary, Implications and Conclusions. Appendix A: Validation of the Empirical Results. Appendix B: Modelling Write Strategy Effects. References. Index.

VLSI AND PARALLEL COMPUTATION

Edited by Robert Suaya (SRI International) and Graham Birtwistle (University of Calgary). 496 pages, cloth; ISBN 1-934613-99-0; Retail Price \$35.95.

Contents: Concurrent Architectures, Charles L. Seitz, (Caltech). Theoretical Aspects of Parallel Computation, Ernst W. Mayr, (Stanford). Network and Processor Architecture for Message-Driven Computers, Bill Dally, (MIT). Communication in Network Architectures, S. Lennart Johnsson, (Yale/Thinking Machines). Neural Networks, Yaser Abu-Mostafa and David Schweizer, (Both of Caltech). VLSI and Machines that Hear, Richard Lyon, (Apple Computer). Knowledge Based VLSI Design Synthesis, Bryan Ackland, (AT&T).

USE FORM ON REVERSE TO PLACE YOUR ORDER

To order by mail, enclose your personal check for the book or books you desire. If you wish us to charge your Visa or Mastercard, provide your credit card number, name as it appears on the card, expiry date and telephone number.

Add shipping and handling (US/Canada Surface Post: \$3.50 for first book, \$2.50 for each additional volume. US/Canada Airmail: \$10.00/first volume, \$4.50/each additional. International Surface Post: \$6.50/first volume, \$2.50/each additional. International Airmail: \$40.00/volume.

California Residents, please add appropriate sales tax.

<u>QTY</u>	TITLE	PRICE	10% DISC.	AMT.
<u> </u>	COMPUTER ARCHITECTURE (069-8) CACHE HIERARCHY (136-8) VLSI & PARALELL COMPUTATION (99-0)	\$ 33.95	\$49.46 \$30.56 \$32.36	\$ \$ \$
	SUBTO TAX SHIP/I	OTAL HNDLG ENCLOS is Enclo VISA	ED Dsed. Mastero Exp Date	

RETURN THIS FORM TO:

MORGAN KAUFMANN PUBLISHERS, INC.

DEPARTMENT C-9
2929 Campus Drive, Suite 260, San Mateo, CA 94403, USA
Telephone (415) 578-9911, Fax 578-0672