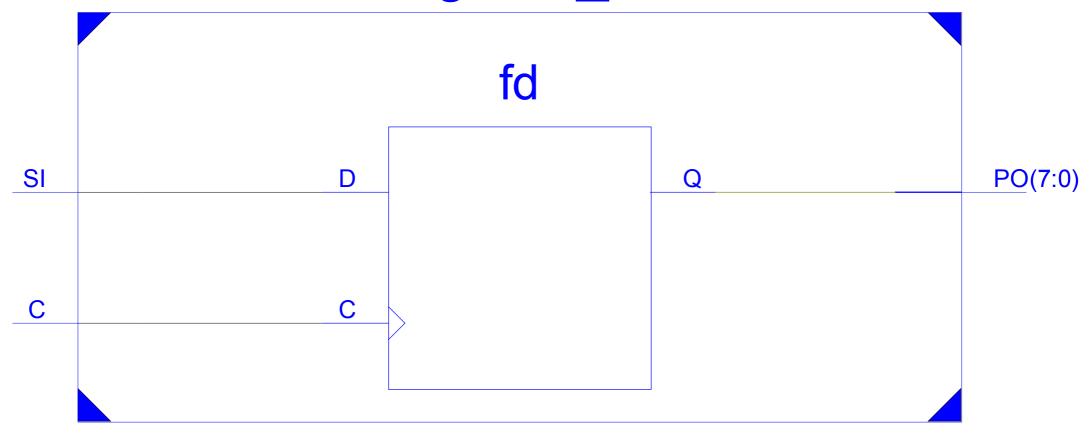
ShiftRegister_SIPO:1



ShiftRegister_SIPO