

WORKSHOP JAVA EE 7 EIN PRAKTISCHER EINSTIEG IN DIE JAVA ENTERPRISE EDITION MI

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Workshop Java EE 7: Ein praktischer Einstieg in die Java Enterprise Edition mit dem Web Profile

Was ist Java EE 7?

Java EE 7 ist eine umfangreiche Plattform für die Entwicklung von Unternehmensanwendungen, die Funktionen wie Dependency Injection, Transaktionsmanagement und Sicherheit bietet. Das Web Profile ist eine Subset von Java EE 7, das auf die Entwicklung von Webanwendungen spezialisiert ist und Komponenten wie Webservices, Servlets und JSPs umfasst.

Was ist das Ziel dieses Workshops?

Dieser Workshop soll Entwicklern einen praktischen Einstieg in Java EE 7 mit dem Web Profile bieten. Die Teilnehmer werden lernen, wie man einfache Webanwendungen erstellt und bereitstellt, einschließlich der Verwendung von Anmerkungen, Dependency Injection und Datenbankbindung.

Welche Vorkenntnisse sind erforderlich?

Um an diesem Workshop teilnehmen zu können, sollten die Teilnehmer über grundlegende Java-Kenntnisse verfügen und mit der Verwendung von Build-Tools wie Maven oder Gradle vertraut sein.

Was wird in diesem Workshop behandelt?

Der Workshop behandelt folgende Themen:

- Einrichten einer Java EE 7-Umgebung
- Erstellen einer einfachen Webanwendung mit JAX-RS, Servlets und JSPs
- Verwendung von Anmerkungen zur Vereinfachung des Codes
- Dependency Injection mit CDI
- Anbindung an eine Datenbank mit JPA

Für wen ist dieser Workshop geeignet?

Dieser Workshop ist für Java-Entwickler geeignet, die ihr Wissen in Java EE erweitern möchten, insbesondere für diejenigen, die sich auf die Entwicklung von Webanwendungen konzentrieren.

What is Xilinx Zynq Ultrascale? The Zynq UltraScale+ MPSoC from Xilinx is an innovative system-on-chip (SoC) that integrates a high-performance ARM multicore processor with Xilinx programmable logic. The Zynq UltraScale+ combines real-time, deterministic embedded computing with the flexibility of an FPGA-based programmable logic fabric.

What is IP in Zynq UltraScale+? The Processing System IP is the software interface around the Zynq™ Ultrascale+™ MPSoC Processing System. The Zynq UltraScale MPSoC family consists of a system-on-chip (SoC) style integrated processing system (PS) and a Programmable Logic (PL) unit, providing an extensible and flexible SoC solution on a single die.

What are the boot options for Zynq UltraScale+? The primary boot mode is the boot mode used by bootROM to load the FSBL and optionally the PMU Firmware. The secondary boot mode is the boot device used by FSBL to load all of the other partitions. The supported secondary boot modes are QSPI24, QSPI32, SD0, eMMC, SD1, SD1-LS, NAND and USB.

What is the difference between MPSoC and FPGA? MPSoCs usually target embedded applications. System-on-modules (SOMs) are small, integrated single-board computers. AMD FPGAs (field programmable gate arrays) are used to offer high-capacity logic and memory storage, while maintaining a module size suitable for

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embedded solutions.

What does MPSoC stand for? (MultiProcessor System On Chip) A system-on-chip (SoC) with two or more CPU cores. Most SOC's are MPSoCs.

Is Zynq a FPGA? Definition of Zynq Therefore the "system" in APSoC as it relates to Zynq, refers to the system of dual dedicated processors (Dual-core ARM Cortex-A9 Processors) and the FPGA technology. With access to both processor and FPGA functions, developers can leverage the best of both worlds.

What is the difference between MPSoC and RFSoc? The big deal with the RFSoc was including a ton of high speed data converters on the same chip. This makes it extremely attractive for custom data acquisition devices, high performance SDR, and similar applications. The MPSoC is basically just a bigger zynq.

What is the voltage of Zynq Ultrascale+?

What is UltraScale FPGA? Xilinx Kintex UltraScale and UltraScale+ are families of high-performance mid-range FPGAs featuring advanced 16nm and 20nm process nodes respectively.

How to program a Zynq board?

What is Zynq 7000 SoC? AMD Zynq™ 7000 SoC family integrates the software programmability of an Arm®-based processor with the hardware programmability of an FPGA, enabling key analytics and hardware acceleration while integrating CPU, DSP, ASSP, and mixed signal functionality on a single device.

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What is Xilinx why it is used for? Xilinx develops highly flexible and adaptive processing platforms that enable rapid innovation across a variety of technologies – from the cloud, to the edge, to intelligent end devices.

What is the most powerful FPGA in Xilinx? Xilinx introduces the Virtex® UltraScale+™ VU19P, the world's largest FPGA, to enable prototyping and emulation of the most advanced ASIC and SoC technologies, as well as the development of complex algorithms.

Zamoyski: A Polish Dynasty with a Rich History

Q: Who were the Zamoyski family? A: The Zamoyski family was a prominent Polish aristocratic dynasty that played a significant role in the country's history from the 16th to the 20th centuries.

Q: What is the significance of Jan Zamoyski? A: Jan Zamoyski (1542-1605) was one of the most influential Polish statesmen and military commanders of his time. He served as Chancellor and Grand Hetman of the Crown, and founded the city of Zamo??, which became a major cultural and educational center.

Q: How did the Zamoyski family contribute to Polish culture? A: The Zamoyski family was known for its patronage of the arts and sciences. They founded the Zamoyski Academy in Zamo??, which was one of the most prestigious educational institutions in Poland. They also supported the development of literature and architecture, with many notable palaces and churches bearing their name.

Q: What was the role of the Zamoyski family during Poland's partitions? A: During the partitions of Poland in the 18th and 19th centuries, the Zamoyski family actively opposed foreign rule and fought for Polish independence. They were known for their involvement in patriotic movements and their support of the Polish cause.

Q: Are there any descendants of the Zamoyski family today? A: Yes, there are still descendants of the Zamoyski family living today. They have settled in various countries around the world, but they continue to play an active role in Polish society and culture.

Writer-Designer: A Symbiotic Relationship

What is a Writer-Designer Collaboration?

A writer-designer collaboration brings together a writer and a designer to create visually engaging and impactful written content. The writer provides the text, while the designer enhances it with visual elements such as typography, imagery, and layout.

Why is Collaboration Beneficial?

Collaboration allows writers and designers to leverage their respective strengths. Writers excel in crafting written narratives, while designers specialize in visual communication. By combining these skills, they can produce content that is both compelling and aesthetically pleasing.

How Does Collaboration Work?

The collaboration process typically involves several steps:

- **Initial Briefing:** The writer and designer discuss the project goals, target audience, and desired outcome.
- **Content Creation:** The writer develops the written content, focusing on clarity, structure, and tone.
- **Design Interpretation:** The designer reviews the text and interprets it into visual form, considering color schemes, fonts, and page layouts.
- **Feedback and Iteration:** The writer and designer exchange feedback and make necessary revisions to ensure the content meets the initial vision.

What are the Benefits of Collaboration?

Collaborations between writers and designers offer numerous benefits:

- **Improved Visual Appeal:** Designers enhance the readability and visual appeal of written content, making it more engaging and accessible.
- **Enhanced Impact:** Visual elements help convey complex ideas, evoke emotions, and create a lasting impression on readers.

- Increased Engagement: Visually stimulating content captures attention and encourages readers to interact with the written materials.
- Optimized Communication: Collaboration ensures that written content is effectively communicated to the target audience.

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