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CS-4515-Homework 2

C.2 [15/15] < C.2 > Suppose the branch frequencies (as percentages of all instructions) are as follows:

Conditional branches	15%
Jumps and calls	1%
Taken conditional branches	60% are taken

a. [15] < C.2 > We are examining a four-stage pipeline where the branch is resolved at the end of the second cycle for unconditional branches and at the end of the third cycle for conditional branches. Assuming that only the first pipe stage can always be completed independent of whether the branch is taken and ignoring other pipeline stalls, how much faster would the machine be without any branch hazards?

Pipeline Speedup =
$$\frac{1}{1+pipeline \text{ stalls}} * pipeline \text{ depth}$$

$$= \frac{1}{1+0} * 4$$

$$= 1 + 4$$

$$= 4$$
Real Pipeline Stalls = $1 * 1\% + 2*9\% + 1*6\%$

$$= 0.01 + 0.18 + 0.06$$

$$= 0.25$$
Real Pipeline Speedup = $\frac{1}{1+0.25}$ * 0.4

$$= 3.2$$

Pipeline speedup without control hazards = 4/3.2 = 1.25

b. [15] < C.2 > Now assume a high-performance processor in which we have a 15-deep pipeline where the branch is resolved at the end of the fifth cycle for unconditional branches and at the end of the tenth cycle for conditional branches. Assuming that only the first pipe stage can always be completed independent of whether the branch is taken and ignoring other pipeline stalls, how much faster would the machine be without any branch hazards?

Real pipeline Stalls =
$$4*1\% + 9*4\% + 8*6\%$$

= $0.04 + 0.81 + 0.48$
= 1.33
Real pipeline speedup = $\frac{1}{1+1.33} \times 4$
= 1.7167
= 1.72

Pipeline speedup without control hazards = 4/1.72 = 2.33