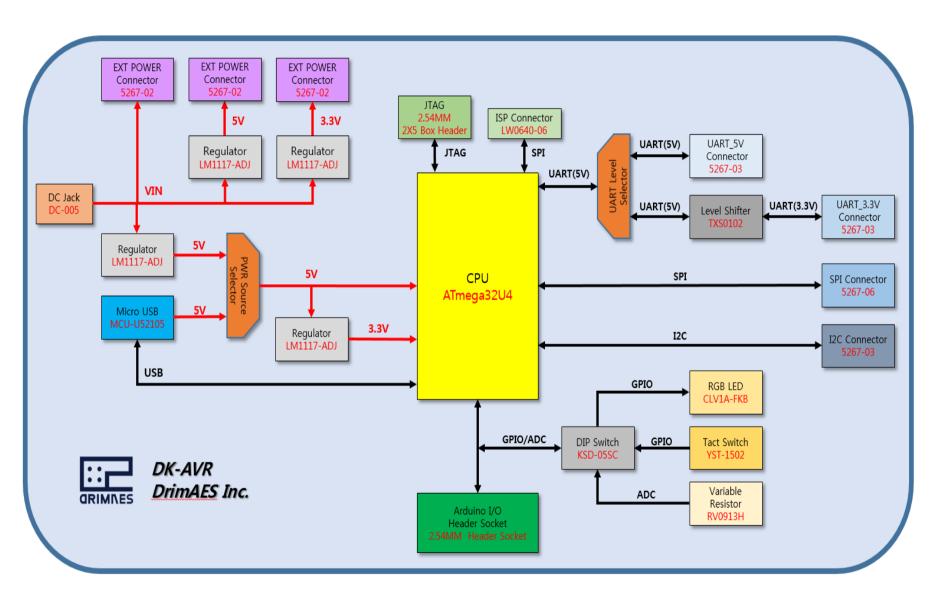
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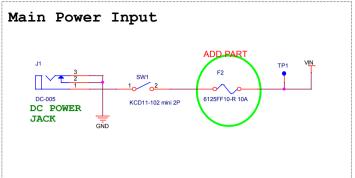


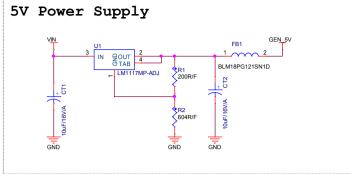
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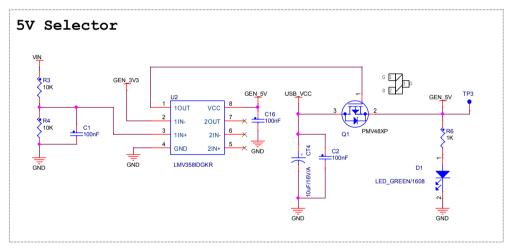
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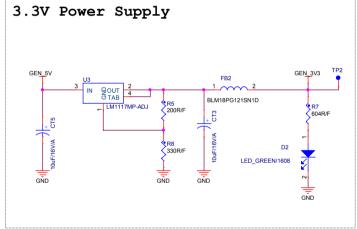
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- 2. CONTENTS
- 3. MAIN POWER
- 4. CPU
- 5. INTERFACE
- 6. REVISION HISTORY

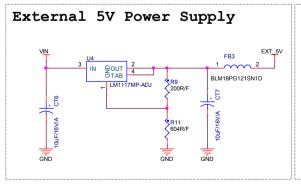
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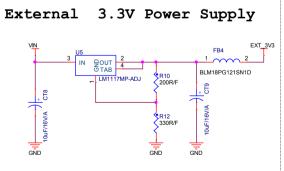


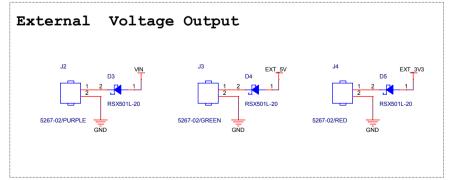


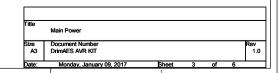




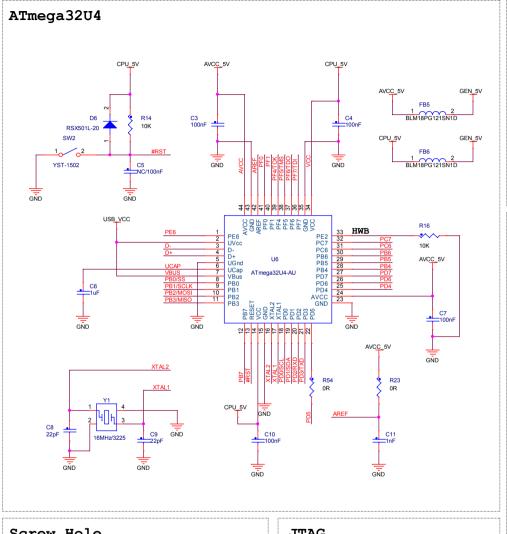


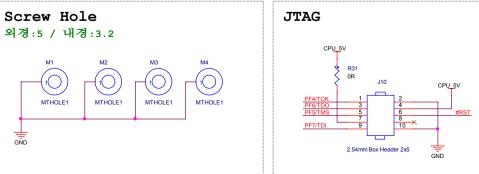


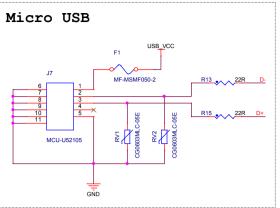


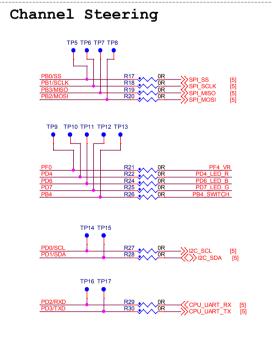


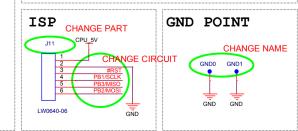
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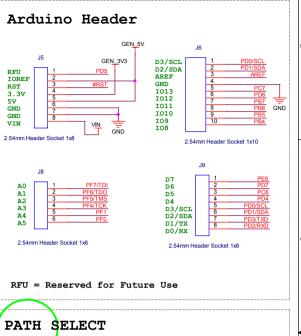


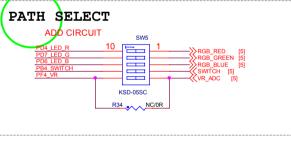


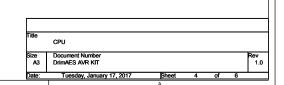




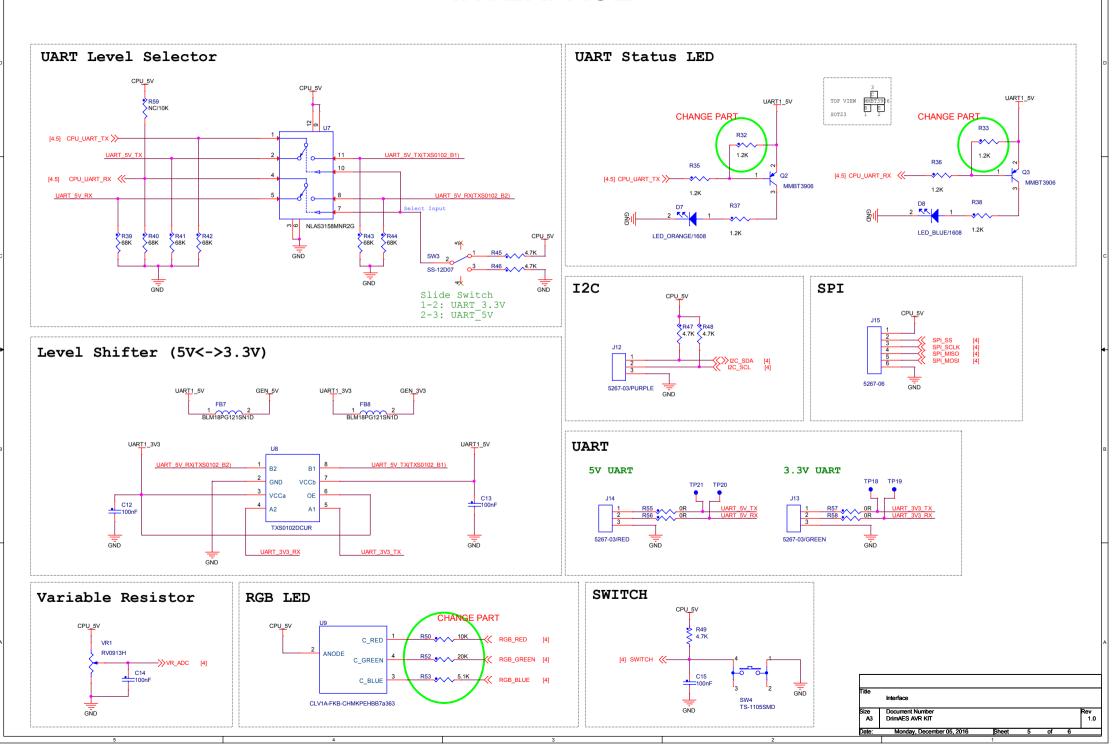








INTERFACE



Revision History

16.12.01	FUSE 추가. F1
16.12.01	ISP Connector 변경. J11
16.12.01	ISP Connector 핀배열 수정. J11
16.12.01	Select Interface Circuit 추가. SW5
16.12.01	UART Status LED 저항 변경. R32,R33 10K>1.2K
16.12.01	RGB LED 저항 변경 R50 1K>10K R52 1K>20K R53 1K>5.1K

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