78L09U

3-Terminal Positive Voltage Regulator



1.OUT 2.GND 3.IN SOT-89 Plastic Package

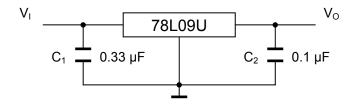
Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit	
Input Voltage	Vı	35	V	
Power Dissipation	P _{tot}	800 ¹⁾	mW	
Operating Temperature	T_{opr}	- 20 to + 120	°C	
Storage Temperature Range	T _{stg}	- 55 to + 150	°C	

 $^{^{1)}}$ 15 mm X 25 mm X 0.7 mm alumina ceramic board, $T_a \le$ 25 °C

Electrical Characteristics (T_a = 25 °C) (Unless otherwise specified, 0 °C ≤ T_J ≤ 125 °C, V_I = 15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F)

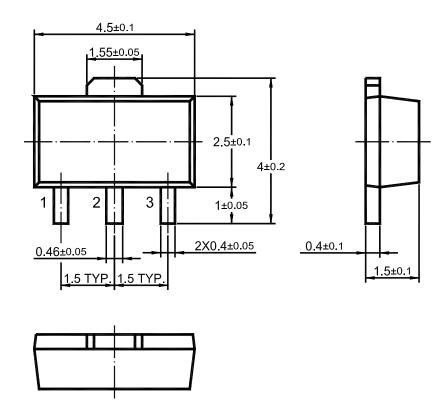
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output Voltage	_	T _j = 25 °C	8.64	9	9.36	V
		11.4 V ≤ V _I ≤ 24 V, 1 mA ≤ I _O ≤ 40 mA	8.55	ı	9.45	V
		$V_1 = 15 \text{ V}, 1 \text{ mA} \le I_0 \le 70 \text{ mA}$	8.55	ı	9.45	V
Line Regulation	Regline	11.4 V \leq V _I \leq 24 V, T _j = 25 °C	ı	ı	200	mV
		12 V \leq V _I \leq 24 V, T _j = 25 °C	ı	ı	160	
Load Regulation	Regload	1 mA \leq I _O \leq 100 mA, T _j = 25 °C	-	-	90	mV
		1 mA ≤ I _O ≤ 40 mA, T _j = 25 °C	-	-	45	
Quiescent Current	IQ	T _j = 25 °C	ı	-	6	mA
Quiescent Current Change	ΔI_Q	$12 \text{ V} \le \text{V}_1 \le 24 \text{ V}, \text{ I}_0 = 40 \text{ mA}$	-	-	1.5	mA
		V_1 = 15 V, 1 mA $\leq I_O \leq$ 40 mA	-	-	0.1	
Output Noise Voltage	V _N	10 Hz \leq f \leq 100 KHz, T _j = 25 $^{\circ}$ C	-	70	-	μV
Ripple Rejection	RR	$f = 120 \text{ Hz}, \ 12 \text{ V} \le \text{V}_{\text{I}} \le 24 \text{ V}, \ \text{T}_{\text{j}} = 25 \ ^{\circ}\text{C}$	38	_	-	dB
Dropout Voltage	V_{Drop}	T _j = 25 °C	-	1.7	-	V



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SOT-89 PACKAGE OUTLINE



Dimensions in mm

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