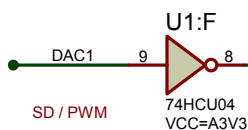


Sigma-Delta Modulator

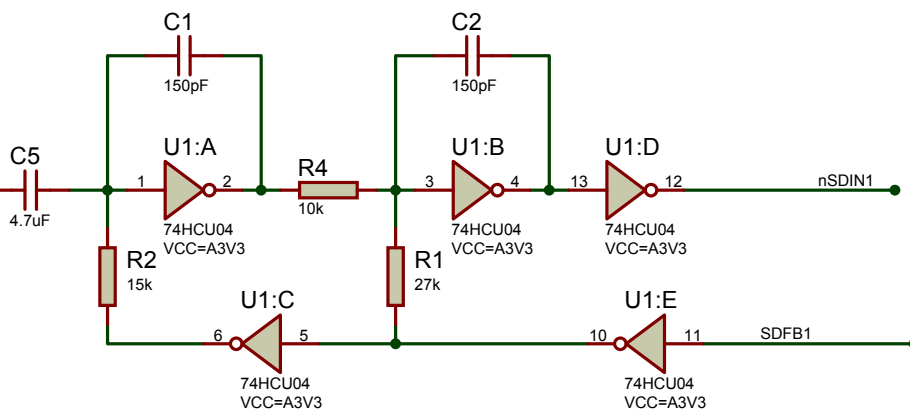
- 2nd Order, Continuous-Time
- Sampling clock: 2MHz
- Oversampling Ratio: 64 (2MHz/64 = 31250 S/s)
- 10.25 Effective bits (max SNR: 63dB)

Input buffer

- Removes supply noise from digital signals



DAC1
SD / PWM



Analog Filter

- 3rd Order, Low-pass, Butterworth response
- Max. output amplitude: 765 mV peak to peak
- 15 kHz Bandwidth
- Direct drive for 32ohm headphones

FILE NAME: **MULTI_AL4.pdsprj**

DESIGN TITLE: **Multimedia Shield**

SHEET: **Audio**

BY: J. Arias

REV: 1.0

DATE:

30/10/2025

PAGE:

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