

Jesse Both

Computer Engineer

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EDUCATION

BS in Computer Engineering, University at Buffalo

Expected graduation: May 2022

- 3.5 GPA

EXPERIENCE

CSE TA

I was a teaching assistant for the Computer Organization course. To TA for this class I had to have a good understanding of the cpu datapath along with MIPS assembly and system verilog.

PROJECTS

Calendar ([Read More](#))

Objective: As a way to stay organized when all of my classes moved online due to the pandemic, I designed a calendar to show me my schedule, running on a raspberry pi.

- Programmed in Scala to parse the calendar and weather and displays the information with HTML/CSS and Javascript

FlowFree Implementation ([Read More](#))

Objective: Final project for my Microprocessors course. This program used interrupts for key presses, the onboard button and one for the timer. ANSI characters were used to enhance the design.

- Programmed in ARM assembly and displayed on the PuTTY terminal via serial communication .

Tizen Development

Objective: Redesigned a watch face for my smartwatch using native watchface designer after previously designing watch faces with an app called WatchMaker.

- Programmed in C using the provided Tizen watch templates and gave me experience in being able to read and understand unknown code.

Programming Languages

- C/C++
- Scala
- Python
- Javascript
- ASM (ARM, MIPS, x86)
- MATLAB
- Verilog
- Lua

Development Software

- VS Code
- IntelliJ
- Eclipse
- Emacs
- Git

Operating Systems

- Windows
- Unix/Linux

Hardware

- Arduino
- Raspberry Pi
- Tiva C
- Nucleo
- 3D Printer

Software

- Fusion 360
- Excel
- AutoHotKey
- LaTeX
- Microsoft Office