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HW#10

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1. asm
              "REV %[x32],%[x32]"
              [x32] "=r" (x32)
2. #define Lower32Bits(x) ((int32_t *) &x)[0]
#define Upper32Bits(x) ((int32_{t}*) &x)[1]
Int64_t src, dst;
asm
       (
              "LSRS %[dstHi],%[srcHi],1 \n\t"
              "RRX %[dstLo],%[srcLo]"
                            "=r"
                                    (Lower32Bits(dst)),
              [dstLo]
       :
                            "=r"
                                    (Upper32Bits(dst))
              [dstHi]
                             "r"
                                    (Lower32Bits(src)),
              [srcLo]
                                    (Upper32Bits(src))
              [srcHi]
                            "r"
              "cc"
       )
4.b. asm
       (
              "UBFX %[res],%[src],%[lsb],%[width]
                                                         n\t'
                            "+r"
                                    (res)
              [res]
                            "r"
                                    (src)
              [src]
                            "i"
              [lsb]
                                    (lsb)
                            "i"
              [width]
                                    (width)
              "cc"
       )
return res;
c. asm
       (
              "SBFX %[res],%[src],%[lsb],%[width]
                                                         n\t'
                             "+r"
              [res]
                                    (res)
                             "r"
       :
              [src]
                                    (src)
                            "i"
              [lsb]
                                    (lsb)
              [width]
                            "i"
                                    (width)
              "cc"
return res;
```