



If all three inputs are 1,  
all paths from VCC are shut off.

If any input is 0,  
at least one VCC  
line goes high.

If any input is 0,  
GND is shut off  
by at least one  
of the N-type  
transistors.

If all three inputs are 1,  
the path to GND opens up.  
3-AND is TRUE, so 3-NAND  
is FALSE in this one case.