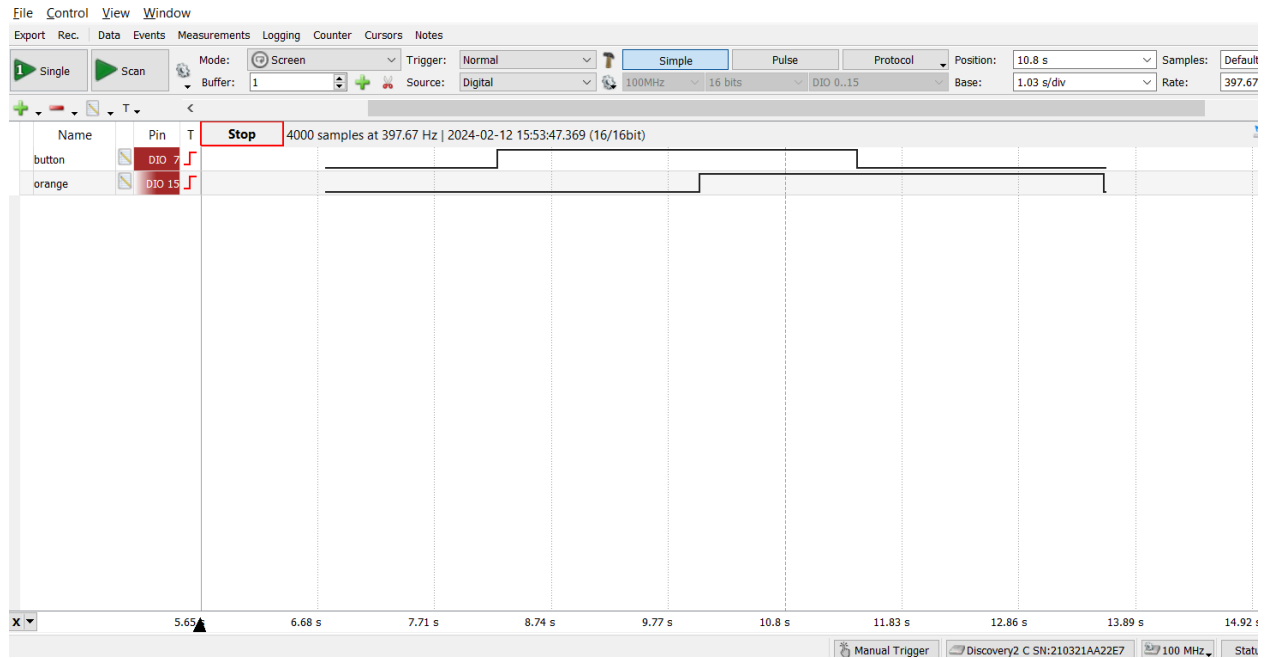


1. SYSCFG pin multiplexers group external pins by their orderings within the GPIO peripherals—for example, PA0, PB0 ... PF0 are on a single multiplexer with the output routed to the EXTI0 input.
2. 0, 3
3. four 8 bit regions totalling 32 bits. Uppermost 2 bits.
4. 1.75 seconds



5. The interrupt will repeat continuously because the request is never acknowledged as complete.