1. Relevant registers and configuration options and Summarized configuration information
2. Time-base unit
3. The prescale factor allows the timer to pre-divide the input clock to a slower frequency
4. The value in the ARR register is the trigger point at which the timer resets and begins to count a new period.
5. The output compare mode modifies the output of a GPIO pin whenever the timer's counter matches the value stored in the CCRx register.
6. An analog voltage ranging between the low and high voltages of the digital signal.
7. Allowing a pin to connect directly to internal peripherals of the STM32F0.
8. The STM32F072xB datasheet and under Section 4 Pinouts and Pin Descriptions.