7	6	5	4	3	2	1	0
JTD	ISC2	–	JTRF	WDRF	BORF	EXTRF	PORF
R/W	R/W	R	R/W	R/W	R/W	R/W	R/W
0	0	0	0	0	0	0	0

Figure 1: Position of the ISC2 bit in the MCUCSR register.

In tabel 2...

7	6	5	4	3	2	1	0
JTD	ISC2	–	JTRF	WDRF	BORF	EXTRF	PORF
R/W	R/W	R	R/W	R/W	R/W	R/W	R/W
0	0	0	0	0	0	0	0

Figure 2: Het register.