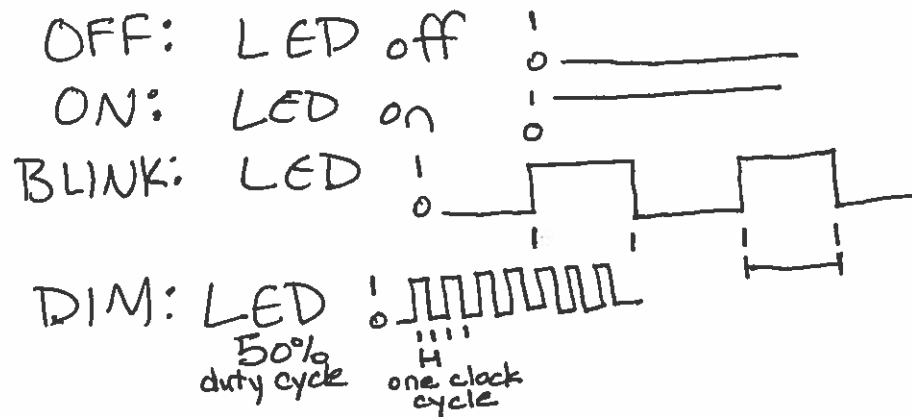


Specification Document

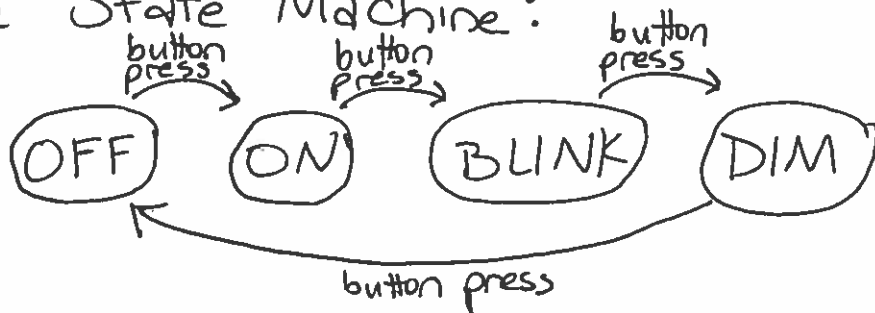
Inputs: Button

Outputs: LED

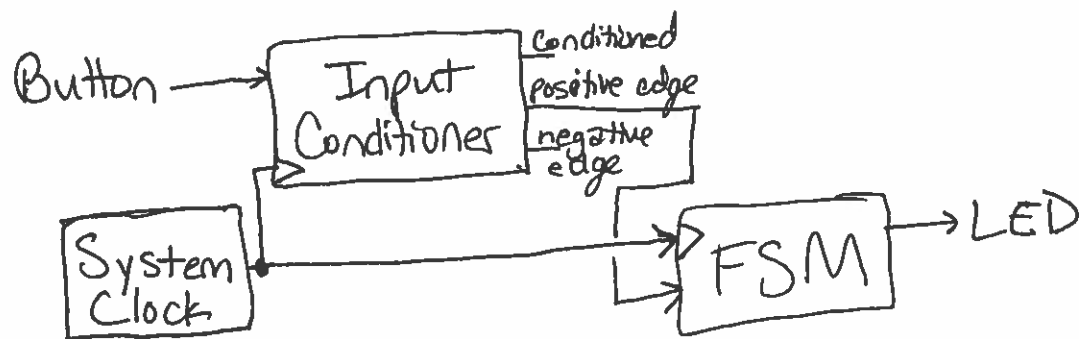
Operational Modes:



Finite State Machine:

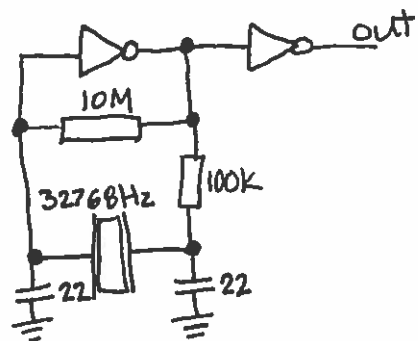


Block Diagram:



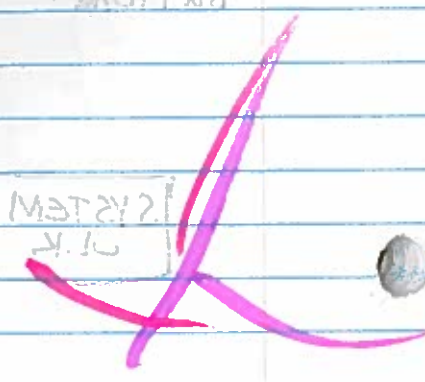
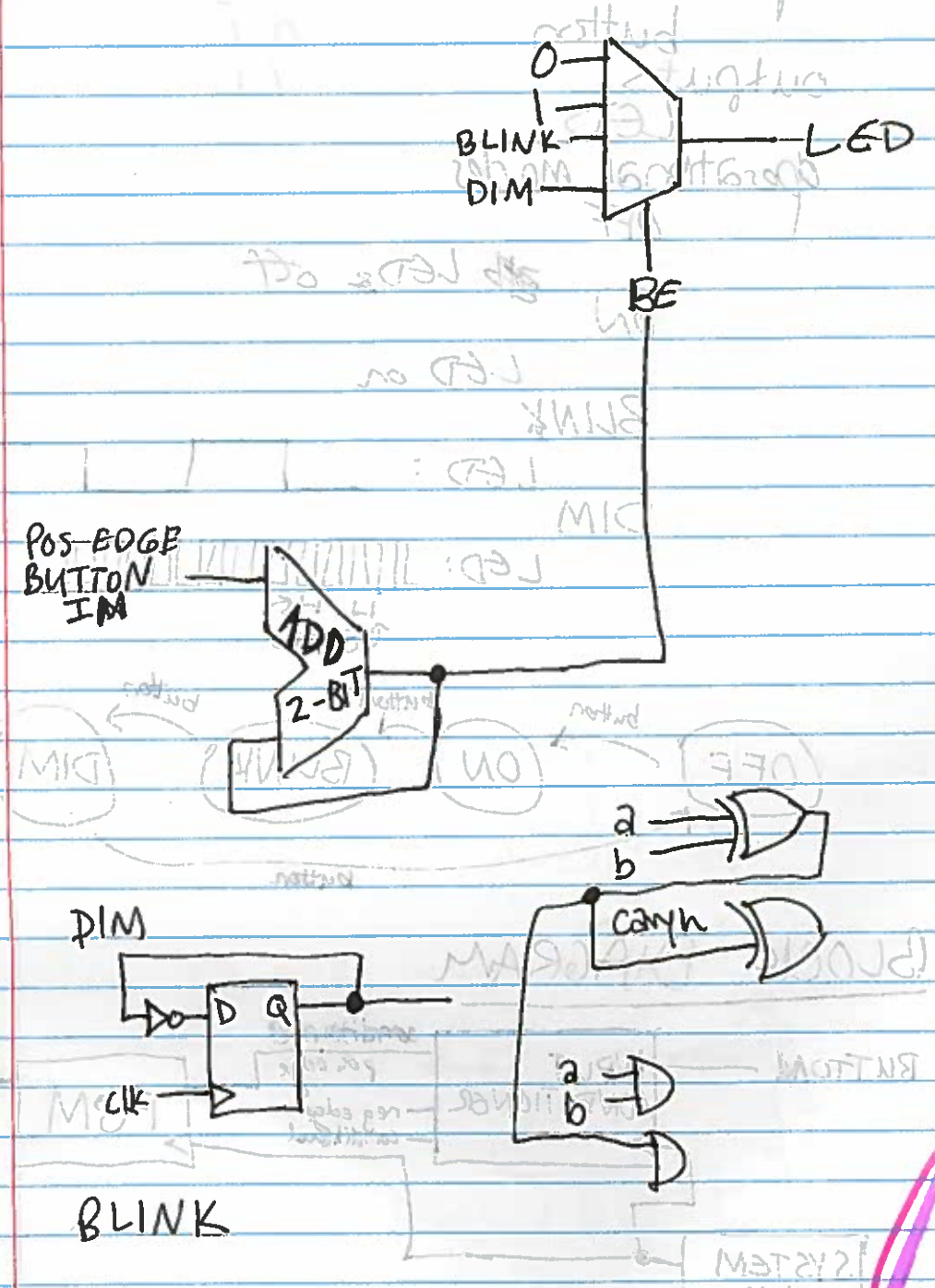
Schematics:

System Clock
Inputs: none
Outputs: 32,768 Hz square wave
Specification



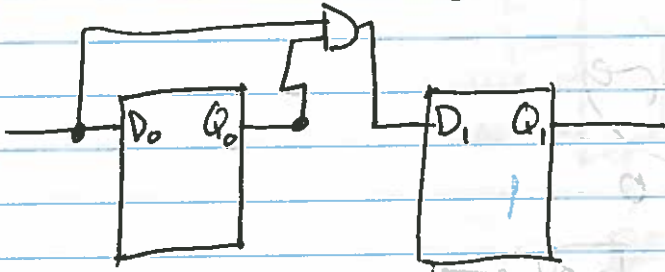
266C1710-92

FSM

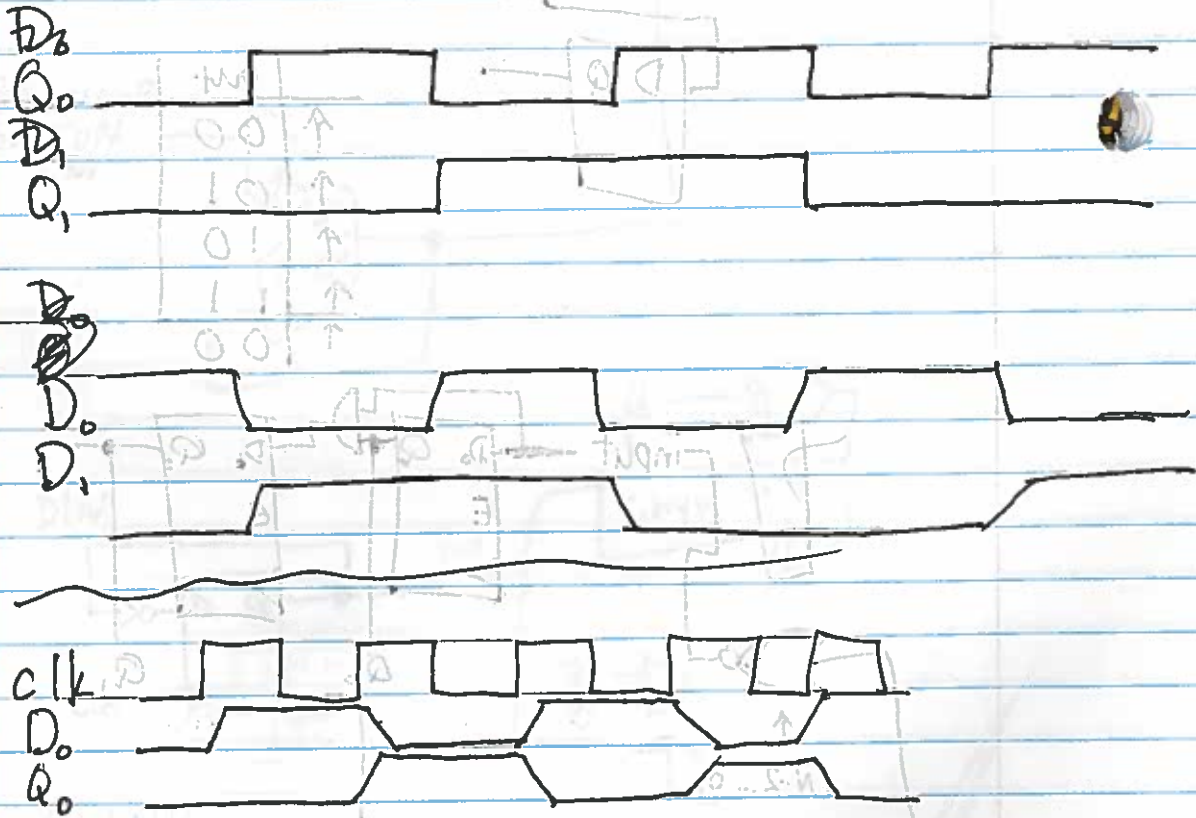


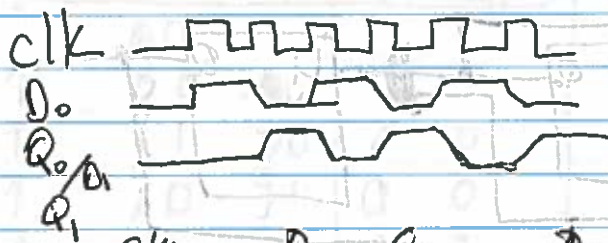
~~FSM~~ timing

not used



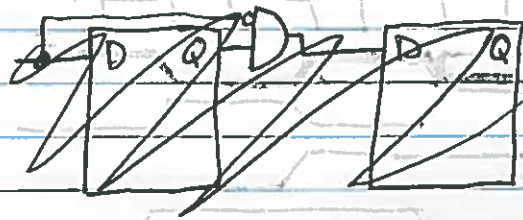
10:1-11(E)
1000

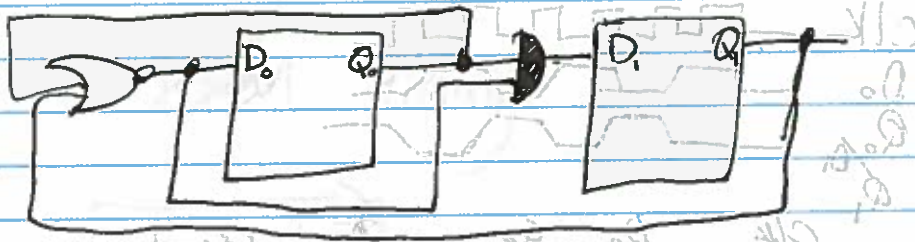




clk	D ₀	Q ₀	D ₁	Q ₁
↑	0	0	0	0
↑	1	0	0	0
↑	0	1	0	0
↑	1	0	1	0
↑	0	1	0	1
↑	1	0	0	1

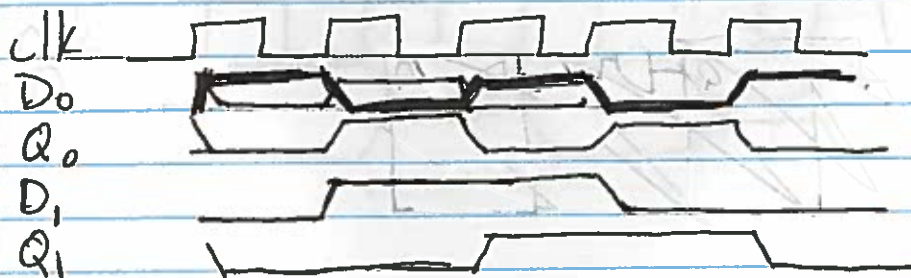
2

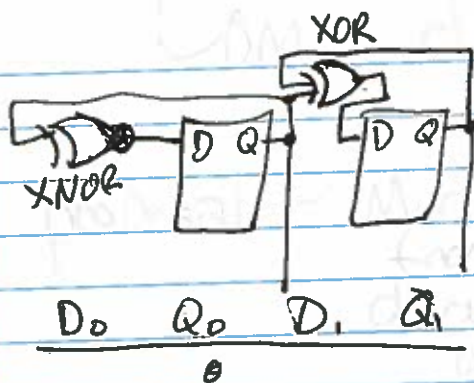




	D_0	Q_0	D_1	Q_1		D_0	Q_0	D_1	Q_1
↑	0	0	0		↑	0	1	↑	
↑	0	0	0	0		0	0	↑	
↑	1	0	0	0		1	0	↑	
↑	1	1	1	0		1	1	↑	
↑	0	1	0	1		0	1	↑	
↑	0	0	0	0		0	0	↑	
↑	1	0	0	0		1	0	↑	
↑	1	1	1	0		1	1	↑	

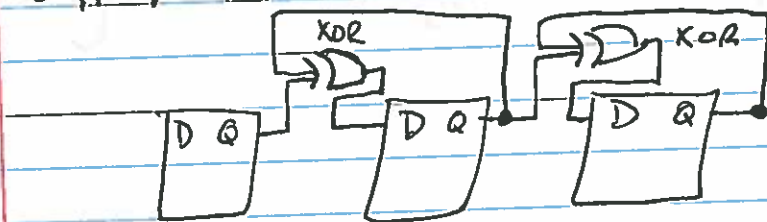
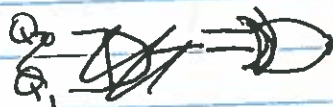
	D_0	Q_0	D_1	Q_1
0 0	1	0		0
0 0	0	1		0
1 0	1	0		1
0 0	0	1		1
0 0				
1 0				





D_0	Q_0	D_1	Q_1
1	0	0	0
0	1	1	0
1	0	1	1
0	1	0	1

D_1	Q_0	Q_1
0	0	0
1	1	0
1	0	1
0	1	1



D_0	Q_0	D_1	Q_1	D_2	Q_2
1	0	0	0	0	0
0	1	1	0	0	0
1	0	1	1	0	0
0	1	0	1	1	0
1	0	0	0	1	1
0	1	1	0	1	1
1	0	1	1	1	1
0	1	0	1	0	1