Instituto Tecnológico de Pabellón de Arteaga

Ingeniería en TIC'S

Materia:

ARQUITECTURA DE COMPUTADORAS

Unidad II.

Practica Linpack

Tabla

Alumno: Jonathan de Jesús García Rodríguez

	A	B	C	D	E	F	G
1				BRENCHMARK			
2	PRUEBA	SIZE	LDA	ALIGN	TIME	GFLOOPS	SUMAS TIEMPO
3	1	15,000	15,000	4	36.597	61.4922	36.597
4	2	15,000	15,000	4	39.114	57.5356	77.388
5		15,000	15,000	4	38.274	58.7992	
6	3	15,000	15,000	4	37.193	56.8246	117.554
7		15,000	15,000	4	40.052	56.1886	
8		15,000	15,000	4	40.309	55.8296	
9	4	15,000	15,000	4	38.748	58.0794	159.483
10		15,000	15,000	4	40.013	56.2425	
11		15,000	15,000	4	40.41	55.6898	
12		15,000	15,000	4	40.312	55.8261	
13	5	15,000	15,000	4	39.406	57.1095	202.193
14		15,000	15,000	4	40.724	55.2615	
15		15,000	15,000	4	40.363	55.7559	
16		15,000	15,000	4	40.49	55.5806	
17		15,000	15,000	4	41.21	54.6091	
18							
19							

Ilustración 1. Brenchmark.