VERIFICATION OF CONCURRENT AND DISTRIBUTED SYSTEMS

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ABOUT ME

- [Software Engineer] DataRobot
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POLL

How many of you heard of formal methods?

- I used one on of: Coq, Isable, TLA+, Alloy, Spin.
- I heard about it, but never used.
- I think formal methods are kinda cool.

PROBLEM STATEMENT.

MOTIVATIONAL EXAMPLE

CONCURRENT AND DISTRIBUTED SYSTEMS ARE HARD

Most of industry uses following techniques for quality assurance:

- Design review
- Code review
- Unit/Integration/Functional testing
- Static code analysis
- Code coverage
- Stress testing
- Fault-injection testing [1]

DISTRIBUTED ALGORITHMS EXTREMELY HARD

Chord: A Scalable Peer-to-peer Lookup Protocol for Internet Applications

Ion Stoica[†], Robert Morris[‡], David Liben-Nowell[‡], David R. Karger[‡], M. Frans Kaashoek[‡], Frank Dabek[‡],

Hari Balakrishnan[‡]

alturaciA fundamental problem that confronts peer-fo-peer applications is the
efficient location of the mode that stores a desired data item. This paer
deficient location of the mode that stores a desired data item. This paer
deficient location is the store of th

A Chord node requires information about $O(\log N)$ other nodes for efficient routing, but performance degrades gracefully when that information is out of date. This is important in practice because nodes will join and leave arbitrarily, and consistency of even $O(\log N)$ state may be hard to maintain. Only one piece of information per node need be correct in order for Chord to guarantee correct (though possibly sowl) routing of queries; Chord has a simple algorithm for maintaining this information in a dynamic environment.

Chord

is popular algorithm for P2P systems, paper published in 2001 by strong team of MIT researchers, 10 years later bug found in specification [11, 12]. Paper won best paper award.

SOMETIMES COST OF ERROR IS VERY HIGH 2



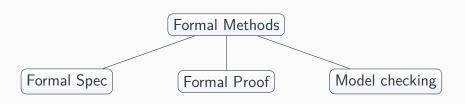
Therac-25

radiation therapy machine, because of concurrent programming errors, it sometimes gave its patients radiation doses that were hundreds of times greater than normal [2]

FORMAL METHODS

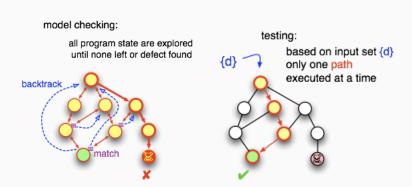
FORMAL METHODS

Formal Methods - are a particular kind of mathematically based techniques for the specification, development and verification of software systems [8].

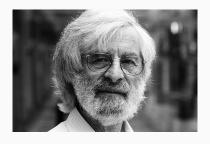


MODEL CHECKING

Model checking is a technique for automatically verifying correctness properties of finite-state systems.



TLA+ - TEMPORAL LOGIC OF ACTIONS



TLA+ language developed by **Leslie Lamport**. It is used to design, model, document, and verify concurrent systems, has been described as exhaustively-testable pseudocode and blueprints for software systems.

TLA+ DESCRIPTION

TLA+ is **specifications** language, it is precise written description of what a system suppose to do. **TLA Toolbox** distribution of TLA related tools. **TLC** - model checker tool to validate invariants stated in spec. **TLAPS** - mechanical proof checker.

Released April 23, 1999; 18 years ago

Syntax math notation, similar to LATEX

Implem. Java

License MIT

IDE TLA Toolbox (Eclipse based) [5]

TLA+ INDUSTRY USAGE: AWS



TLA+ helped to find design bugs in **S3**, **Dynamo**, **EBS**, **EC2**, etc, some requiring traces of 35 steps. [9]

TLA+ INDUSTRY USAGE: MICROSOFT

MS used TLA+ to define consistency protocol for CosmosDB and memory allocator for XBox [7]





TLA+ INDUSTRY USAGE: OPEN-SOURCE

Open source projects use TLA+ to verify algorithms:

- Linux Kernel verify fairness of qspinlock [6]
- Elastic data replication protocol [3]
- Mongodb data replication protocol [13]
- Hadoop/YARN registry of long lived processes [4]





TLA+ BASICS

TLA+ HELLO WORLDS

```
----- MODULE HourClock -----
EXTENDS Naturals
VARIABLE hr
HCini == hr \setminus in (1 .. 12)
HCnxt == hr' = IF hr # 12 THEN hr + 1 ELSE 1
HC == HCini /\ [][HCnxt] hr
THEOREM HC => []HCini
  1 MODULE HourClock —
 2 EXTENDS Naturals
 3 VARIABLE hr
 _{4} HCini \stackrel{\triangle}{=} hr \in (1...12)
  5 HCnxt \triangleq hr' = \text{if } hr \neq 12 \text{ Then } hr + 1 \text{ else } 1
  _{6} HC \stackrel{\triangle}{=} HCini \wedge \Box [HCnxt]_{hr}
  8 THEOREM HC \Rightarrow \Box HCini
```

TLA+ SYNTAX. LOGIC

Basic logical operators:

Symb.	ASCII	Python	Description
\vee	\/	or	logical OR
\wedge	/\	and	logical AND
\neg	~	not	logical NOT
=	=	==	boolean operator, checks equality, it is
			not an assignment operator.
\triangleq	==	=	means defined to equal.

EXERCISE

$$FALSE \land (TRUE \land (FALSE \lor TRUE) \lor (TRUE \lor FALSE)) = ??$$

EXERCISE

TLA+ SYNTAX. MORE LOGIC

More logic operators:

Symb.	ASCII	Python	Description	
3	\E	any()	means "there exists"	
\forall	\A	all()	means "for all"	
:	:		reads as "such that"	

 $\exists x \in 1, 2, 3, 4, 5 : x > 3$ - exists x in set of integers 1, 2, 3, 4, 5 such that x > 3 expression evaluates to TRUE.

TLA+ SYNTAX. MORE LOGIC

More logic operators:

Operator	ASCII	Description
	[]	formula is TRUE on each step
\Longrightarrow	=>	<pre>implication x_imp_y = y if x else True</pre>
,	1	reads as prime, state of variable on
		next step $x' = x + 1$

TLA+ SYNTAX. SETS

Basic set operations:

Symb.	ASCII	Python	Description
$S \cup T$	\union	s.union(t)	Union
$S \cap T$	\intersect	s.intersection(t)	Intersection
$S \subseteq T$	\supseteq	s in t	Membership
$S \setminus T$	\	s.difference(t)	Difference

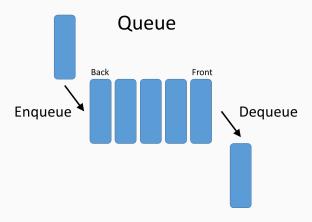
TLA+ SPEC TEMPLATE

```
----- MODULE ModuleName -----
    (* Imports and variable declarations*)
    EXTENDS Naturals, Sequences, Integers, FiniteSets
    (* Initial Conditions *)
    Init. == ...
    TypeOK == ...
    (* Body of the spec *)
    Next. == ...
10
    Invariant == ...
11
12
    (* Invariant declaration with temporal formula *)
13
14
    THEOREM Spec => ...
15
```

MULTITHREADING QUEUE SPEC

MULTITHREADING QUEUE

Classic *bounded buffer*, attempts to put an element into a full queue or take from empty will block.



QUEUE IMPLEMENTATION, PART 1

```
import threading
2
    class BoundedQueue:
3
        def __init__(self, capacity=3):
4
             self.capacity = capacity
5
             self.buffer = [None] * capacity
6
             self.mutex = threading.Lock()
8
             self.condition = threading.Condition(self.mutex)
9
            self.size = 0
            self.head = 0
10
            self.tail = 0
11
12
        def is_full(self):
13
             return self.size == self.capacity
14
15
        def is_empty(self):
16
             return self.size == 0
17
18
19
        def _next(self, x):
             return (x + 1) % self.capacity
20
```

QUEUE IMPLEMENTATION, PART 2

```
def put(self, item):
22
             with self.condition:
23
                 while self.is full():
24
                     self.condition.wait()
25
                 self.buffer[self.tail] = item
26
                 self.tail = self._next(self.tail)
27
                 self.size += 1
28
29
                 self.condition.notify()
30
31
         def get(self):
             with self.condition:
32
                 while self.is empty():
33
                     self.condition.wait()
34
                 item = self.buffer[self.head]
35
                 self.buffer[self.head] = None
36
                 self.head = self. next(self.head)
37
                 self.size -= 1
38
                 self.condition.notify()
39
                 return item
40
```

CAN YOU GUESS TYPE OF BUG?



```
----- MODULE buffer -----
   EXTENDS Naturals, Sequences
   CONSTANTS Producers,
            Consumers,
            BufCapacity,
            Data
   ASSUME /\ Producers # {}
10
         /\ Consumers # {}
         /\ Producers \intersect Consumers = {}
11
         /\ BufCapacity > 0
12
         /\ Data # {}
13
   VARIABLES buffer.
14
            waitSet
15
16
```

Imports and variables definitions.

```
16
    Participants == Producers \union Consumers
17
    RunningThreads == Participants \ waitSet
18
19
    TypeInv == /\ buffer \in Seq(Data)
20
              /\ Len(buffer) \in 0..BufCapacity
21
              /\ waitSet \subseteq Participants
22
23
24
    Notify == IF waitSet # {}
             25
             ELSE UNCHANGED waitSet
26
27
    NotifyAll == waitSet' = {}
28
29
    Wait(t) == waitSet | = waitSet \union {t}
30
31
```

Helper formulas.

```
Init == buffer = <<>> /\ waitSet = {}
32
    Put(t,m) == IF Len(buffer) < BufCapacity</pre>
33
                 THEN /\ buffer' = Append(buffer, m)
34
                      /\ Notify
35
                 ELSE /\ Wait(t)
36
                      /\ UNCHANGED buffer
37
    Get(t) == IF Len(buffer) > 0
38
               THEN /\ buffer' = Tail(buffer)
39
40
                   /\ Notify
               ELSE /\ Wait(t)
41
                    /\ UNCHANGED buffer
42
```

Initial conditions and main methods specification.

Invariant definition

QUEUE TRACE

TLC shows all steps that leads to invariant violation.

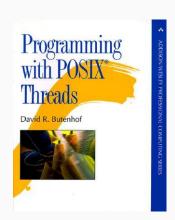
Name		Value
- ▶	waitSet	{"p1", "p2", "p3", "c3", "c4", "c5"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 26)</td></action>	State (num = 26)
	■ buffer	<<"m1", "m1">>
>	■ waitSet	{"p1", "p2", "p3", "c4", "c5"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 27)</td></action>	State (num = 27)
- ▶	■ buffer	<<"m1">>
- ▶	■ waitSet	{"p1", "p2", "p3", "c5"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 28)</td></action>	State (num = 28)
	■ buffer	<<>>>
-	■ waitSet	{"p1", "p2", "p3"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 29)</td></action>	State (num = 29)
	■ buffer	<< >>
>	■ waitSet	{"p1", "p2", "p3", "c1"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 30)</td></action>	State (num = 30)
	■ buffer	<< >>
>	■ waitSet	{"p1", "p2", "p3", "c1", "c2"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 31)</td></action>	State (num = 31)
	■ buffer	<< >>
-	■ waitSet	{"p1", "p2", "p3", "c1", "c2", "c3"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 32)</td></action>	State (num = 32)
	■ buffer	<< >>
-	■ waitSet	{"p1", "p2", "p3", "c1", "c2", "c3", "c4"}
₩ 🛦	<action 46,="" 47,="" 62="" 9="" b<="" col="" line="" module="" of="" td="" to=""><td>State (num = 33)</td></action>	State (num = 33)
	■ buffer	<< >>
>	■ waitSet	{"p1", "p2", "p3", "c1", "c2", "c3", "c4", "c5"}

Malina

THE BUG

- Condition variable shared between producers and consumer
- On producer signal other producer make wake up instead of consumer
- Bug is very hard to reproduce since 30 specific steps need to happen
- NotifyAll strategy fixes issue with deadlock.
- If number of threads > 2 * buffer capacity algorithm is not deadlock free.

NOTIFY_ALL() VS NOTIFY()



If ..., you share a condition variable between multiple predicates, you must always broadcast, never signal; Book: Programming with POSIX Threads by David R. Butenhof

AIORWLOCK SPEC

AIORWLOCK – READ WRITE LOCK FOR ASYNCIO

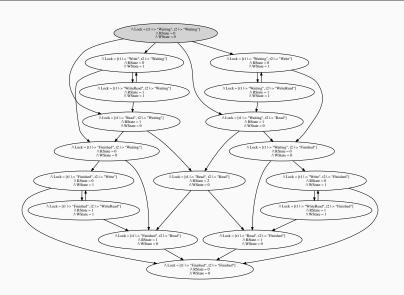
An RW lock allows concurrent access for read-only operations, while write operations require exclusive access, simple example:

```
import asyncio
    import aiorwlock
3
    async def go():
4
        rwlock = aiorwlock.RWLock()
5
         async with rwlock.writer:
             print("inside writer: only one writer is possible")
         async with rwlock.reader:
10
             print("inside reader: multiple reader possible")
11
12
    loop = asyncio.get event loop()
13
    loop.run_until_complete(go())
14
```

AIORWLOCK TRACE

Name	Value
▼ ▲ <initial predicate=""></initial>	State (num = 1)
▶ ■ Lock	[t1 -> "Waiting", t2 -> "Waiting", t3 -> "Waiting"]
State	0
Action line 30, col 15 to line 33, col 40 of mod	State (num = 2)
▼ ■ Lock	[t1 -> "Write", t2 -> "Waiting", t3 -> "Waiting"]
• t1	"Write"
• t2	"Waiting"
• t3	"Waiting"
■ State	-1
Action line 26, col 18 to line 28, col 38 of mod	State (num = 3)
▼ ■ Lock	[t1 -> "WriteRead", t2 -> "Waiting", t3 -> "Waiting"]
• t1	"WriteRead"
• t2	"Waiting"
• t3	"Waiting"
■ State	0
Action line 22, col 18 to line 25, col 43 of mod	State (num = 4)
▼ ■ Lock	[t1 -> "WriteRead", t2 -> "Read", t3 -> "Waiting"]
• t1	"WriteRead"
• t2	"Read"
• t3	"Waiting"
State	1

AIORWLOCK POSSIBLE STATES FOR 2 TASKS



AIORWLOCK SPEC RESULTS

- In fact bug reveled itself on specification phase, before running any models.
- Only three steps required to reproduce issue.
- First aio-libs project with formal verification!

CONCLUSIONS

LIMITATIONS OF MODEL-CHECKING

- State space explosion number of states reachable by a system can quickly become huge, or even infinite
- Not a replacement for tests or other standard quality assurance methods
- Formal methods are not a panacea, but can increase confidence in a product's reliability if applied with care and skill

Questions?



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