

PRODUCT BRIEF

AMD SPARTAN™ ULTRASCALE+™ FPGA SCU35 EVALUATION KIT



together we advance_

OVERVIEW

The AMD Spartan™ UltraScale+™ FPGA SCU35 Evaluation Kit is an affordable development platform offering a wide range of sensor and peripheral interfacing options for diverse applications. It features the Spartan UltraScale+ SU35P device, optimized for I/O expansion and board management applications. The kit offers the flexibility to connect and test multi-sensor configurations, monitor power consumption, and leverage security features for robust protection.

The kit comes with a breadth of connectivity options, software tools, and example designs to accelerate development of applications across multiple markets. The SCU35 Evaluation Kit is highly accessible and out-of-the-box ready for developers of varying FPGA development experience.

HIGHLIGHTS

SPARTAN ULTRASCALE+ SU35P FPGA CAPABILITIES

- High I/O, Low Power
- State-of-the-Art Security Features
- 36K Logic Cells and 304 Max Total I/O
- 1.93 Mb Total On-Chip Memory

BREADTH OF BOARD-LEVEL CONNECTIVITY OPTIONS

- 2x MikroE Click Ports and 1x HSIO Connector
- Arduino Shield I/O Expansion Interface
- 4x Pmods and 2x Raspberry Pi HAT I/O Expansion Interfaces
- 1x MII 10/100 Ethernet Networking with RJ-45 Connector
- HyperRAM Memory (64 Mb)

DEVELOPMENT TOOLS & OUT-OF-THE-BOX READY

- AMD Vivado™ Design Suite and Vitis™ Unified Software Platform
- Example designs and tutorials to get started quickly without FPGA experience

KEY APPLICATIONS

INDUSTRIAL

- Factory Automation
- Robotics
- IIoT Gateway and Edge Applications
- Smart City, Smart Grid

MEDICAL

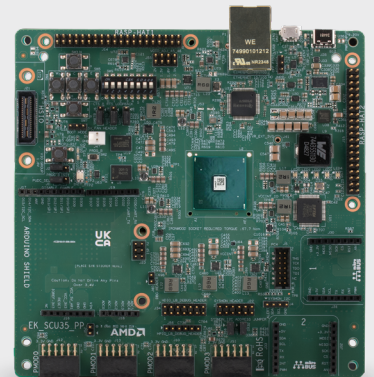
- Smart Patient Monitor
- Robot-Assisted Surgery

WIRED & WIRELESS

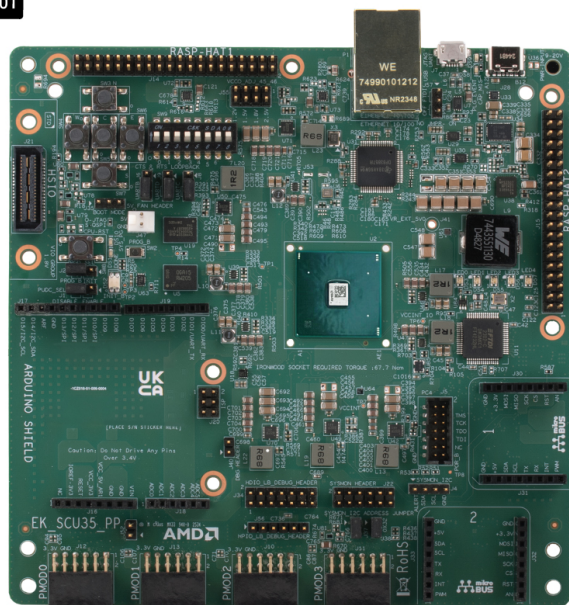
- 4G & 5G Wireless Infrastructure
- Board Controller
- Access Network and Connectivity

DATA CENTER

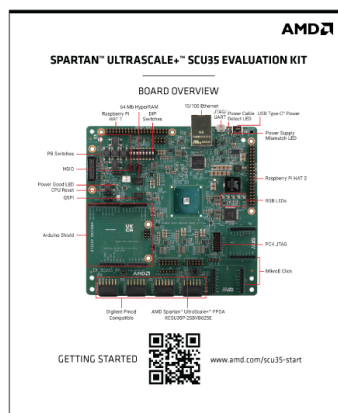
- Board Management Controller
- Hyperscale Storage



01



02



- 01 | AMD Spartan™ UltraScale+™ SCU35 Evaluation Kit
- 02 | Kit Flyer

NEXT STEPS

- For more information on the AMD Spartan UltraScale+ FPGA SCU35 Evaluation Kit, visit www.amd.com/scu35.
- For more information on AMD Spartan UltraScale+ FPGAs, visit www.amd.com/spartan-ultrascale-plus.

DISCLAIMERS

The information contained herein is for informational purposes only and is subject to change without notice. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for particular purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD products are as set forth in a signed agreement between the parties or in AMD's Standard Terms and Conditions of Sale. GD-18u.

COPYRIGHT NOTICE

© 2025 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, Spartan, UltraScale+, Vitis, Vivado, and combinations thereof are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective owners. Certain AMD technologies may require third-party enablement or activation. Supported features may vary by operating system. Please confirm with the system manufacturer for specific features. No technology or product can be completely secure. PID4067403